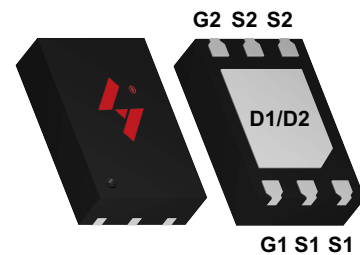


Dual N-Channel Enhancement Mode MOSFET

Features

- 20V/ 12A
 $R_{DS(ON)} = 9 \text{ m}\Omega \text{ (typ.) @ } V_{GS}=4.5\text{V}$
 $R_{DS(ON)} = 11.5 \text{ m}\Omega \text{ (typ.) @ } V_{GS}=2.5\text{V}$
- High Cell Desity for low Rds(on)
- ESD Rating:2000V HBM
- Halogen Device Available

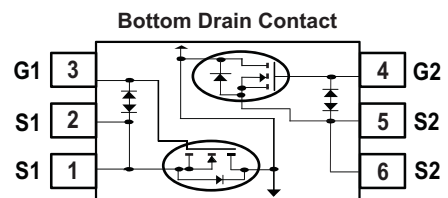
Pin Description



DFN6L(0203)

Applications

- Battery pack protection
- Power tool , Cell phone



Dual N-Channel MOSFET

Ordering and Marking Information

 C HY0C20 YYXXJWW C	Package Code C : DFN6L(0203) Date Code YYXX WW Assembly Material C : Green Device
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Note: HUAYI lead -free products contain molding compounds/die attach materials and 100% matte tin plate Termination finish;which are fully compliant with RoHS. HUAYI lead -free products meet or exceed the lead-Free requirements of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. HUAYI defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
Common Ratings ($T_C=25^{\circ}\text{C}$ Unless Otherwise Noted)			
V_{DSS}	Drain-Source Voltage	20	V
V_{GSS}	Gate-Source Voltage	± 10	
T_J	Maximum Junction Temperature	150	$^{\circ}\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^{\circ}\text{C}$
I_S	Diode Continuous Forward Current	$T_C=25^{\circ}\text{C}$ 12	A
Mounted on Large Heat Sink			
I_{DM}	Pulsed Drain Current *	$T_C=25^{\circ}\text{C}$ 48**	A
I_D	Continuous Drain Current	$T_C=25^{\circ}\text{C}$ 12	A
		$T_C=70^{\circ}\text{C}$ 9	
P_D	Maximum Power Dissipation	$T_C=25^{\circ}\text{C}$ 1.56	W
		$T_C=70^{\circ}\text{C}$ 1.00	
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	80	$^{\circ}\text{C/W}$

Note : * Repetitive rating ; pulse width limited by junction temperature

** Drain current is limited by junction temperature

Electrical Characteristics ($T_C = 25^{\circ}\text{C}$ Unless Otherwise Noted)

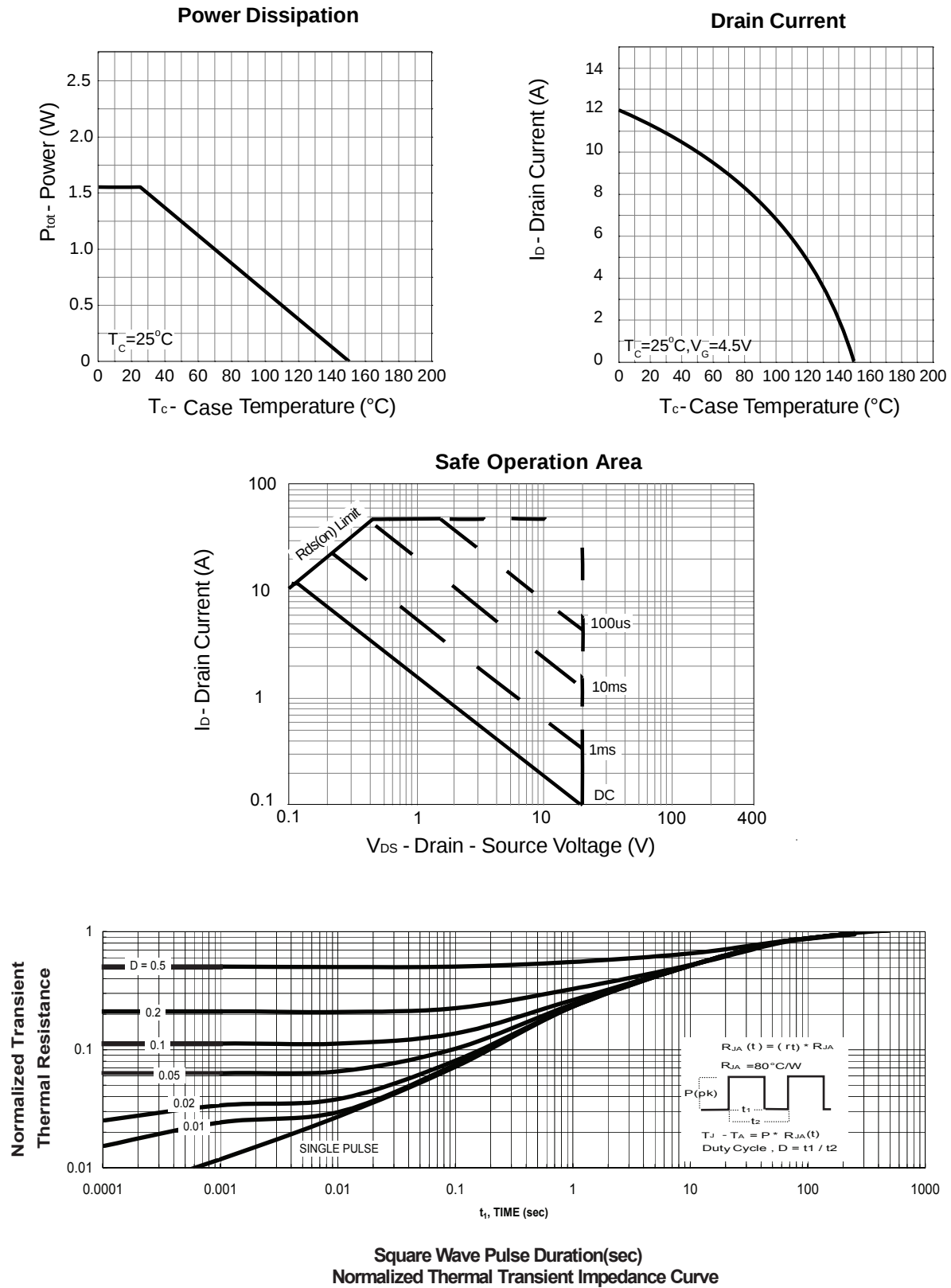
Symbol	Parameter	Test Conditions	HY0C20			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	20	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =16V, V _{GS} =0V	-	-	1	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	0.5	0.8	1.5	V
I _{GSS}	Gate Leakage Current	V _{GS} =±8 V, V _{DS} =0V	-	-	±10	uA
R _{DS(ON)} *	Drain-Source On-state Resistance	V _{GS} =4.5V, I _{DS} = 8 A	-	9	11	mΩ
		V _{GS} =2.5V, I _{DS} =5.5 A	-	11.5	14	mΩ
Diode Characteristics						
V _{SD} *	Diode Forward Voltage	I _{SD} =1 A, V _{GS} =0V	-	0.7	1.1	V

Electrical Characteristics (Cont.) ($T_c = 25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HY0C20			Unit
			Min.	Typ.	Max.	
Dynamic Characteristics						
C _{iSS}	Input Capacitance	V _{GS} =0V, V _{DS} =10V, Frequency=1.0MHz	-	670	-	pF
C _{oSS}	Output Capacitance		-	180	-	
C _{rSS}	Reverse Transfer Capacitance		-	140	-	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =10V, R _G =6 Ω I _{DS} =1A, V _{GS} =4.5V,	-	15	-	ns
T _r	Turn-on Rise Time		-	22	-	
t _{d(OFF)}	Turn-off Delay Time		-	46	-	
T _f	Turn-off Fall Time		-	21	-	
Gate Charge Characteristics						
Q _g	Total Gate Charge	V _{DS} =16V, V _{GS} =4.5V, I _{DS} =6 A	-	10	-	nC
Q _{gs}	Gate-Source Charge		-	1.6	-	
Q _{gd}	Gate-Drain Charge		-	4	-	

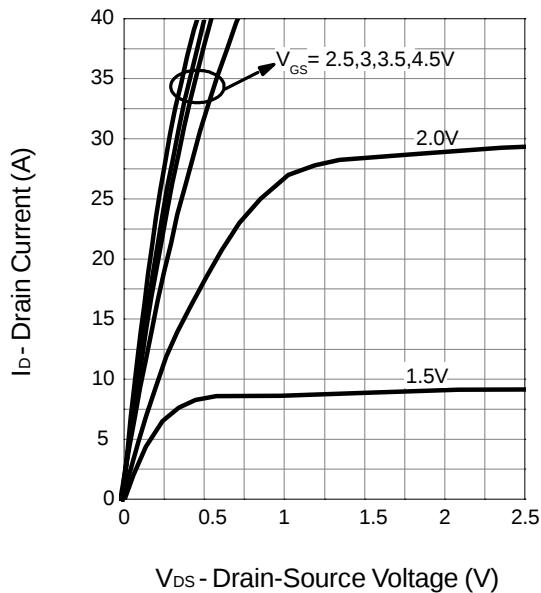
Note * : Pulse test ; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

Typical Operating Characteristics

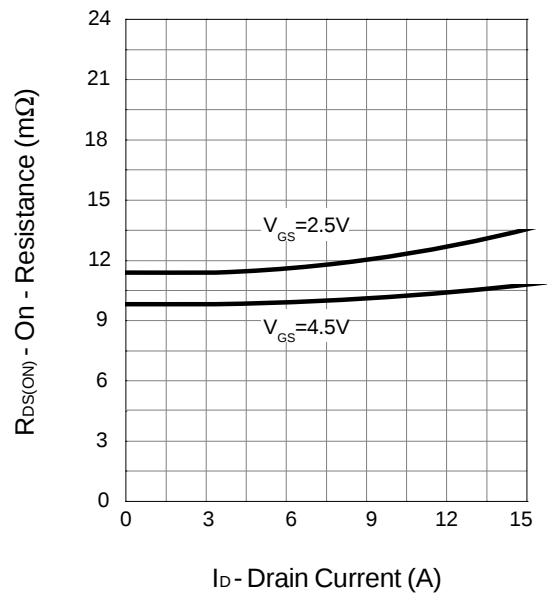


Typical Operating Characteristics (Cont.)

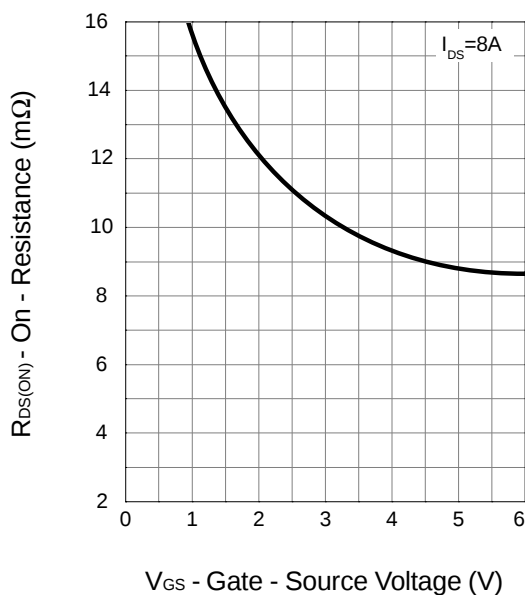
Output Characteristics



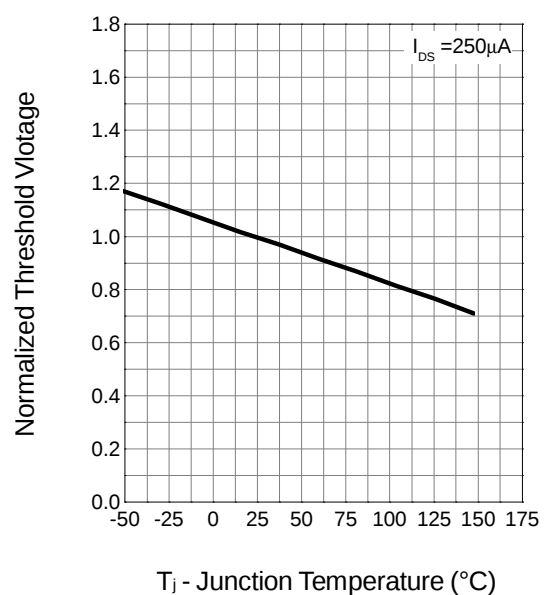
Drain-Source On Resistance



Drain-Source On Resistance

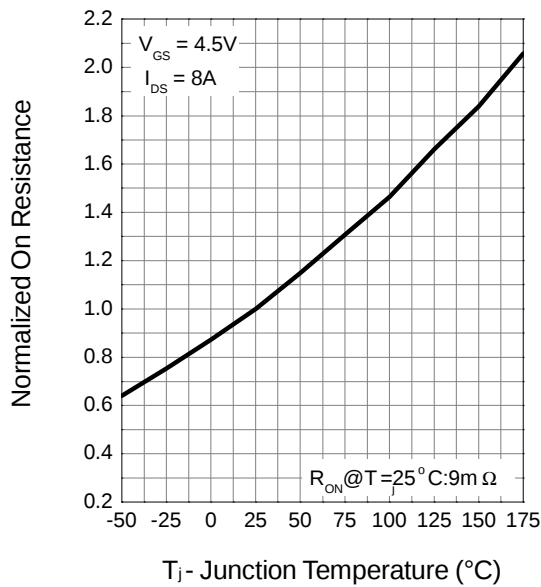


Gate Threshold Voltage

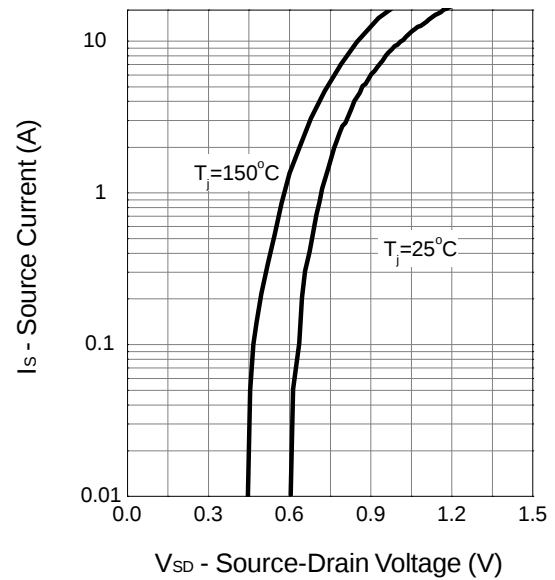


Typical Operating Characteristics (Cont.)

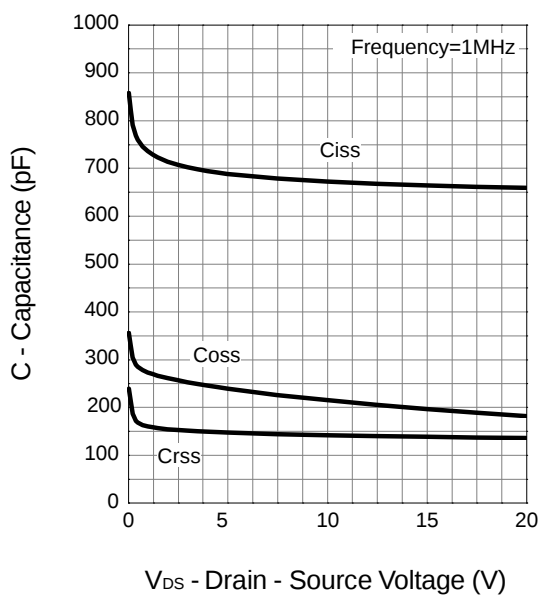
Drain-Source On Resistance



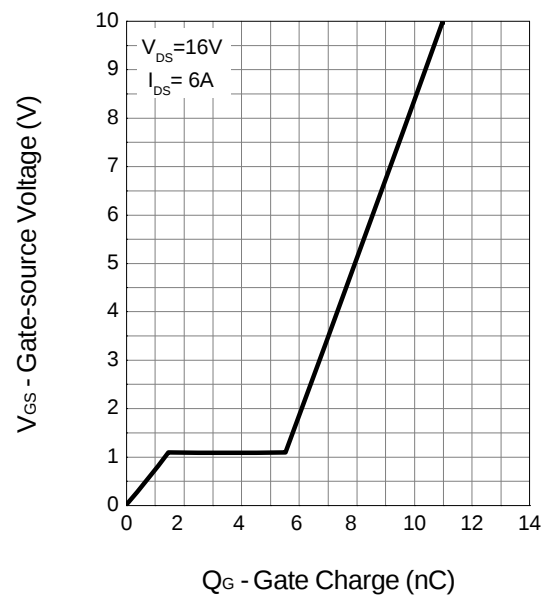
Source-Drain Diode Forward



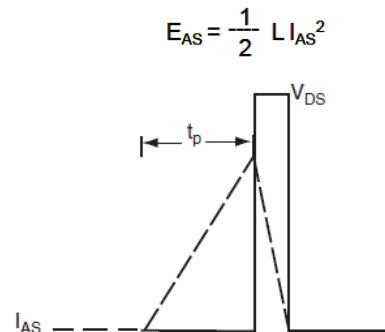
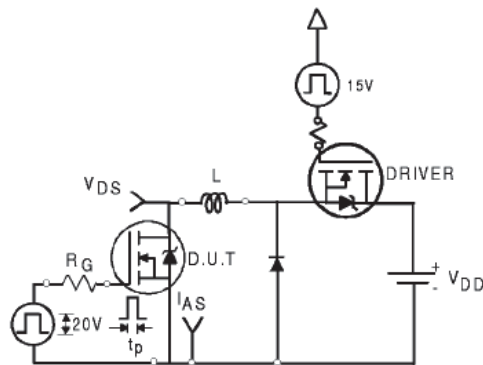
Capacitance



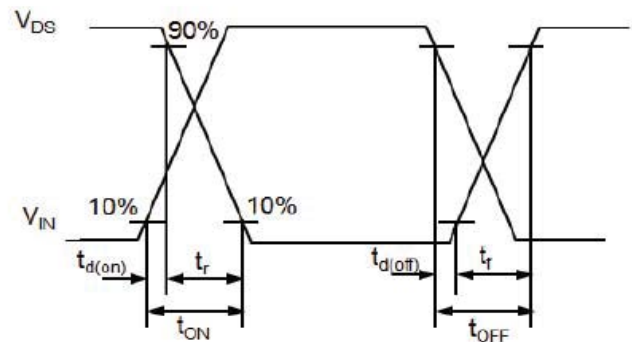
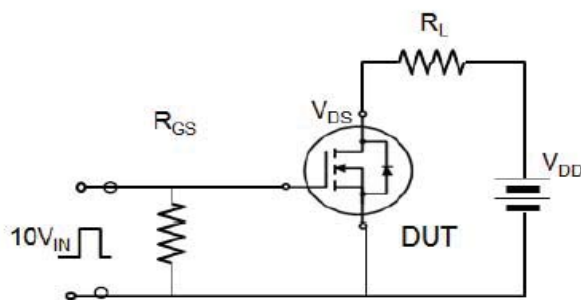
Gate Charge



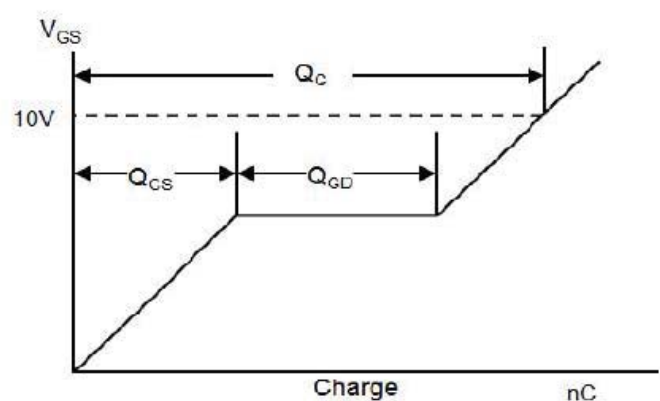
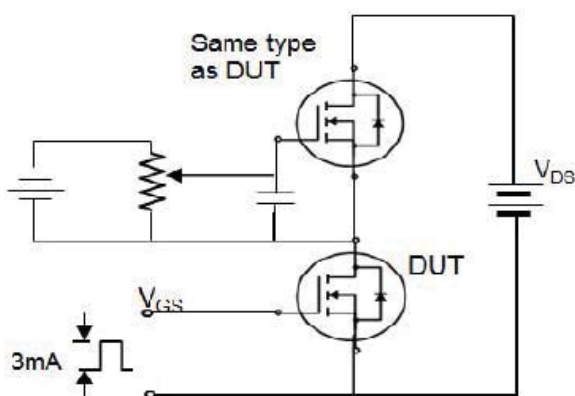
Avalanche Test Circuit



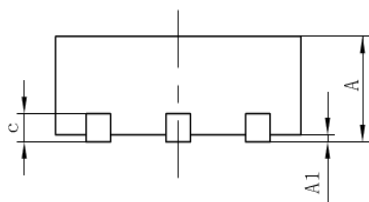
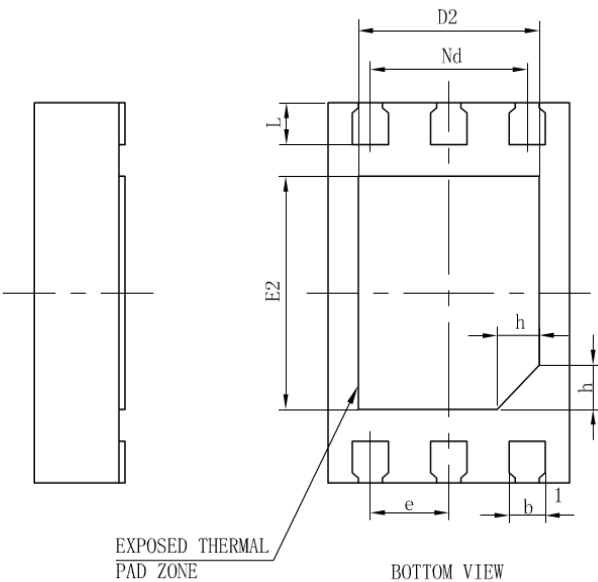
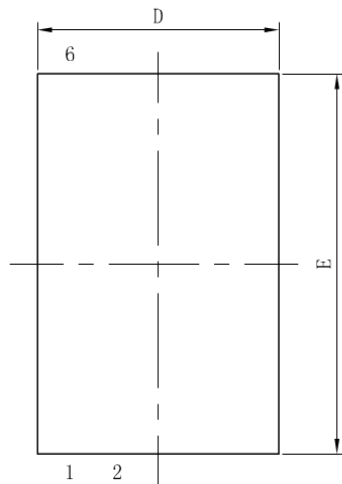
Switching Time Test Circuit



Gate Charge Test Circuit

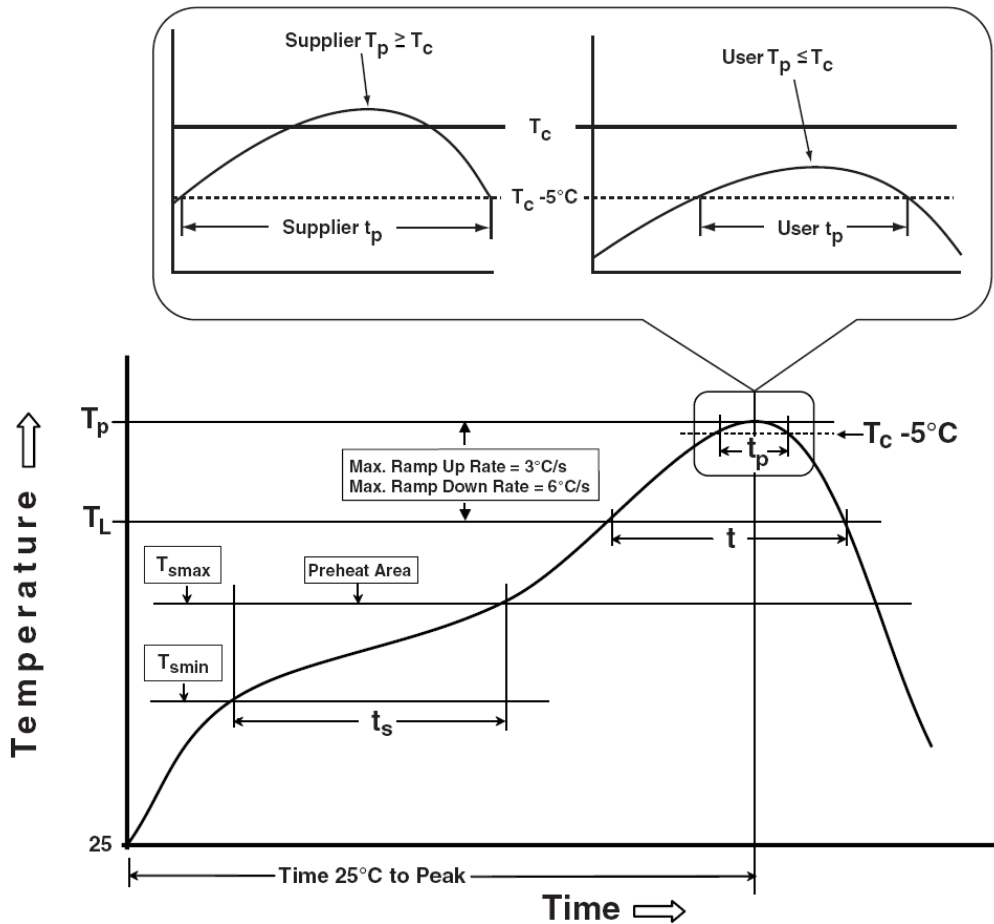


Package Information
DFN6L(0203*0.75-0.65)



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	—	0.02	0.05
b	0.25	0.30	0.35
c	0.18	0.20	0.25
D	1.90	2.00	2.10
D2	1.40	1.50	1.60
e	0.65BSC		
Nd	1.30BSC		
E	2.90	3.00	3.10
E2	1.74	1.84	1.94
L	0.28	0.33	0.38
h	0.30	0.35	0.40
载体尺寸 (mil)	67X76		

Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak		
Temperature min (T_{smin})	100 °C	150 °C
Temperature max (T_{smax})	150 °C	200 °C
Time (T_{smin} to T_{smax}) (t_s)	60-120 seconds	60-120 seconds
Average ramp-up rate (T_{smax} to T_p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L)	183 °C	217 °C
Time at liquidous (t_L)	60-150 seconds	60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum.		
** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (Tc)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168 Hrs /500 Hrs /1000 Hrs, Bias @ 150°C
PCT	JESD-22, A102	96Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -55°C~150°C

Customer Service

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