

ILLUMINANT 北極光企業有限公司

PRODUCT SPECIFICATION FOR TFT LCM

| | |
|---------------------|------------------------|
| CUSTOMER: | |
| MODEL NO: | I1513-6SEN1212A |
| ACCEPTED BY: | |

| APPROVED BY: | CHECKED BY: | ORGANIZED BY: |
|---|--|---|
|  <p>LCM產品部 2012.12.3 Zhang</p> |  <p>LCM產品部 2012.12.3 He</p> |  <p>LCM產品部 2012.12.3 Han</p> |

- Note**
- 1. Version of Specifications : 2.0**
 - 2. Others : ROHS Compliant**

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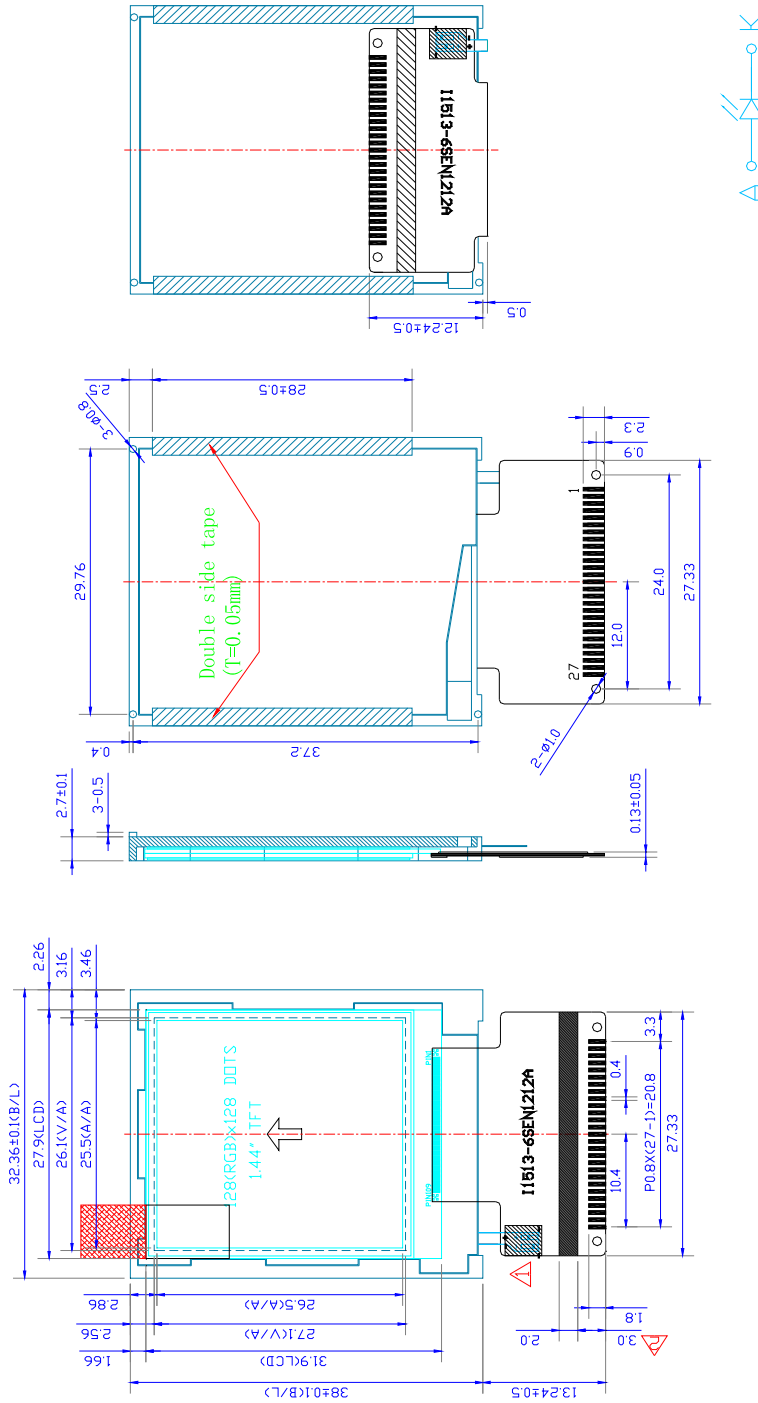
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1. Mechanical Specification:

| Item | Standard Value | Unit |
|-------------------|---------------------------|------|
| Display Size | 1.44 (Diagonal) | inch |
| Number of Dots | 128RGB*128 | Dots |
| Module Dimension | 32.36(W)*38.00(H)*2.70(D) | mm |
| Active Area | 25.4976(W)*26.496(H) | mm |
| Viewing Direction | 6H | - |
| Driver | ST7735S | |
| Interface | 8080 system 8bit | |
| Backlight Type | White LED *1 | - |
| Touch Panel | Without | |

| PIN | SYMBOL |
|-----|--------|
| 1 | VDD |
| 2 | VSS |
| 3 | NC |
| 4 | NC |
| 5 | /CS |
| 6 | D/C |
| 7 | RD |
| 8 | W/R |
| 9 | RESET |
| 10 | D0 |
| 11 | NC |
| 12 | D1 |
| 13 | NC |
| 14 | D2 |
| 15 | NC |
| 16 | D3 |
| 17 | NC |
| 18 | D4 |
| 19 | NC |
| 20 | D5 |
| 21 | NC |
| 22 | D6 |
| 23 | NC |
| 24 | D7 |
| 25 | NC |
| 26 | LEDK |
| 27 | LEDA |



| NOTE: ST7735S | | REVISED RECORD | |
|----------------------|----------------------|------------------|---|
| 1. DISPLAY TYPE | TFT/POSITIVE | A | FPC DIMENSION MODIFIED |
| 2. VIEWING DIRECTION | 6 O' CLOCK | B | DRIVE IC CHANGED (ST735R->ST7735S) |
| 3. POLARIZER MODE | TRANSMISSIVE | C | THE POSITION OF BLU FPC SOLDERING CHANGED FROM REAR TO FRONT |
| 4. BACKLIGHT TAPE | WHITE LED | D | THE SIZE BETWEEN DOUBLE-SIDE TAPE AND FPC PAD CHANGED FROM 2.5mm TO 3.0mm |
| 5. OPERATIVE VOLTAGE | 3.0v | E | |
| 6. OPERATIVE TEMP | -20° C~70° C | | |
| 7. STORAGE TEMP | -30° C~80° C | | |
| 8. CONNECTOR | SOLDER TYPE | | |
| Drawn | wwf | 2012.06.18 | |
| Check | | 2012.12.03 | |
| Approve | | 2012.12.03 | |
| Dwn.No | 11513-6SEN1212A_V2.0 | 2012.12.03 | |
| Page: 1 of 4 | Unit: mm | Date: 2012-12-03 | |
| Rev: 1.1 | Scale: 1/1 | Projection: | |

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2. Absolute Maximum Ratings:

| Item | Symbol | Min. | Typ. | Max. | Unit | Remark |
|--------------------------|--------------|------|------|--------------|------|--------|
| Supply Voltage for Logic | V_{DD-VSS} | -0.3 | | 4.0 | V | |
| | V_{DD2} | -0.3 | | 4.0 | | |
| Power Supply for LCD | V_{LCD} | -0.3 | | 19.8 | V | |
| Logic Input Voltage | V_{IN} | -0.4 | | $V_{DD}+0.5$ | V | - |
| Operating Temperature | T_{OP} | -20 | | +70 | °C | - |
| Storage Temperature | T_{ST} | -30 | | +80 | °C | |

3. Electrical Characteristics:

| Item | Symbol | Min. | Typ. | Max. | Unit | Note |
|-------------------------|-----------|-------------|------|-------------|------|------|
| Supply Voltage Logic | V_{DD} | 2.4 | 3.0 | 3.4 | V | |
| Input Voltage | H Level | $0.8V_{DD}$ | - | V_{DD} | V | |
| | L Level | V_{SS} | - | $0.2V_{DD}$ | V | |
| Backlight Input Voltage | V_{LED} | 3.0 | 3.2 | 3.3 | V | |
| Backlight Current | I_{BL} | - | 18 | - | mA | - |

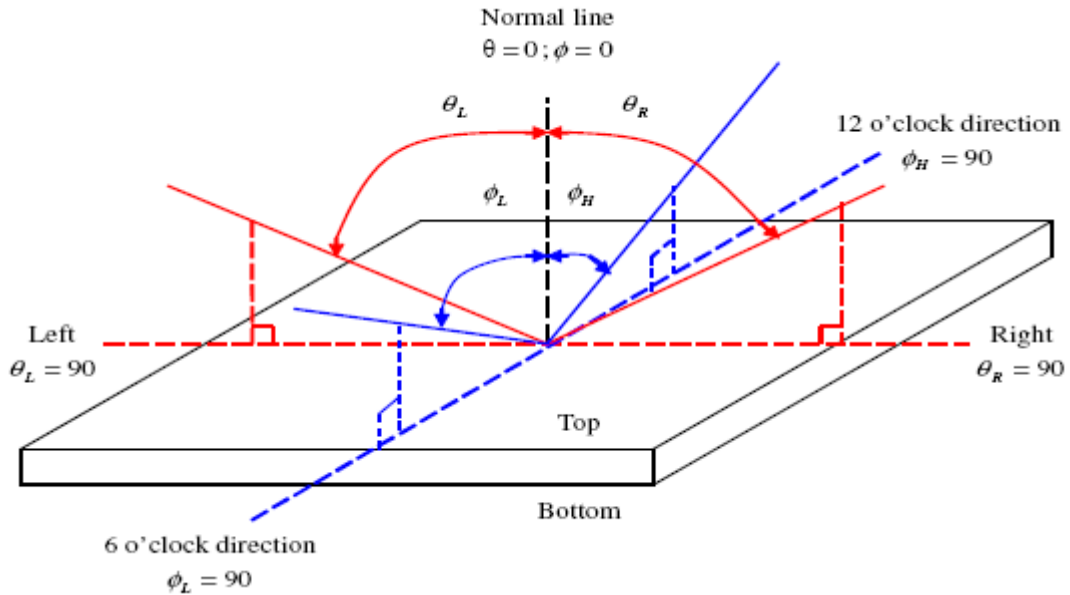
4. Optical Characteristics:

| Item | Symbol | Conditions | Specifications | | | Unit | Note |
|----------------------------|--------|-------------------------|----------------|------|------|-------------------|------|
| | | | Min. | Typ. | Max. | | |
| Contrast Ratio | CR | At optimized view angle | | 350 | - | - | |
| Luminous Intensity | IV | Center | | 100 | | Cd/m ² | |
| Response Time(T_R+T_F) | | $\theta=0$ | - | 25 | | ms | |
| Viewing Angle | Top | CR \geq 10 | - | 20 | | Deg. | |
| | Bottom | | - | 30 | | | |
| | Left | | - | 45 | | | |
| | Right | | - | 45 | | | |

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*Note

(1) Definition of Viewing Angle:

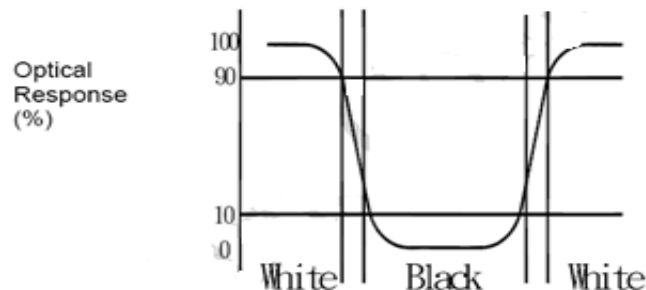


(2) Definition of Contrast Ratio

$$\text{Contrast Ratio (CR)} = \frac{\text{Brightness measured when LCD is at "white state"}}{\text{Brightness measured when LCD is at "black state"}}$$

(3) Definition of Response Time : Tr and Tf

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

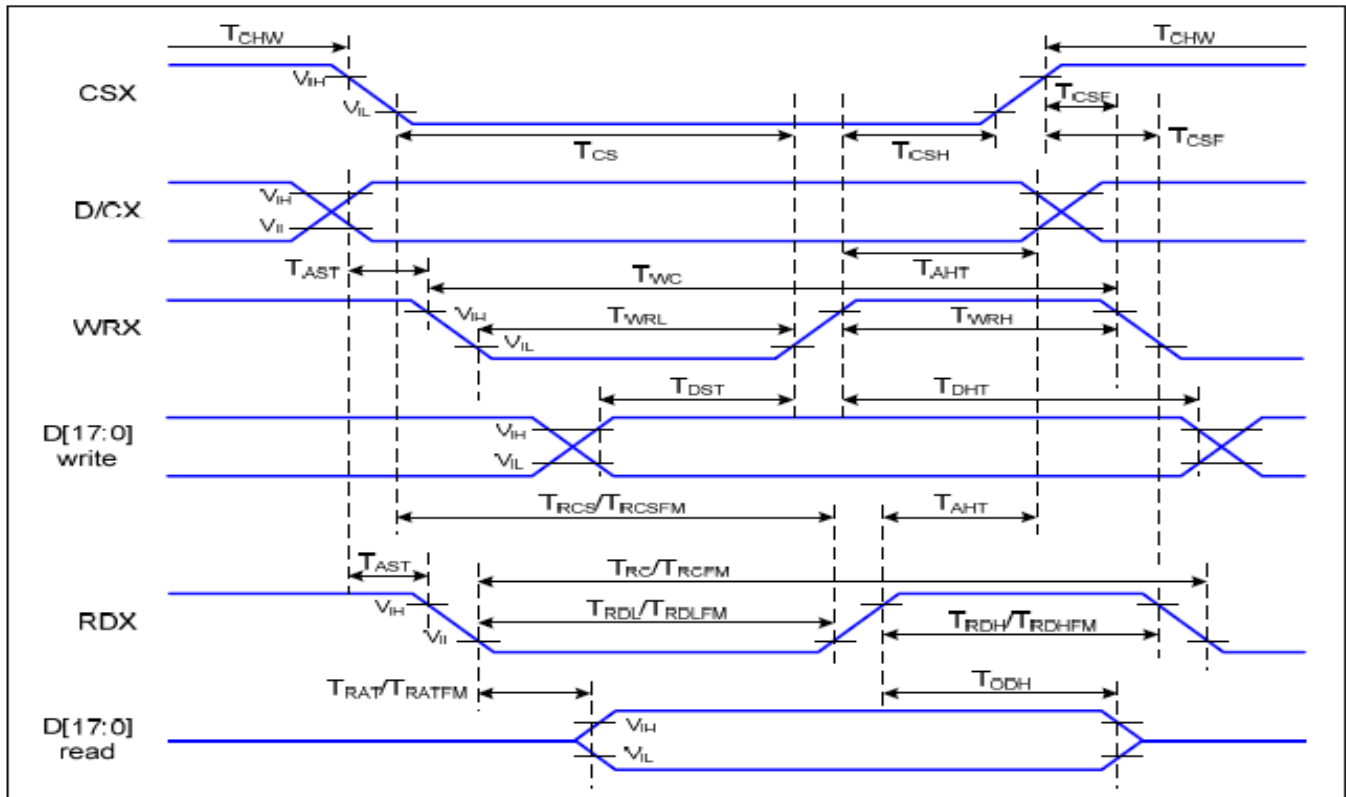


5. Interface:

| Pin No | Symbol | Description |
|--------|--------|--|
| 1 | VDD | Power Supply for Logic (2.8~3.3V) |
| 2 | VSS | System Ground |
| 3 | NC | No Connection |
| 4 | NC | No Connection |
| 5 | /CS | Chip Select Input Pins |
| 6 | D/C | D/C="L" :Control Data D/C="H" :Display Data |
| 7 | RD | Read Execution Control Pin |
| 8 | W/R | Write Execution Control Pin |
| 9 | /RESET | When /RESET is "L", initialization is executed |
| 10 | D0 | Data Bus |
| 11 | NC | No Connection |
| 12 | D1 | Data Bus |
| 13 | NC | No Connection |
| 14 | D2 | Data Bus |
| 15 | NC | No Connection |
| 16 | D3 | Data Bus |
| 17 | NC | No Connection |
| 18 | D4 | Data Bus |
| 19 | NC | No Connection |
| 20 | D5 | Data Bus |
| 21 | NC | No Connection |
| 22 | D6 | Data Bus |
| 23 | NC | No Connection |
| 24 | D7 | Data Bus |
| 25 | NC | No Connection |
| 26 | LEDK | LED Power Supply |
| 27 | LEDA | LED Power Supply |

6. Timing Control:

Parallel Interface Timing Characteristics (8080-series MCU Interface)



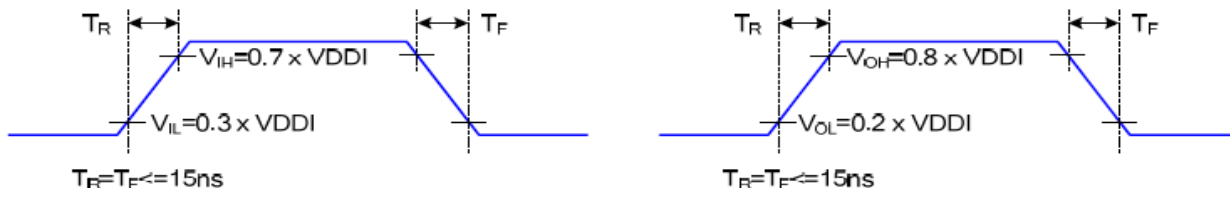
8080 Parallel Interface Characteristics

$T_a=25\text{ }^\circ\text{C}$, $V_{DDI}=1.65\sim 3.7\text{V}$, $V_{DD}=2.3\sim 4.8\text{V}$

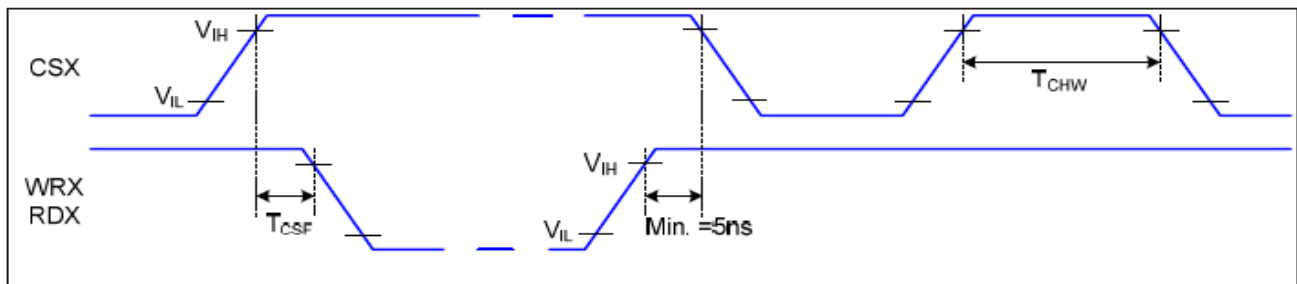
| Signal | Symbol | Parameter | Min | Max | Unit | Description |
|----------|--------|------------------------------------|-----|-----|------|-----------------------------|
| D/CX | TAST | Address setup time | 10 | | ns | - |
| | TAHT | Address hold time (Write/Read) | 10 | | ns | |
| CSX | TCHW | Chip select "H" pulse width | 0 | | ns | - |
| | TCS | Chip select setup time (Write) | 15 | | ns | |
| | TRCS | Chip select setup time (Read ID) | 45 | | ns | |
| | TRCSFM | Chip select setup time (Read FM) | 355 | | ns | |
| | TCSF | Chip select wait time (Write/Read) | 10 | | ns | |
| | TCSH | Chip select hold time | 10 | | ns | |
| | TWC | Write cycle | 66 | | ns | |
| WRX | TWRH | Control pulse "H" duration | 15 | | ns | - |
| | TWRL | Control pulse "L" duration | 15 | | ns | |
| | TRC | Read cycle (ID) | 160 | | ns | |
| RDX (ID) | TRDH | Control pulse "H" duration (ID) | 90 | | ns | When read ID data |
| | TRDL | Control pulse "L" duration (ID) | 45 | | ns | |
| | TRCFM | Read cycle (FM) | 450 | | ns | |
| RDX (FM) | TRDHFM | Control pulse "H" duration (FM) | 90 | | ns | When read from frame memory |
| | TRDLFM | Control pulse "L" duration (FM) | 355 | | ns | |
| | TDST | Data setup time | 10 | | ns | |
| D[17:0] | TDHT | Data hold time | 10 | | ns | For $CL=30\text{pF}$ |
| | TRAT | Read access time (ID) | | 40 | ns | |
| | TRATFM | Read access time (FM) | | 340 | ns | |
| | TODH | Output disable time | 20 | 80 | ns | |
| | | | | | | |

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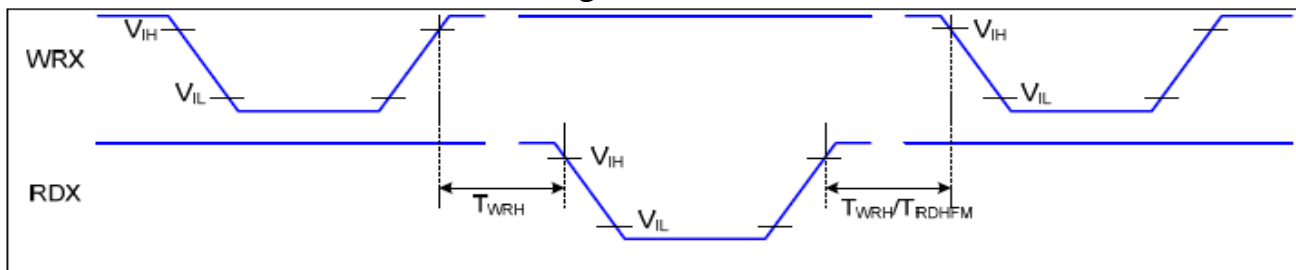
Rising and Falling Timing for Input and Output Signal



Chip Selection (CSX) Timing



Write to Read and Read to Write Timing



Note: The rising time and falling time (T_r , T_f) of input signal are specified at 15 ns or less. Logic high and low levels are specified as 30% and 70% of V_{DDI} for Input signals.

7. Environmental / Reliability Tests:

| No. | Test Item | Conditions | Judgment Criteria |
|-----|--|--|--|
| 1 | High Temperature Operating | +70°C 120hrs | Per table in below |
| 2 | Low Temperature Operating | -20°C 120hrs | Per table in below |
| 3 | High Temperature Non-Operating | +80°C 120hrs | Per table in below |
| 4 | Low Temperature Non-Operating | -30°C 120hrs | Per table in below |
| 5 | High Temperature / High Humidity Storage | 50°C ; 90%RH ; 120hrs | Per table in below (polarizer discoloration is excluded) |
| 6 | Thermal Shock (Non-Operating) | -30°C 30mins ~ +80°C 30mins Change Time:5mins, 10cycles | Per table in below |
| 7 | ESD (Operation) | C=150pF, R=330Ω, 5points/panel Air:±8KV, 5times; Contact:±4KV, 5times | Per table in below |
| 8 | Vibration (Non-Operating) | Frequency Range:10~55Hz, Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2hrs for each direction of X,Y,Z | Per table in below |
| 9 | Shock (Non-Operating) | 60G 6ms, ±X, ±Y, ±Z 3times, for each direction | Per table in below |
| 10 | Package Drop Test | Height:80cm, 1 corner, 3 edges, 6 surfaces | Per table in below |

| Inspection | Criterion(After Test) |
|------------------------|---|
| Appearance | No Crack on the FPC, on the LCD Panel |
| Alignment of LCD Panel | No Bubbles in the LCD Panel No other Defects of Alignment in Active Area |
| Electrical Current | Within Device Specifications |
| Function/Display | No Broken Circuit, No Short Circuit or No Black Line No Other Defects of Display |

8. Precautions for Use:

8.1 Safety

- (1) Do not swallow any liquid crystal, even if there is no proof that liquid crystal is poisonous.
- (2) If the LCD panel breaks, be careful not to get liquid crystal to touch your skin.
- (3) If skin is exposed to liquid crystal, wash the area thoroughly with alcohol or soap.

8.2 Storage Conditions

- (1) Store the panel or module in a dark place where the temperature is $23\pm 5^{\circ}\text{C}$ and the humidity is below $50\pm 20\% \text{RH}$.
- (2) Store in anti-static electricity container.
- (3) Store in clean environment, free from dust, active gas, and solvent.
- (4) Do not place the module near organics solvents or corrosive gases.
- (5) Do not crush, shake, or jolt the module.
- (6) Do not exposed to direct sun light of fluorescent lamps.

8.3 Installing LCD Module

Attend to the following items when installing the LCM.

- (1) Cover the surface with a transparent protective plate or touch panel to protect the polarizer and LC cell.
- (2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be $\pm 0.1\text{mm}$.

8.4 Precautions For Operation

- (1) Viewing angle varies with the change of liquid crystal driving voltage (V_0). Adjust V_0 to show the best contrast.
- (2) Driving the LCD in the voltage above the limit will shorten its lifetime.
- (3) Response time is greatly delayed at temperature below the operating temperature range. However, this does not mean the LCD will be out of the order. It will recover when it returns to the specified temperature range.
- (4) When turning the power on, input each signal after the positive/negative voltage becomes stable.
- (5) Do not apply water or any liquid on product which composed of T/P.

8.5 Handling Precautions

- (1) Avoid static electricity which can damage the CMOS LSI; please wear the wrist strap when handling.
- (2) The polarizing plate of the display is very fragile. so, please handle it very carefully.

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- (3) Do not give external shock.
- (4) Do not apply excessive force on the surface; it may cause display abnormal .
- (5) Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- (6) Do not use ketonics solvent & Aromatic solvent, use with a soft cloth soaked with a cleaning naphtha solvent.
- (7) Do not operate it above the absolute maximum rating.
- (8) Do not remove the panel or frame from the module.
- (9) Do not apply water or any liquid on product, which composed of T/P.

8.6 Warranty

- (1) The period is within 12 months since the date of shipping out under normal using and storage conditions.
- (2) The warranty will be avoided in case of defect induced by customer.