

DESCRIPTION

The ICPLW135, ICPLW136 and ICPLW4503 devices each consist of an infrared emitting diode, optically coupled to a high speed photo detector transistor. A separate connection for the photodiode bias and output-transistor collector increase the speed by several orders of magnitude over conventional phototransistor couplers by reducing the base-collector capacitance of the input transistor.

These devices belong to Isocom wide body package range optocouplers.

FEATURES

- High Speed 1Mbit/s
- Wide Body Package
- 15kV/µs min. Common Mode Transient Immunity (ICPLW4503)
- High AC Isolation Voltage 5000V_{RMS}
- Guaranteed Performance from 0°C to +70°C
- Pb Free and RoHS Compliant
- Safety Approvals Pending

APPLICATIONS

- Line Receivers
- Telecommunication Equipments
- Power Transistor Isolation in Motor Drives
- Replacement of Low Speed Phototransistor Optocouplers
- Feedback Loop in Switch Mode Power Supplies
- High Speed Logic Ground Isolation
- Home Appliances

ORDER INFORMATION

 Add SMT&R after PN for Surface Mount Tape & Reel

1. 2. 3. 4. 7. 6. 7. 6. 7. ICPLW135 ICPLW136 ICPLW4503 8.

- 1. NC
- 2. Anode
- 3. Cathode
- 4. NC
- 5. Gnd
- 6. Vout
- 7. V_B (ICPLW135/

ICPLW136)

NC (ICPLW4503)

8. V_{CC}

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$)

Input Diode

Forward Current 25mA
Peak Forward Current 50mA
(50% duty cycle, 1ms pulse width)

Peak Transient Current 1A (≤1µs pulse width, 300pps)

Reverse Voltage 5V Power dissipation 45mW

Output

Output Current 8mA
Peak Output Current 16mA
Emitter-Base Reverse Voltage* 5V
Base Current* 5mA

(*ICPLW135 and ICPLW136 only)

Output Voltage -0.5 to 20V Supply Voltage -0.5 to 30V Power Dissipation 100mW

Total Package

Isolation Voltage 5000V_{RMS} (R.H. 40 - 60%, 1 min,

Pins 1, 2, 3 & 4 shorted together, Pins 5, 6, 7 & 8 shorted together)

Operating Temperature -55 to +100 °C Storage Temperature -55 to +125 °C Lead Soldering Temperature (10s) 260 °C

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ELECTRICAL CHARACTERISTICS ($T_A = 0$ °C to +70°C unless otherwise specified)

INPUT

Parameter	Symbol	Test Condition	Min	Тур*	Max	Unit
Forward Voltage	$V_{\rm F}$	$I_F = 16\text{mA}$		1.45	1.8	V
Reverse Voltage	V_R	$I_R = 10 \mu A$	5.0			V
Temperature Coefficient of Forward Voltage	$\Delta V_F/\Delta T_A$	$I_F = 16mA$		-1.9		mV/°C

OUTPUT

Parameter	Symbol	Test Condition	Min	Тур.*	Max	Unit
Logic High Output Current	I_{OH}	$I_F = 0$ mA, $V_O = V_{CC} = 5.5$ V, $T_A = 25$ °C		0.001	0.5	μΑ
		$I_F = 0$ mA, $V_O = V_{CC} = 15V$, $T_A = 25$ °C		0.01	1	
		$I_F = 0 \text{mA}, V_O = V_{CC} = 15 \text{V}$			50	
Logic Low Supply Current	I_{CCL}	$I_F = 16\text{mA}, V_O = \text{Open},$ $V_{CC} = 15\text{V}$		110	200	μΑ
Logic High Supply Current	I_{CCH}	$I_F = 0$ mA, $V_O = O$ pen, $V_{CC} = 15$ V, $T_A = 25$ °C		0.01	1	μΑ
		$I_F = 0$ mA, $V_O = O$ pen, $V_{CC} = 15V$			2	

^{*} Typical values at $T_A = 25$ °C



ELECTRICAL CHARACTERISTICS ($T_A = -40^{\circ}$ C to +70°C unless otherwise specified)

COUPLED

Parameter	Symbol	Test Condition	Min	Тур.*	Max	Unit
Current Transfer Ratio	CTR	ICPLW135 ICPLW136 / ICPLW4503	7 19		50 50	%
		$I_F = 16\text{mA}, V_O = 0.4\text{V}$ $V_{CC} = 4.5\text{V}, T_A = 25^{\circ}\text{C}$				
		ICPLW135 ICPLW136 / ICPLW4503	5 15			
		$I_F = 16\text{mA}, V_O = 0.5\text{V}$ $V_{CC} = 4.5\text{V}$				
Logic Low Output Voltage	V_{OL}	ICPLW135		0.18	0.4	V
Output Voltage		$I_F = 16\text{mA}, \ I_O = 1.1\text{mA}, \ V_{CC} = 4.5\text{V}, \ T_A = 25^{\circ}\text{C}$				
		ICPLW136 / ICPLW4503		0.25	0.4	
		$I_F = 16\text{mA}, \ I_O = 3\text{mA}, \ V_{CC} = 4.5\text{V}, \ T_A = 25^{\circ}\text{C}$				
		ICPLW135			0.5	
		$I_F = 16\text{mA}, I_O = 0.8\text{mA},$ $V_{CC} = 4.5\text{V}$				
		ICPLW136 / ICPLW4503			0.5	
		$I_F = 16\text{mA}, I_O = 2.4\text{mA},$ $V_{CC} = 4.5\text{V}$				

^{*} Typical values at $T_A = 25$ °C



ELECTRICAL CHARACTERISTICS ($T_A = 0$ °C to +70°C unless otherwise specified)

Switching Characteristics ($T_A = 0$ °C to +70°C, $V_{CC} = 5V$, $I_F = 16$ mA unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Тур.*	Max	Unit	
Propagation Delay Time	T_{PHL}	ICPLW135				μs	
to Logic Low		$R_{L} = 4.1k\Omega, T_{A} = 25^{\circ}C$ $R_{L} = 4.1k\Omega$		0.36	1.5 2.0		
		ICPLW136 / ICPLW4503					
		$R_{L} = 1.9k\Omega, T_{A} = 25^{\circ}C$ $R_{L} = 1.9k\Omega$		0.32	0.8 1.0		
Propagation Delay Time	T_{PLH}	ICPLW135				μs	
to Logic High		$R_L = 4.1k\Omega, T_A = 25^{\circ}C$ $R_L = 4.1k\Omega$		0.45	1.5 2.0		
		ICPLW136 / ICPLW4503					
		$R_{L} = 1.9k\Omega, T_{A} = 25^{\circ}C$ $R_{L} = 1.9k\Omega$		0.25	0.8 1.0		
Common Mode	CM_H	ICPLW135	1000			V/µs	
Transient Immunity at Logic High		$I_F = 0 \text{mA}, V_{CM} = 10 \text{Vp-p},$ $R_L = 4.1 \text{k}\Omega, T_A = 25^{\circ}\text{C}$					
		ICPLW136	1000				
		$I_F = 0 \text{mA}, V_{CM} = 10 \text{Vp-p},$ $R_L = 1.9 \text{k}\Omega, T_A = 25 ^{\circ}\text{C}$					
		ICPLW4503	15000	20000			
		$I_F = 0 \text{mA}, V_{CM} = 1500 \text{Vp-p},$ $R_L = 1.9 \text{k}\Omega, T_A = 25^{\circ}\text{C}$					
Common Mode	CM_L	ICPLW135	1000			V/µs	
Transient Immunity at Logic Low	unity at	$I_F = 16\text{mA}, V_{CM} = 10\text{Vp-p},$ $R_L = 4.1\text{k}\Omega, T_A = 25^{\circ}\text{C}$					
		ICPLW136	1000				
		$I_F = 16\text{mA}, V_{CM} = 10\text{Vp-p},$ $R_L = 1.9\text{k}\Omega, T_A = 25^{\circ}\text{C}$					
		ICPLW4503	15000	20000			
		$I_F = 16\text{mA}, V_{CM} = 1500\text{Vp-p},$ $R_L = 1.9\text{k}\Omega, T_A = 25^{\circ}\text{C}$					



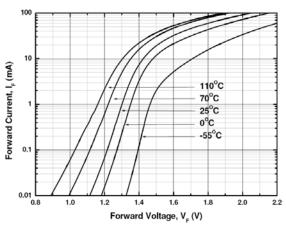


Fig 1 Forward Current vs Forward Voltage

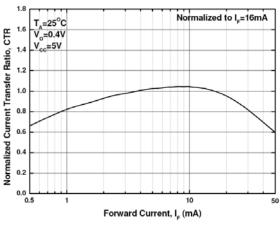


Fig 3 Normalized CTR vs Forward Current

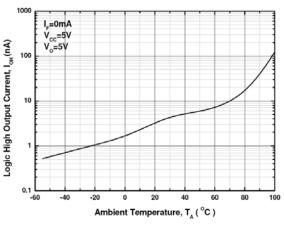


Fig 5 High Level Output Current vs TA

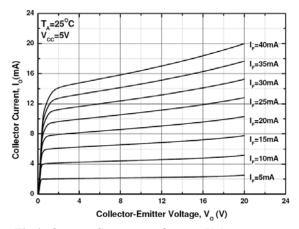


Fig 2 Output Current vs Output Voltage

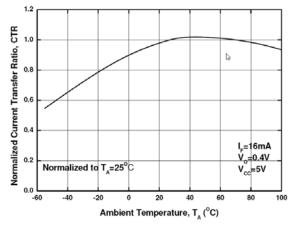


Fig 4 Normalized CTR vs T_A

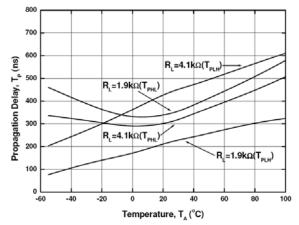


Fig 6 Propagation Delay vs T_A



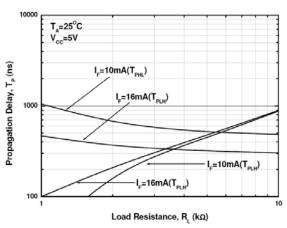
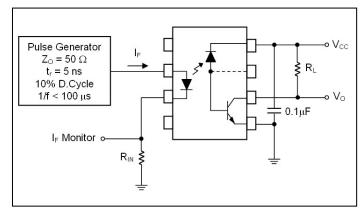


Fig 7 Propagation Delay vs Load Resistance



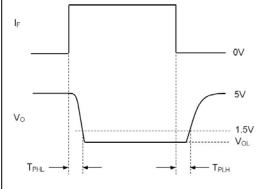
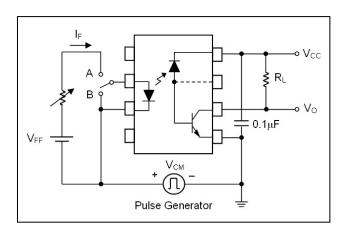


Fig 8 Switching Time Test Circuit





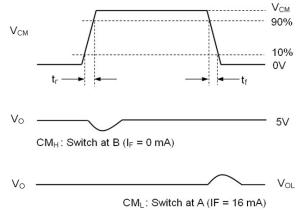


Fig 9 Common Mode Transient Immunity Test Circuit

 CM_H : Common mode transient immunity in logic high level is the maximum tolerable (positive) dV_{CM}/dt on the leading edge of the common mode pulse signal V_{CM} , to assure that the output will remain in a logic high state (i.e., $V_O > 2.0V$).

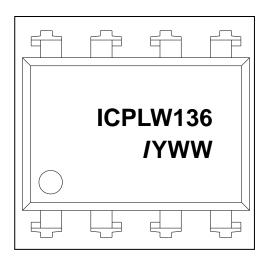
 CM_L : Common mode transient immunity in logic low level is the maximum tolerable (negative) dV_{CM}/dt on the trailing edge of the common mode pulse signal, V_{CM} , to assure that the output will remain in a logic low state (i.e., $V_O < 0.8V$).



ORDER INFORMATION

ICPLW135, ICPLW136, ICPLW4503						
After PN	PN	Description	Packing quantity			
None	ICPLW135, ICPLW136, ICPLW4503	Standard DIP	45 pcs per tube			
SM	ICPLW135SMT&R, ICPLW136SMT&R, ICPLW4503SMT&R	Surface Mount Tape and Reel	500 pcs per reel			

DEVICE MARKING



ICPLW136 denotes Device Part Number (ICPLW136 is used as example)

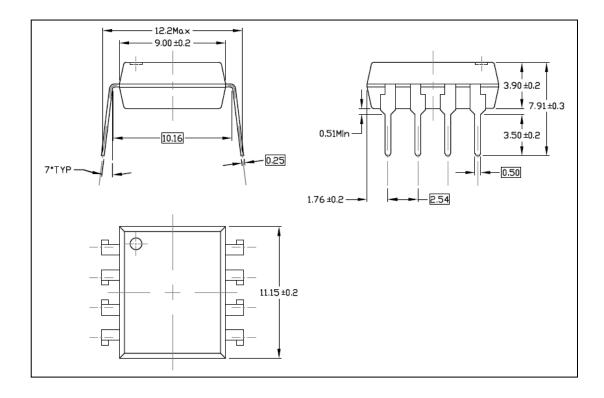
Y denotes 1 digit Year code WW denotes 2 digit Week code

I denotes Isocom

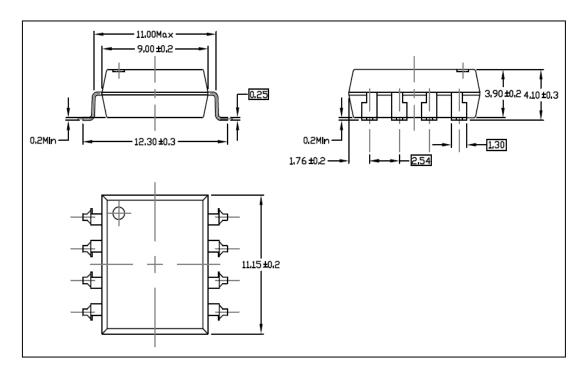


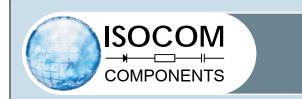
PACKAGE DIMENSIONS (mm)

DIP

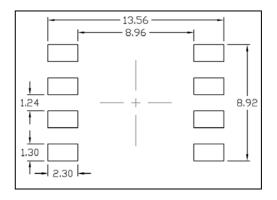


SMD



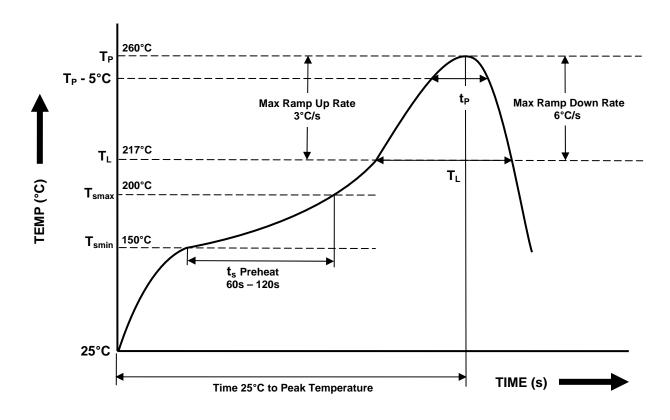


RECOMMENDED PAD LAYOUT FOR SMD (mm)





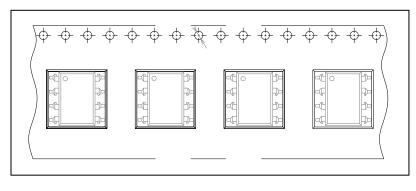
IR REFLOW SOLDERING TEMPERATURE PROFILE (One Time Reflow Soldering is Recommended)



Profile Details	Conditions
$ \begin{array}{l} \textbf{Preheat} \\ - \text{ Min Temperature } (T_{SMIN}) \\ - \text{ Max Temperature } (T_{SMAX}) \\ - \text{ Time } T_{SMIN} \text{ to } T_{SMAX} (t_s) \end{array} $	150°C 200°C 60s - 120s
$\begin{tabular}{ll} \textbf{Soldering Zone} \\ - & \begin{tabular}{ll} - & \begin{tabular}{ll} & \begin{tabular}{ll$	260°C 217°C 30s 60s - 100s 3°C/s max 6°C/s max
Average Ramp Up Rate (T _{smax} to T _P)	3°C/s max
Time 25°C to Peak Temperature	8 minutes max

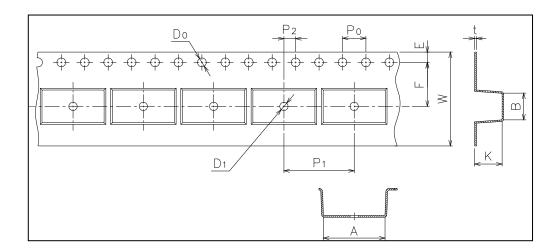


TAPE AND REEL PACKAGING





Direction of feed from reel

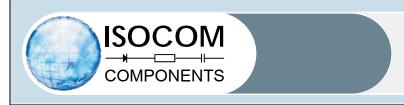


Dimension No.	Α	В	Do	D1	E	F
Dimension(mm)	12.7±0.1	11.45±0.1	1.5±0.1	1.5±0.1	1.75±0.1	11.5±0.1
Dimension No.	Ро	P1	P2	t	w	К
Dimension (mm)	4.0±0.1	16.0±0.1	2.0±0.1	0.4±0.05	24.00±0.3	4.6±0.1



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- The contents described herein are subject to change without prior notice.
- Do not immerse device body in solder paste.



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