Integrated Circuit Systems, Inc.

ICS9FG110
Advance Information

Programmable FTG for Differential PCI-Express

Recommended Application:

Frequency Timing Generator for Differential PCI-Express clocks

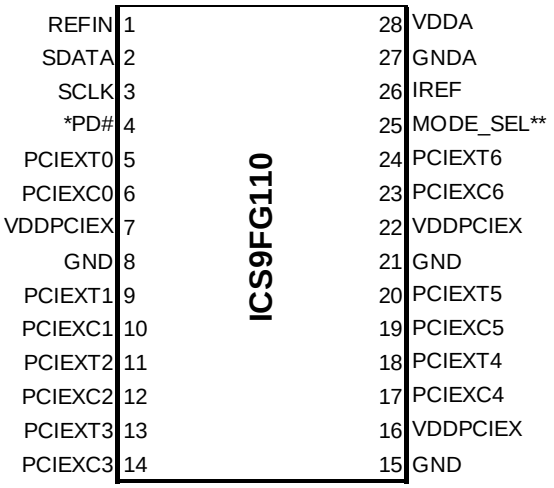
Features:

- Generates common PCI-Express frequencies from reference input clock (33.33MHz)
- 7 - 0.7V current-mode differential output pairs
- Three spread spectrum modes: spread tracking mode, 0 to -0.5 downspread and +/-0.25% centerspread
- Support for M/N programming
- Programmable spread percentage
- Programmable output divider
- SMBus address is DC/DD

Key Specifications:

- Output cycle-to-cycle jitter < 85 ps
- Output to output skew < 85 ps
- +/-300 ppm frequency accuracy on output clocks

Pin Configuration



28-SSOP/TSSOP

* This pin have 120K pull-up to VDD

** This pin have 120K pull-down to GND

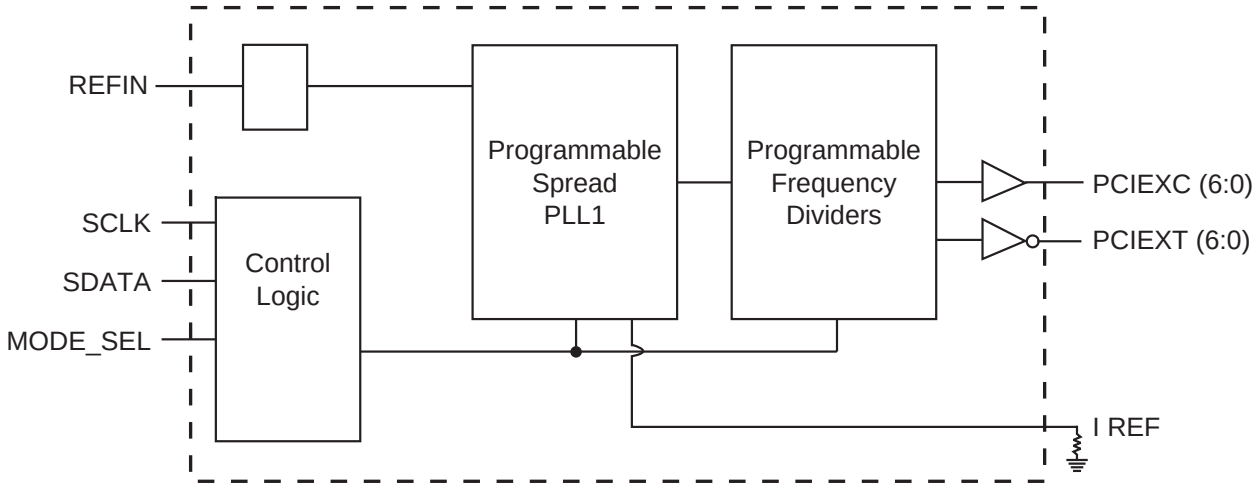
Functionality Table

MODE_SEL	PCIEX Freq	Spread Spectrum %
0	100	Spread Tracking Input
1	100	+/- 0.25 Center Spread

Power Groups

Pin Number		Description
VDD	GND	
28	27	Analog power & GND for CPU Core
7, 22	8, 15, 21	Power & GND for PCI Express Clock Outputs

Block Diagram



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