

Ultra Long-Range Ultrasonic Time-of-Flight Range Sensor

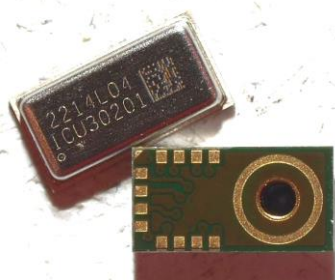
ICU-30201 HIGHLIGHTS

ICU-30201 is a miniature, ultra-low power, ultra-long -range ultrasonic Time-of-Flight (ToF) transceiver. Based on TDK's patented MEMS technology, the ICU-30201 is a system-in-package that integrates a nominally 53kHz PMUT (Piezoelectric Micromachined Ultrasonic Transducer) together with a 2nd generation ultra-low power SoC (System on Chip) in a miniature, reflowable package.

The SoC is designed for overall low system power and for ease of use with external host processors. Low power is achieved through custom digital circuitry to process and buffer the raw sensor readings. This keeps the integrated microcontroller in a low power idle state until needed. For ease of use, the microcontroller can process the raw sensor readings into high level signals, such as range to nearby target(s), or events, such as presence, which can be directly read by the host processor. The SoC communicates with the host processor over SPI at up to 13MHz at either 1.8 or 3.3V.

The small and thin package with bottom port configuration allows an easy integration.

Complementing TDK's other ultrasonic ToF sensor products, ICU-30201 provides accurate range measurements to targets at distances up to 9m. Based on ultrasonic pulse-echo measurements, the sensor works in any lighting condition, including full sunlight, and provides millimeter-accurate range measurements independent of the target's color and optical transparency. The sensor's Field of View (FoV) can be customized and enables simultaneous range measurements to multiple objects in the FoV. Several available algorithms can further process the echo information for a variety of usage cases in a range of applications (listed below).



DEVICE INFORMATION

PART NUMBER	PACKAGE	LID OPENING
ICU-30201	5.17 x 2.68 x 0.9 mm LGA	1-Hole

RoHS and Green-Compliant Package

APPLICATIONS

- Smart Home/ Smart Building / Energy saving
- Smart Lighting
- Home appliances
- Indoor security cameras
- Presence detection (LCD TV, Notebook)
- Smart Speakers / Meeting room device
- Smart door lock wake-on-approach

FEATURES

- Fast, accurate range-finding
 - 40MHz integrated CPU enables on-chip algorithms including rangefinding, human presence, and wake-on approach
 - Operating range from 30 cm to 9.5m
 - Human presence detection up to 7m
 - Programmable modes optimized for long or short-range sensing applications
 - Customizable Field of View (FoV) up to 180°
 - Multi-object detection
 - Works in any lighting condition, including full sunlight to complete darkness
 - Insensitive to object color, detects optically transparent surfaces (glass, clear plastics etc.)
- Easy to integrate
 - Single bottom port for receive and transmit
 - 1.8V or 3.3V I/O voltage
 - Low power 1.8V core operating voltage
 - 13MHz SPI interface allows baseband data stream out at full measurement rate
 - Optional 32kHz reference clock input
 - Two programmable open drain I/O for triggering the sensor or waking the host
 - Platform-independent software driver enables turnkey range-finding
- Miniature integrated package
 - 5.17mm x 2.68mm x 0.9mm, 13-pin LGA
 - Compatible with standard SMD reflow
 - Operating temperature range: -40°C to 85°C
- Low supply current
 - 1 measurements/sec: 30 μ A (9m max range)
 - 11 measurements/sec: 243 μ A (9m max range)

1 PIN OUT AND PIN DESCRIPTIONS

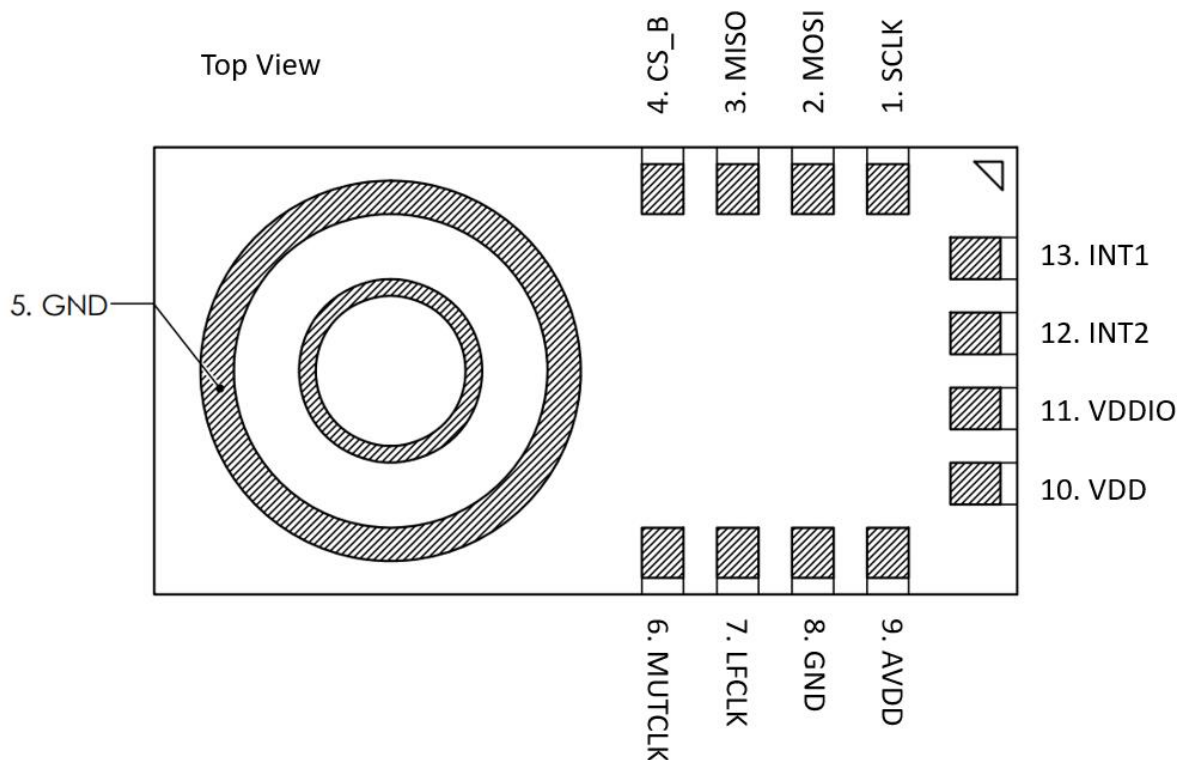


Figure 1: Ultrasonic Transceiver Pin Out (Top View)

1.1 PIN DESCRIPTIONS

PIN	NAME	DESCRIPTION
1	SCLK	SPI Interface Clock (from external SPI host) CPOL=1, CPHA=1
2	MOSI	<u>MCU Out</u> <u>Sensor In</u> serial data (from external SPI host)
3	MISO	<u>MCU In</u> <u>Sensor Out</u> serial data (to external SPI host)
4	CS_B	SPI Chip Select (active-low, from external SPI host)
5	GND	Ground – Bottom port ring (shorted with pin #8)
6	MUTCLK	External Input/Output 16x Operating Frequency Reference Clock (optional)
7	LFCLK	External Input/Output Low Frequency Reference Clock - 32.768kHz typical (optional)
8	GND	Ground - shorted with (shorted with pin #5)
9	AVDD	Analog Power Supply. Connect to externally regulated 1.8V +/-5% supply. Bypass with a 0.1μF capacitor close to AVDD & GND pad.
10	VDD	Digital Logic Supply. Connect to externally regulated 1.8V +/-5% supply. Suggest common connection to AVDD. If not connected locally to AVDD, bypass with a 0.1μF capacitor close to VDD & GND pad.
11	VDDIO	I/O Power Supply. Connect to externally regulated 1.8V +/-5% or 3.3V +/-10% supply. Bypass with a 1μF capacitor close to VDDIO & GND pad.
12	INT2	Interrupt Request Open Drain Input/Output 2
13	INT1	Interrupt Request Open Drain Input/Output 1

Table 1. Pin Descriptions

2 ELECTRICAL CHARACTERISTICS

2.1 ABSOLUTE MAXIMUM RATINGS

PARAMETER	MIN.	TYP.	MAX.	UNIT
AVDD/VDD to GND	-0.3		2.2	V
VDDIO to GND	-0.3		4.0	V
Digital I/O pins to GND	-0.3			V
Digital I/O pins to VDDIO			0.3	V
Electrostatic Discharge (ESD)	Human Body Model (HBM) ⁽¹⁾	-2	2	kV
	Charge Device Model (CDM) ⁽²⁾	-500	500	V
Latchup	-100		100	mA
Temperature, Operating	-40		85	°C
Relative Humidity, Storage			90	%RH
Continuous Input Current (Any Pin)	-20		20	mA
Soldering Temperature (reflow)			260	°C

1. HBM Tests conducted in compliance with ANSI/ESDA/JEDEC JS-001-2024 Or JESD22-A114E
2. CDM Tests conducted in compliance with JESD22-C101.

2.2 ULTRASONIC TRANSCEIVER CHARACTERISTICS

(AVDD = VDD = 1.8VDC, VDDIO=3.3V, GND = 0V, T_A = +25°C, min/max are from T_A = -40°C to +85°C, unless otherwise specified.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
POWER SUPPLY						
Analog Power Supply	AVDD		1.71	1.8	1.89	V
Digital Power Supply	VDD		1.71	1.8	1.89	V
IO Power Supply	VDDIO		1.71	3.3	3.63	V
Operating Temperature			-40	25	85	°C
ULTRASONIC TRANSMIT CHANNEL						
Operating Frequency	f _{op}		45 ⁽¹⁾	53	60 ⁽¹⁾	kHz
TxRx OPERATION⁽²⁾						
Maximum Range	Max Range	Wall Target 58mm Diameter Post		9.5 ⁽³⁾ 6 ⁽³⁾		m m
Minimum Range	Min Range			0.25	0.3 ⁽⁴⁾	m
Baseband I/Q Output Sample Rate		Output Data Rate ODR=2 ODR=3 ODR=4 ODR=5 ODR=6		f _{op} /32 f _{op} /16 f _{op} /8 f _{op} /4 f _{op} /2		Samples/s
Measuring Rate (Meas/sec)	MR	2m max range 9m max range		75 18		Measurements/sec Measurements/sec
Field of View	FoV		Configurable up to 180°			deg
Current Consumption (AVDD + VDD) ⁽⁵⁾	I _s	MR=1Meas/s, Max Range=2m MR=1Meas/s, Max Range=9m MR=11Meas/s, Max Range=9m MR=25Meas/s, Max Range=2m MR=25Meas/s, Max Range=6m		17 30 ⁽⁶⁾ 243 ⁽⁶⁾ 214 395 ⁽⁶⁾		μA μA μA μA μA
Idle Current (AVDD+VDD)	I _i	T=+25°C, VDD=AVDD=1.8V Sensor Idle, FW programmed.		9	18	μA
Measurement Latency ⁽⁷⁾		2m max range 9m max range		13 55		ms ms
Programming Time				5 ⁽⁸⁾		ms

Notes:

- 100% tested; some shift is normal after SMT
- General Purpose Transceiver Firmware loaded; ODR=4 unless otherwise noted
- By characterization, tested using a typical mid-FoV acoustic housing.
- By characterization, tested with a stationary target. While objects closer than 30 cm can be detected, the range measurement is not ensured.
- When configured for rangefinding; stationary target rejection not enabled
- ODR = 3
- By characterization, from start of measurement to data ready signal on INT line
- By characterization, tested with SCLK frequency of 12.5MHz

2.3 I/O CHARACTERISTICS

(AVDD = VDD = 1.8V, VDDIO=3.3V, GND = 0V, T_A = +25°C, min/max are from T_A = -40°C to +85°C, unless otherwise specified.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
DIGITAL INPUTS (MOSI, CS_B, SCLK, MUTCLK, LFCLK, INT1, INT2)						
V _{IH} , High Level Input Voltage	V _{IH}		0.7*V _{VDDIO}			V
V _{IL} , Low Level Input Voltage	V _{IL}				0.3*V _{VDDIO}	V
Input Capacitance	C _I			<10		pF
DIGITAL OUTPUT (MISO, MUTCLK, LFCLK, INT1, INT2)						
High Level Output Voltage	V _{OH}	R _{LOAD} =1 MΩ	0.9*V _{VDDIO}			V
Low Level Output Voltage	V _{OL}	R _{LOAD} =1 MΩ			0.1*V _{VDDIO}	V
Output Leakage Current		OPEN=1		±100		nA

2.4 SPI TIMING CHARACTERISTICS – 4-WIRE SPI MODE

(AVDD = VDD = 1.8VDC, VDDIO=1.8V or 3.3V, GND = 0V, T_A = +25°C, min/max are from T_A = -40°C to +85°C, unless otherwise specified.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
DIGITAL I/O CHARACTERISTICS						
SCLK Clock Frequency	f _{CLK}			13		MHz
SCLK Low Period	t _{LOW}		11			ns
SCLK High Period	t _{HIGH}		11			ns
CS_B Setup Time	t _{SU.CSB}		80			ns
CS_B Hold Time	t _{HD.CSB}		80			ns
MOSI Setup Time	t _{SU.MOSI}		2			ns
MOSI Hold Time	t _{HD.MOSI}		2			ns
MISO Valid Time	t _{VD.MISO}	C _{load} = 20pF		28		ns
MISO Hold Time	t _{HD.MISO}	C _{load} = 20pF	2			ns
MISO Output Disable Time	t _{DIS.MISO}			3		ns

Notes: Based on characterization of 10 parts from 3 typical and 2 corner lots. Measured over temperature and voltage as mounted in sockets.

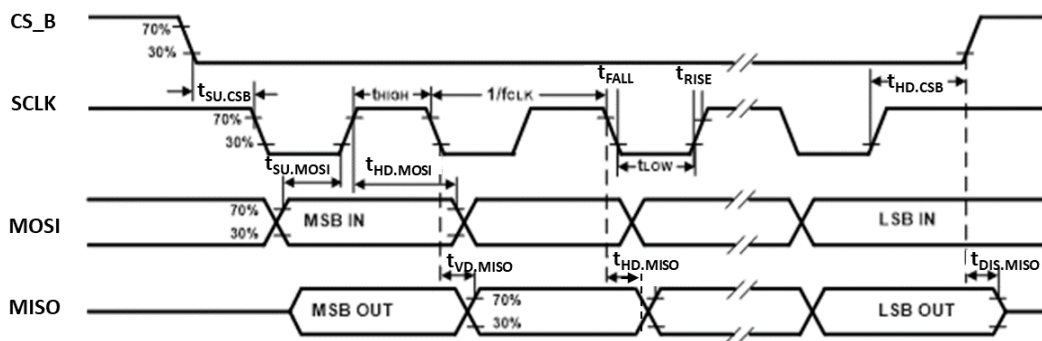


Figure 2: 4-wire SPI timing diagram

2.5 SPI TIMING CHARACTERISTICS – 3-WIRE SPI MODE

(AVDD = VDD = 1.8V, VDDIO=1.8V or 3.3V, GND = 0V, T_A = +25°C, min/max are from T_A = -40°C to +85°C, unless otherwise specified.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
DIGITAL I/O CHARACTERISTICS						
SCLK Clock Frequency	f _{CLK}				13	MHz
SCLK Low Period	t _{LOW}		11			ns
SCLK High Period	t _{HIGH}		11			ns
CS_B Setup Time	t _{SU,CSB}		80			ns
CS_B Hold Time	t _{HD,CSB}		80			ns
SDIO Input Setup Time	t _{SU,SDIO}		2			ns
SDIO Input Hold Time	t _{HD,SDIO}		2			ns
SDIO Output Valid Time	t _{VD,SDIO}	C _{load} = 20pF			21.5	ns
SDIO Output Hold Time	t _{HD,SDIO}	C _{load} = 20pF	2			ns
SDIO Output Disable Time	t _{DIS,SDIO}				3	ns

Notes: Based on characterization of 10 parts from 3 typical and 2 corner lots. Measured over temperature and voltage as mounted in sockets.

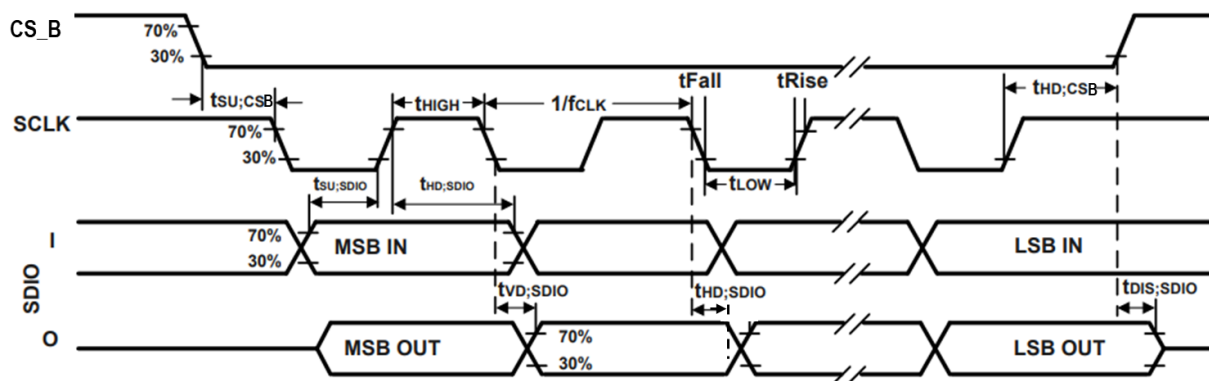


Figure 3: 3-wire SPI timing diagram

3 DETAILED DESCRIPTION

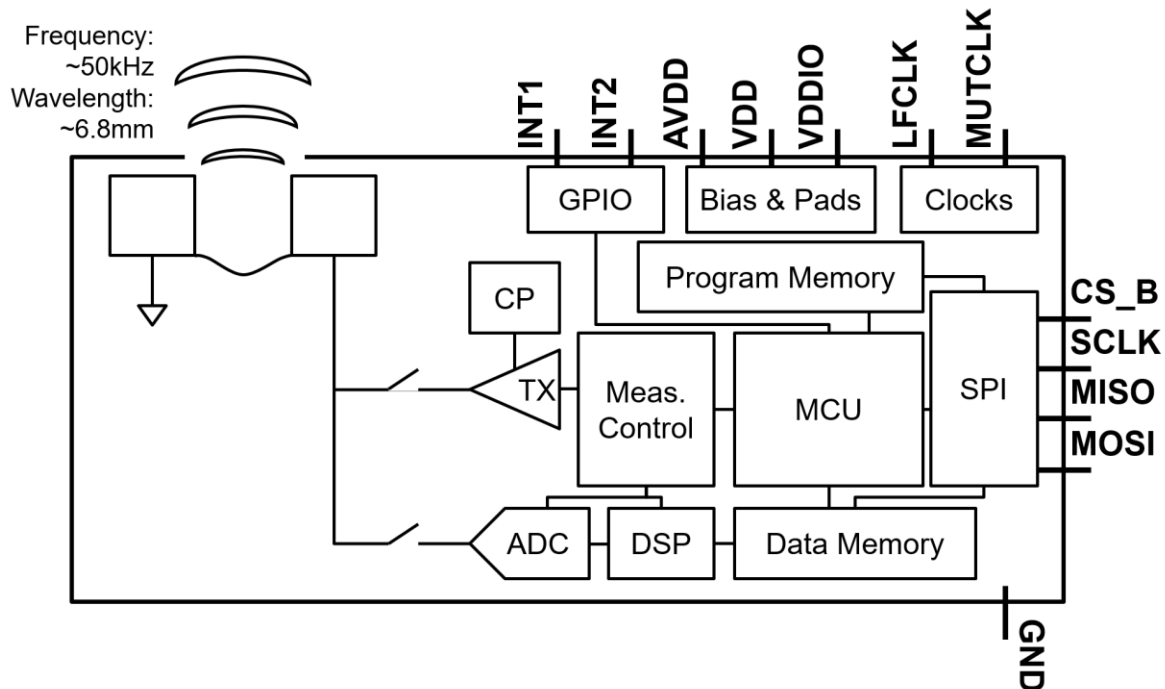


Figure 4. Simplified Block Diagram

3.1 THEORY OF OPERATION

The ICU-30201 is an autonomous, digital output ultrasonic transceiver. The Simplified Block Diagram in Figure 4 shows the main components at the package-level. The package includes a piezoelectric micro-machined ultrasonic transducer (PMUT) and system-on-chip (SoC). The SoC controls the PMUT to produce pulses of ultrasound at an operational frequency f_{op} that reflect off targets in the sensor's Field of View (FoV). The reflections are received by the same PMUT after a short time delay, amplified by sensitive electronics, digitized, and stored as I/Q baseband data in the on-chip data memory. Software defined algorithms can process the I/Q data to detect targets using the on-chip microcontroller (MCU), and these algorithms can be tuned to detect stationary or moving targets preferentially. I/Q data can also be transferred to a larger host or application processor (AP) for further processing.

Ultrasonic Transducer

The PMUT within ICU-30201 is positioned directly above the acoustic port on the bottom of the device. The PMUT can transmit and receive ultrasound. During the transmitting process, a high voltage pulse train is applied to the PMUT and the PMUT transmits a sound wave into the air from the bottom of ICU-30201. During the receive process, the sound waves impinging on the PMUT create a small current which is amplified and digitized and stored in the on-chip memory.

The frequency of the PMUT varies as a function of the MEMS process; TDK's patented frequency locking algorithm ensures that the transmitting and receiving operational frequency f_{op} is tuned to the PMUT frequency and the transmitting and receiving sensitivities are optimized.

ICU-30201's acoustic port provides a nearly hemispherical radiation pattern. Most applications require a narrower FoV and so will use a horn to focus the sound. Somewhat unintuitively, wider diameter horns provide narrower FoV beam patterns. TDK has several horn 3D models readily available for customer design-in.

Measurement process

ICU-30201's measurement state machine (MSM) is the core of the ultrasonic transceiver measurement process. The MSM, once triggered by the MCU, fetches specialized instructions from memory and executes those instructions, causing specific actions to be performed by the transceiver. Instructions consist of a command, interrupt configuration, command specific configuration, and a

length. The supported commands are transmit, receive, count, and end-of-file (EOF). Each command is executed for the length of the instruction, which is specified in counts of the measurement clock, which operates at $16f_{op}$.

Taken together, several instructions form a measurement queue, which defines the measurement to be taken by the transceiver. The measurement queue can be adapted to address specific requirements posed by an application. Typically, the penultimate instruction will consist of a receive instruction with the DONE_IEN bit set, and the last instruction will be EOF. The MCU will then wake up at the conclusion of the measurement and process the I/Q data or forward it to the host for processing.

CMD[1:0]	Description
00	Count
01	Transmit
10	Receive
11	End of file

Table 2. PMUT Commands

Byte	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
Byte 0	X	X	RX_BIAS_EN	RX_RESET	DONE_IEN	RDY_IEN	0	0
Byte 1	X	X	X	X	X	X	X	X
Byte 2	LENGTH[7:0]							
Byte 3	LENGTH[15:8]							

Table 3: Count instruction format

Byte	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
Byte 0	X	X	RX_BIAS_EN	RX_RESET	DONE_IEN	RDY_IEN	0	1
Byte 1	Phase[3:0]				X	Pulse_Width[2:0]		
Byte 2	LENGTH[7:0]							
Byte 3	LENGTH[15:8]							

Table 4: TX instruction format

Byte	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
Byte 0	0	1	RX_BIAS_EN=1	RX_RESET	DONE_IEN	RDY_IEN	1	0
Byte 1	X	RX_ATTEN[1:0]			RX_GAIN_REDUCTION[4:0]			
Byte 2	LENGTH[7:0]							
Byte 3	LENGTH[15:8]							

Table 5: RX instruction format

Byte	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
Byte 0	X	X	X	X	DONE_IEN	RDY_IEN	1	1
Byte 1	X	X	X	X	X	X	X	X
Byte 2	0xXX							
Byte 3	0xXX							

Table 6: EOF instruction format

I/Q Baseband data

The received signal is amplified, digitized, and downconverted (multiplied) to the baseband (dc) by sine and cosine components of a digital carrier signal at the operational frequency f_{op} . The component of the signal that is in-phase with the cosine demodulator is called in-phase (I) and the component that is in-phase with the sine demodulator is called quadrature (Q). I and Q signals are then filtered and downsampled to a lower sample rate by a digital comb filter.

The I/Q baseband data contains amplitude (calculated by $\sqrt{I^2 + Q^2}$), representing the envelope of the signal, and phase ($\text{atan2}(Q, I)$), representing the relative contribution of the I and Q parts of the signal. I/Q data contains all the information necessary to reconstruct the ultrasonic signal; however, efficient digital signal processing is best done in the complex baseband. Many algorithms calculate amplitude and discard the phase information, detecting rising edge(s) in the amplitude data which correspond to echoes or pulses from other transceivers. In certain applications, the phase data may provide additional information (i.e., to refine the range estimate) or provide the primary source of information (e.g., in an ultrasonic interferometer).

After the comb filter outputs a sample of the I/Q data, the measurement state machine (MSM) writes the I/Q data directly to the data memory as two signed 16-bit integers. The sample rate of the I/Q data is set by the ODR setting and can be $f_{op}/32$, $/16$, $/8$, $/4$, or $/2$. Generally, lower ODR is used for low-power presence sensing applications; medium ODR is used for rangefinding up to 5m, and higher ODR is used for short range sensing.

Rangefinding

The time it takes the ultrasound pulse to propagate from the PMUT to a target and back is called the time-of-flight (ToF). The distance to the target is found by calculating the time-of-flight using a rangefinding algorithm, multiplying the time-of-flight by the speed of sound, and dividing by two (to account for the round-trip). The speed of sound in air is approximately 343 m/s. The speed of sound is not a constant, but it is stable enough (0.2%/°C) to give measurement accuracies within a few percent error.

The rangefinding algorithm typically looks for a rising edge in the amplitude (envelope) of the I/Q data; as the data is smooth at nearly all sample rates except the lowest ODR, interpolation is typically used to refine the ToF estimate. The general-purpose transceiver (GPT) firmware outputs an unsigned 16-bit integer to represent time-of-flight; the time resolution of the time-of-flight is $\frac{1}{8 \times 2^{ODR} f_{op}}$ or 147ns for ODR=4 and $f_{op}=53\text{kHz}$.

Low power systems

ICU-30201 has several features that enable low power operation of a system. A low-power, on-chip real-time clock (RTC) sets the sample rate and provides the reference for the time-of-flight measurement. The host processor does not need to provide any stimulus to the ICU-30201 during normal operation, allowing the host processor to be shut down into its lowest power mode until the ICU-30201 generates a wake-up interrupt. There are also two general-purpose input/output (INT1 and INT2) pins that can be used as a system wake-up source. The interrupt pins can be configured to wake up the host when a target is detected.

Clock Calibration

ICU-30201 has 3 internal clocks, the LFCLK, the MUTCLK, and the CPUCLK. The LFCLK is nominally 30kHz and can be measured using the pulse timer peripheral. Alternatively, a crystal-derived 32.768kHz (or other known frequency) low frequency clock can be input through the LFCLK pin of ICU-30201. Once the LFCLK frequency is known, it can be used to measure the frequency of MUTCLK and CPUCLK using the frequency timer peripheral.

The CPUCLK is nominally 40MHz and can be tuned to increase algorithm runtime consistency between ICU-30201.

The MUTCLK is digitally controlled to 16 times the operating (transmit and receive) frequency f_{op} , and the PMUT is most sensitive at the PMUT resonant frequency f_n , which is typically 53kHz; therefore, MUTCLK is typically 800kHz.

The ICU-30201's SonicLib C driver includes a method to measure the LFCLK frequency using the pulse timer feature. The host MCU will configure the pulse timer, pulse the INT1 line low, wait for a known duration (this delay, typically 100ms, must be generated by a crystal or a trimmed oscillator), and then pulse the INT1 line low again. The LFCLK frequency will then be known to the driver.

The SonicLib driver also includes methods to measure the CPUCLK and MUTCLK frequencies (relative to the LFCLK frequency) using the frequency counter feature in ICU-30201.

Ultimately, the time-of-flight and I/Q data read from ICU-30201 is produced from the MUTCLK time base, and the MUTCLK frequency (and by extension the LFCLK frequency) must be known to have an accurate time-of-flight measurement.

3.2 DEVICE CONFIGURATION

An ICU-30201 program file must be loaded into the on-chip memory at initial power-on. The program, or firmware, is loaded through the SPI interface by the SonicLib C driver. The GPT firmware enables autonomous range finding operation of the ICU-30201. It also supports hardware-triggering of the ICU-30201 for applications requiring multiple transceivers. Chip configuration can also be tailored to the customer's application using the ICU-30201 EVK. Contact TDK support for more information.

4 APPLICATIONS

4.1 TYPICAL OPERATING CHARACTERISTICS

AVDD = VDD = VDDIO= 1.8VDC, GND = 0V, T_A = +25°C, unless otherwise specified.

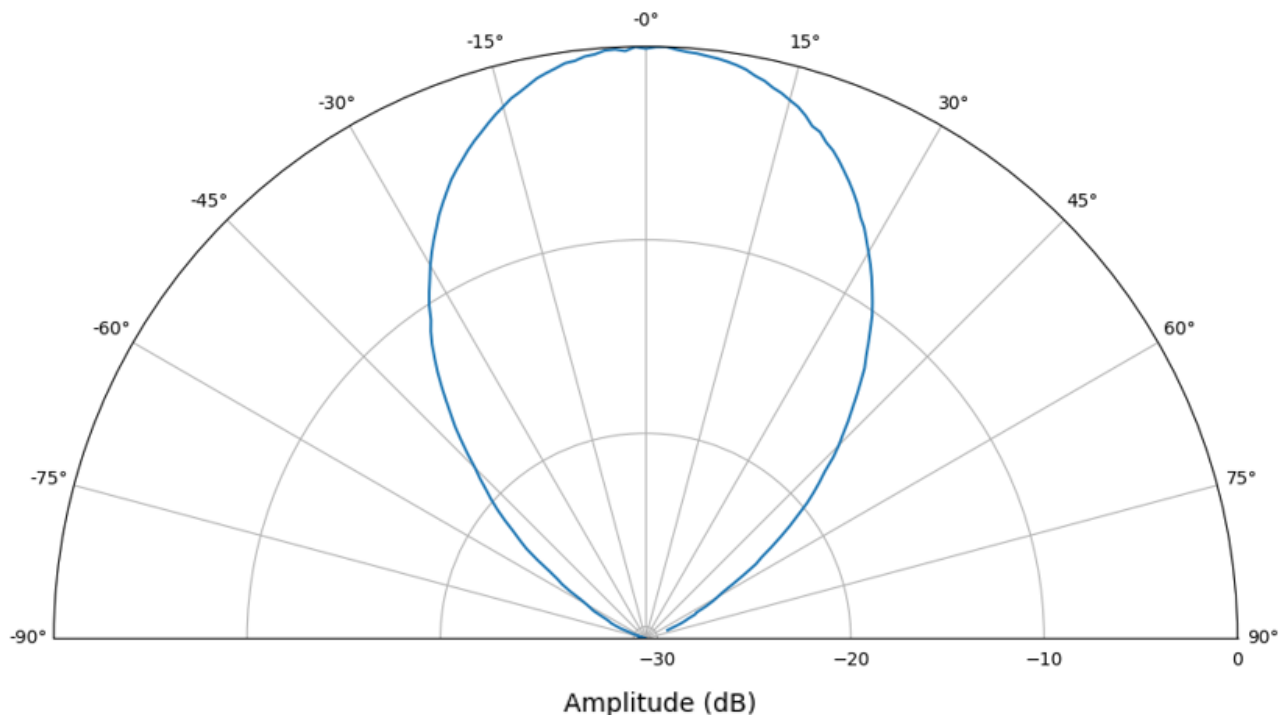


Figure 5. Typical Beam Pattern of Mid-FoV Module
(Measured in pulse-echo mode with a 58 mm pole target at a 0.3 m range)

4.2 SONICLIB DRIVER

TDK provides a compiler- and microcontroller-independent C driver for the ICU-30201 which greatly simplifies integration. The ICU-30201 driver implements high-level control of one or more ICU-30201s attached to one or more SPI ports on the host processor. The ICU-30201 driver allows the user to program, configure, trigger, and readout data from the ICU-30201 through use of C function calls without direct interaction with the ICU-30201 SPI registers. The ICU-30201 driver only requires the customer to implement an I/O layer which communicates with the host processor's SPI hardware and GPIO hardware. TDK highly recommends that all designs use the ICU-30201 driver.

4.3 OBJECT DETECTION

Detecting the presence of objects or people can be optimized via software, by setting the sensor's full-scale range (FSR), and via hardware, using an acoustic housing to narrow or widen the sensor's field-of-view. The former means that the user may set the maximum distance at which the sensor will detect an object. FSR values refer to the one-way distance to a detected object.

In practice, the FSR setting controls the amount of time that the sensor spends in the listening (receiving) period during a measurement cycle. Therefore, the FSR setting affects the time required to complete a measurement. Longer full-scale range values will require more time for a measurement to complete.

Ultrasonic signal processing using the ICU-30201's GPT Firmware will detect echoes that bounce off the first target in the Field-of-View. The size, position, and material composition of the target will affect the maximum range at which the sensor can detect the target. Large targets, such as walls, are much easier to detect than smaller targets. Thus, the associated operating range for smaller targets will be shorter. The range to detect people will be affected by a variety of factors such as a person's size, clothing, orientation to the sensor and the sensor's field-of-view. In general, given these factors, people can be detected at a maximum distance of 6-7m away from the ICU-30201 sensor.

4.4 DEVICE MODES OF OPERATION

Free-Running Mode

In the free-running measurement mode, the ICU-30201 runs autonomously at a user specified measurement rate derived from either the internal LFCLK or the external LFCLK pin. In this mode, the INT1 or INT2 pin is configured as an output. The ICU-30201 pulses the INT pin low when a new range sample is available. At this point, the host processor may read the sample data from the ICU-30201 over the SPI interface. When in free-running mode using the internal LFCLK, there must not be another ICU-30201 within range of the device, otherwise interference may occur, as each device will operate at slightly different measurement rates.

Hardware-Triggered Mode

In the hardware triggered mode, one of the INT1/2 pins is used to trigger the start of a measurement. The ICU-30201 remains in an idle condition until triggered by pulsing the INT pin low. The measurement will start with deterministic latency relative to the falling edge on INT. This mode is most useful for synchronizing several ICU-30201 transceivers. The host controller can use the individual INT pins of several transceivers to coordinate the exact timing. A single INT1/2 pin can be used bi-directionally, or the transceiver can be configured to accept a trigger on one INT pin and indicate data ready on another.

Software-Triggered Mode

In the software triggered mode, the host processor triggers the start of a measurement by a SPI write to the transceiver. INT1 or INT2 are pulsed low by ICU-30201 when the measurement is complete. By chip selecting and writing to several ICU-30201 simultaneously, it is possible to synchronize the start of a measurement using this method as well; however, hardware trigger mode is recommended for best performance.

4.5 COEXISTENCE NEAR OTHER ICU-30201

Nearby ICU-30201 which have similar f_{op} can receive sound from one another if they are within <20m of each other. When ICU-30201 has an algorithm loaded that ignores stationary targets (e.g., static target rejection mode or presence FW), it is possible for multiple ICU-30201 to co-exist in the same space without detecting the other device as a target, if each ICU-30201 is triggered at a precise measurement rate (such as 10Hz).

The measurement rate must be derived from a quality crystal- or MEMS-based oscillator. When ICU-30201 receives a pulse from a nearby ICU-30201, it will interpret it as a stationary target. The reason is that both ICU-30201 will be running at nearly the same measurement rate (excepting the tolerance of the crystals used), so the receiving ICU-30201 will hear the other ICU-30201's pulse at nearly the same time during each measurement.

There are two recommended methods to operate ICU-30201 at a precise measurement rate to ensure good coexistence behavior are a) to use Hardware-triggered mode with a timer driven by a crystal-derived clock; or b) to use Free-running mode with a crystal-derived clock input to the LFCLK pin and ICU-30201 configured appropriately. In both cases, the crystal-derived clock must match the clock used by the 2nd ICU-30201 to ensure that ICU-30201 will not detect the nearby device.

4.6 PCB REFLOW RECOMMENDATIONS:

See App Note AN-000159 CH101 and CH201 Ultrasonic Transceiver Handling and Assembly Guidelines.

4.7 3-WIRE SPI MODE

ICU-30201 supports 3-wire SPI mode, with MOSI and MISO using a single data line. Communication with ICU-30201 is always single duplex, with either MISO or MOSI carrying data. When MISO is not active, its output driver is disabled and it is kept in a high-z state. As such, in order to use ICU-30201 in 3-wire SPI mode, no special configuration is required; simply short MISO and MOSI nearby the ICU-30201 package and configure the host to operate in 3-wire SPI mode with a single shared data line.

4.8 TYPICAL OPERATING CIRCUITS

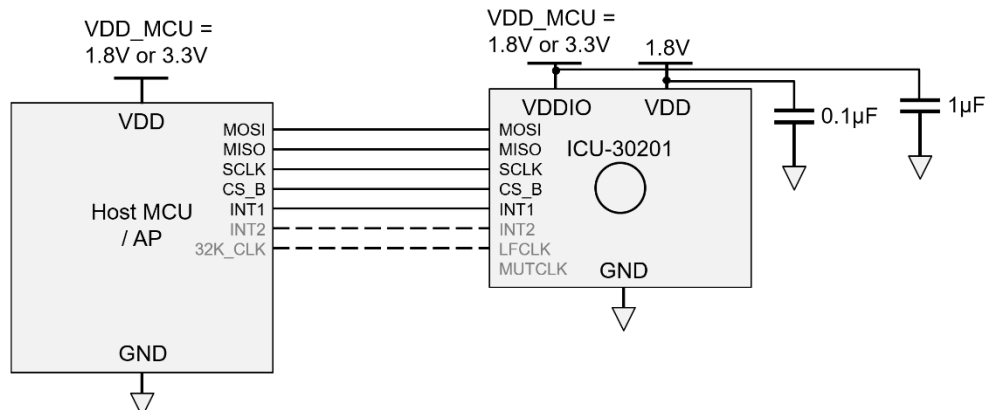


Figure 6. Single Transceiver Operation. Dashed connections are optional. For 3-wire SPI operation, simply short MOSI and MISO near ICU-30201.

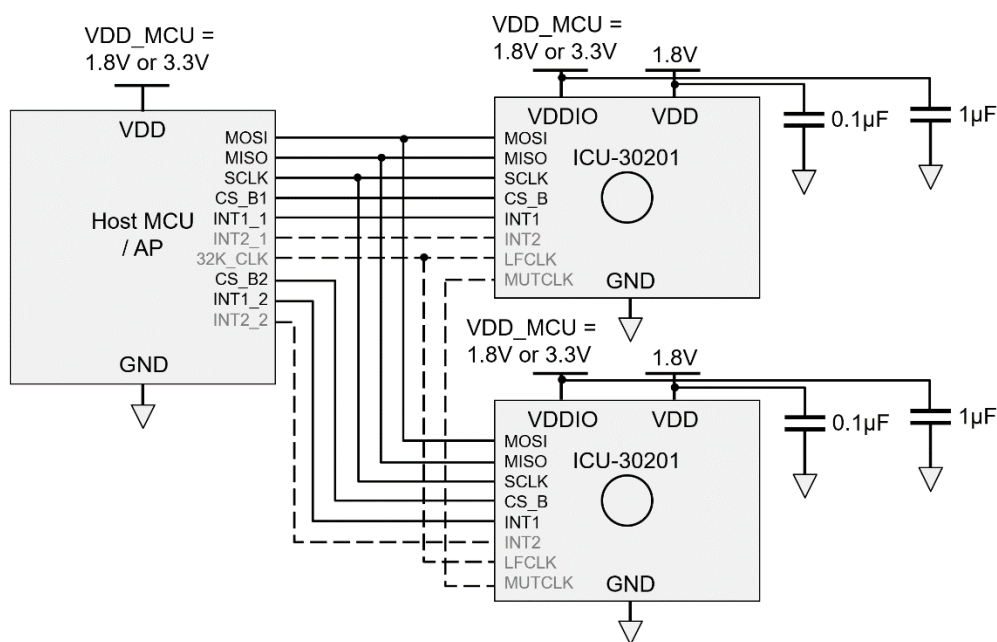


Figure 7. Multi-Transceiver Operation. Dashed connections are optional. Add additional transceivers by adding CS_B and INT1 for each additional transceiver and sharing common MOSI/MISO/SCLK connections. For 3-wire SPI operation, simply short MOSI and MISO near ICU-30201.

4.9 RECOMMENDED PCBA LAYOUT

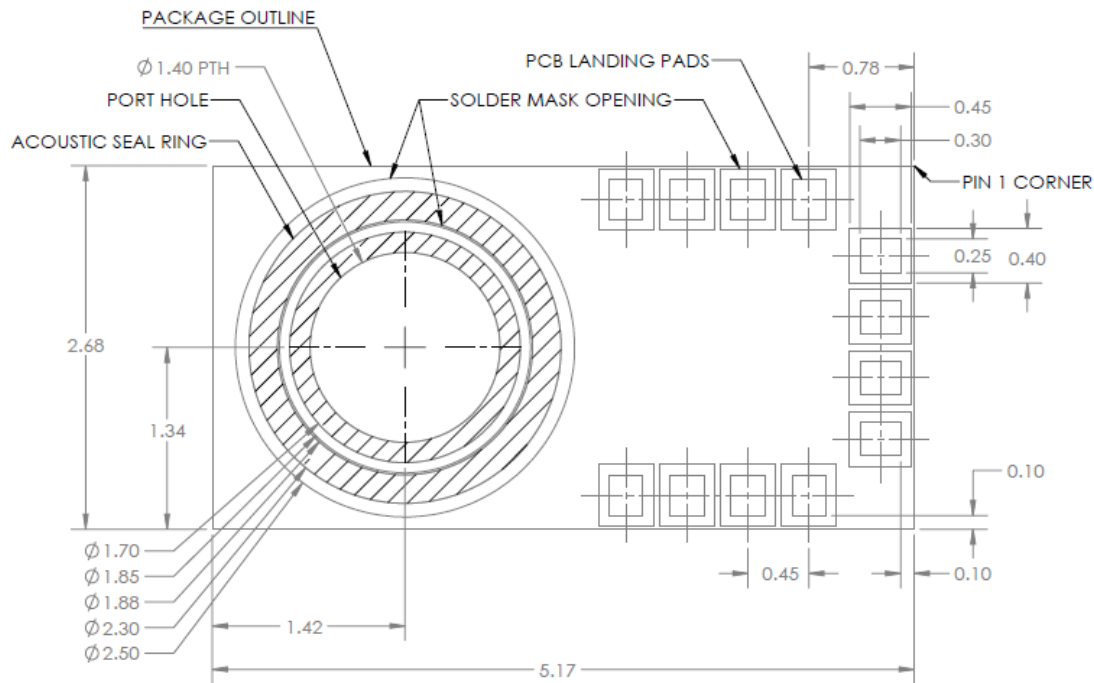


Figure 8. Recommended SMT dimensions for solder mask and PCB land pads

The PCB layout for ICU-30201 should adhere to the following recommendations:

- PCB design should be as symmetrical as possible
 - Since the transceiver has very low power consumption, large VDD and GND traces are not required
 - Do not place vias or traces on the top metal layer within the transceiver footprint
- **PCB thickness should equal 0.60 +/- 0.08mm.** This is required for good acoustic performance of ICU-30201.
- PCB land and connecting traces should be symmetrical
- PCB land height and width should be equal the transceiver pad height and width (Figure 8)
- Solder mask opening should exceed PCB land height and width by 0.1mm (Figure 8)
- Drill holes to meet +/-0.08mm (3 mil) tolerance
- Placement of the transceiver on the PCB should avoid locations close to hot spots such as microprocessors and points of mechanical stress such as pushbuttons and screws

4.10 RECOMMENDED SOLDER PROFILE

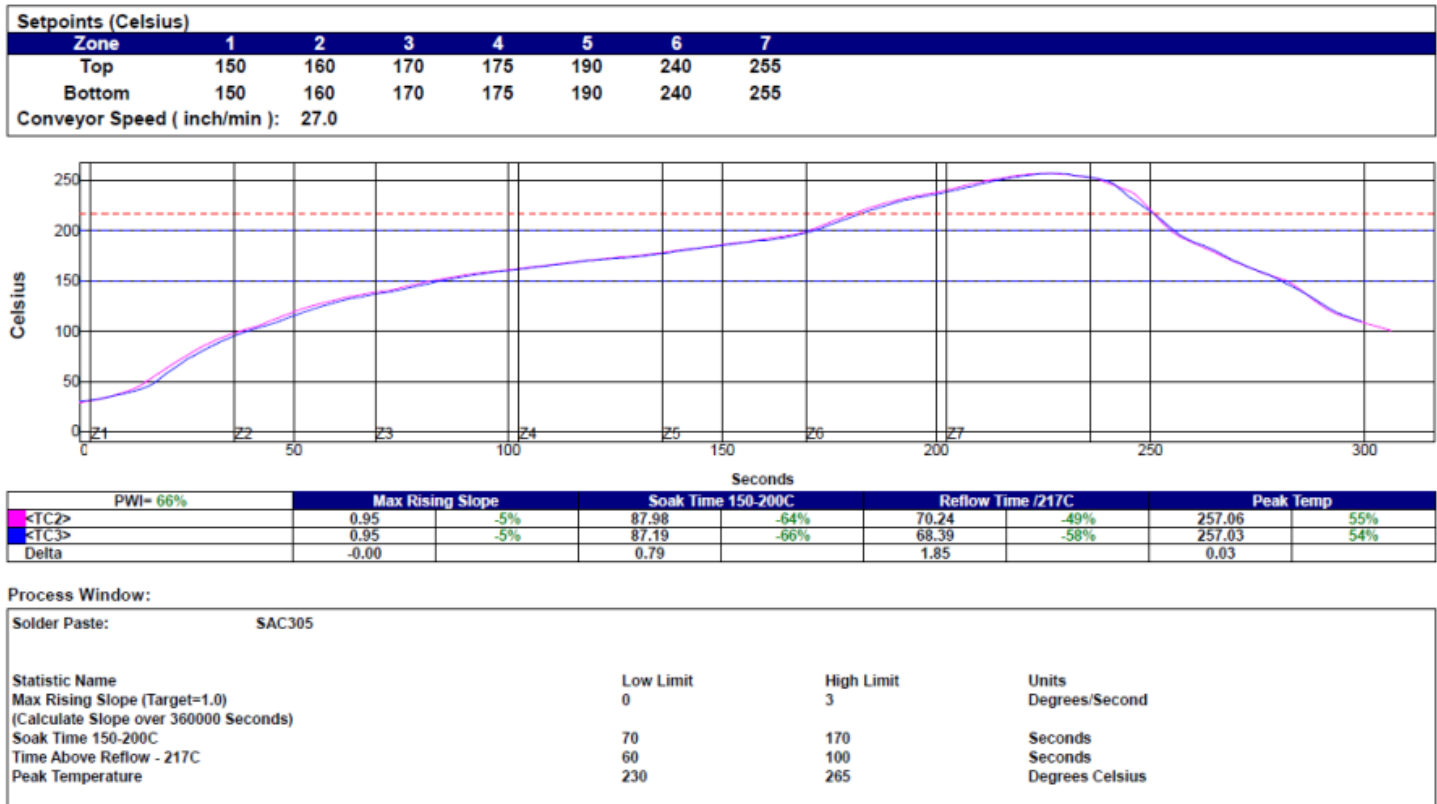


Figure 9: Recommended Solder Profile

5 PACKAGE INFORMATION

5.1 PIN CONFIGURATION

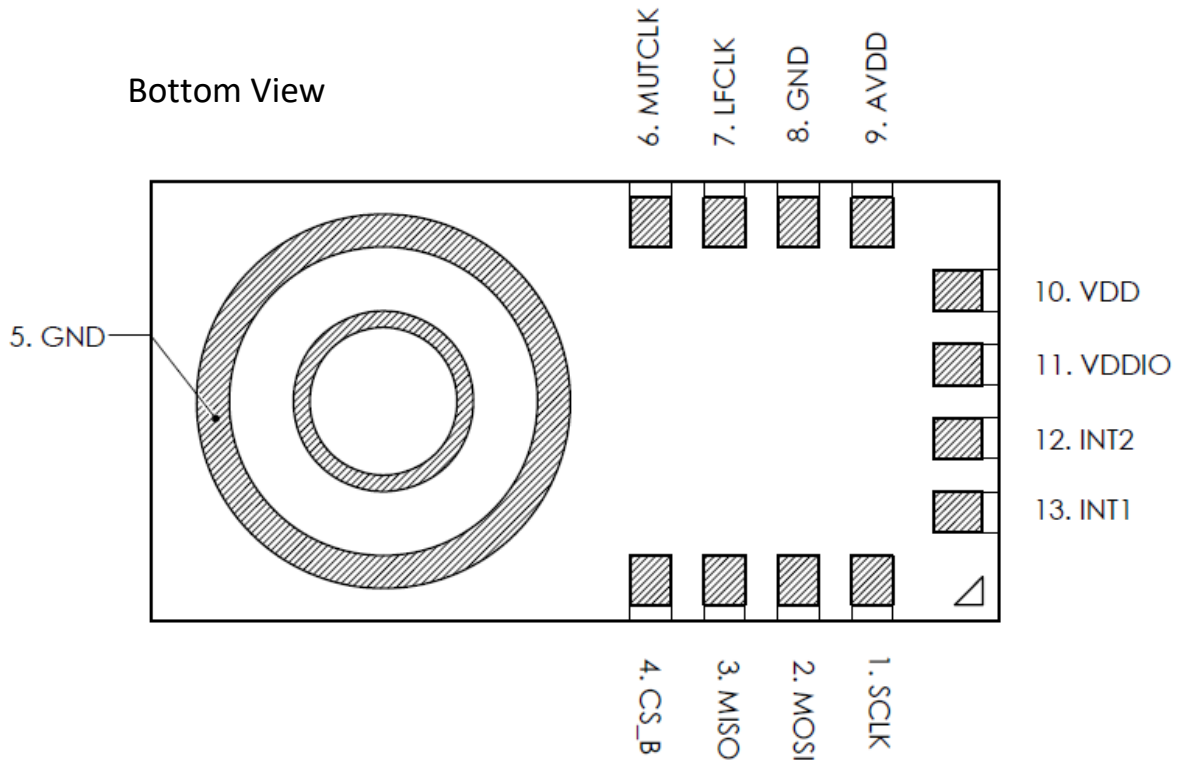
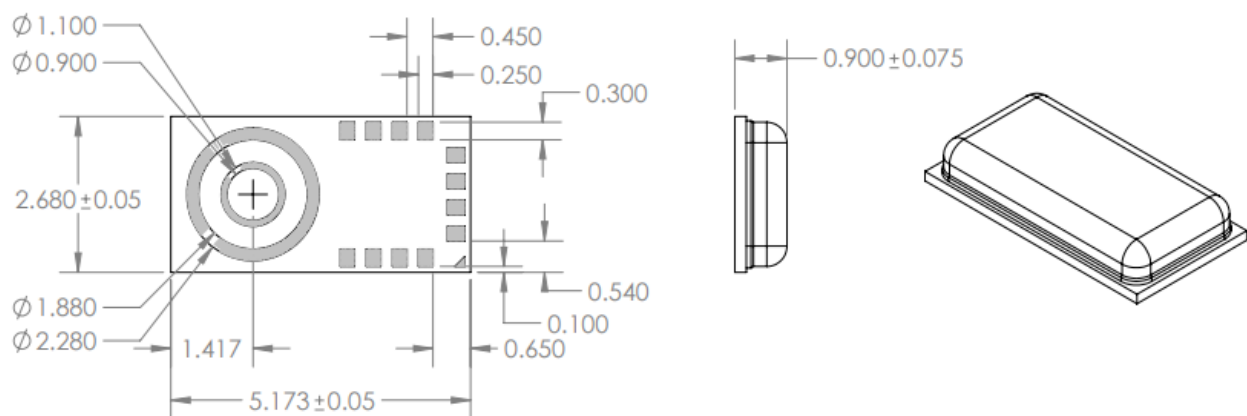


Figure 10. ICU-30201 Package Bottom View

5.2 PACKAGE DIMENSIONS



Note: All dimensions in mm. Package Bottom View.

Figure 11. Package dimensions

6 ORDERING INFORMATION

6.1 PART NUMBER DESIGNATION

This datasheet specifies the following part numbers:

PART NUMBER	PACKAGE BODY	QUANTITY	PACKAGING
ICU-30201	5.17 x 2.68 x 0.9 LGA-13L	1000	7" Tape and Reel

Table 7. ICU-30201 Part Number

6.2 PACKAGE MARKING

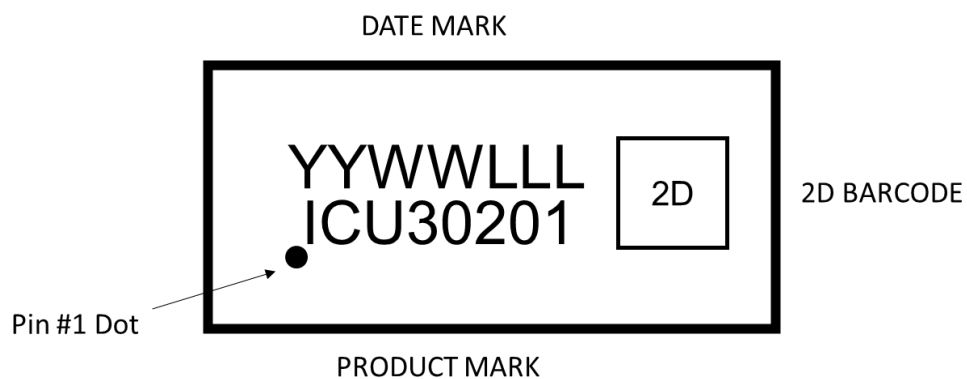


Figure 12. Packaging Marking

6.3 TAPE & REEL SPECIFICATION

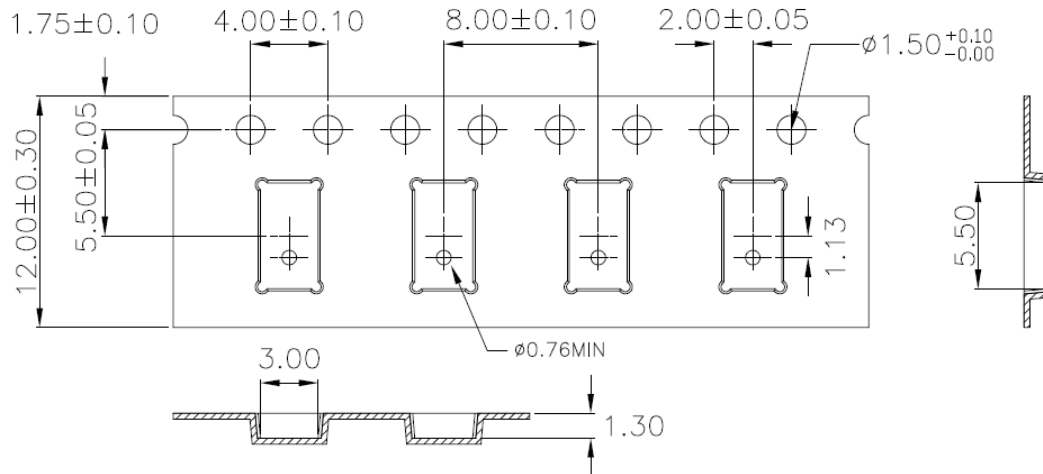


Figure 13. Tape & Reel Specifications

6.4 SHIPPING LABEL

A shipping label will be attached to the reel, bag and box. The information provided on the label is as follows:

- Device: This is the full part number
- Lot Number: TDK/InvenSense manufacturing lot number
- Date Code: Date the lot was sealed in the moisture proof bag
- Quantity: Number of components on the reel
- 2D Barcode: Contains Lot No., quantity and reel/bag/box number

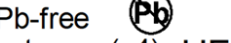
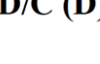
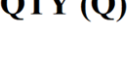
   		
DEVICE (1P) : ICU-30201	P.O:	REEL QTY (Q) :
		
LOT 1 (1T) :	D/C (D) :	QTY (Q) :
		
LOT 2 (1T) :	D/C (D) :	QTY (Q) :
		
Reel Date :	QC STAMP	

Figure 14. Shipping Label

7 REVISION HISTORY

DATE	REVISION	CHANGES
06/16/2022	0.1	Preliminary release
2/7/2023	1.0	Preliminary version update
4/2/2024	1.1	Removed preliminary watermark Updated Section 2.2 Added Product Marking information Added Tape and Reel information Added Shipping Label information Added pinout diagram and moved block diagram to section 3
4/8/2024	1.2	Add idle current specification in section 2.2 Fixed word wrapping issue in Section 2.3: Low Level Input Voltage and Low Level Output Voltage have maximum specifications, not minimum specifications Added section 4.7 3-wire SPI mode Added notes in section 4.8 about 3-wire SPI mode Fixed minor formatting errors
3/13/2025	1.3	Updated typical supply current
3/20/2025	1.4	Fix Figure 1: MOSI is pin 2, MISO is pin 3

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