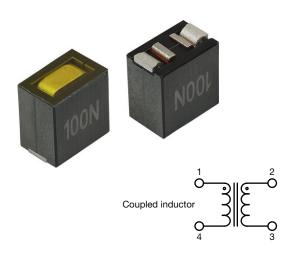
HALOGEN

FREE

GREEN



Coupled Ferrite, High Current Power Inductors for TLVR Topologies



LINKS TO ADDITIONAL RESOURCES



FEATURES

- Optimized for trans-inductor voltage regulator (TLVR) topologies
- Enables near-instant power delivery for highly dynamic loads
- Tightens output voltage ripple and decreases size of output capacitors
- Low core loss due to ferrite construction
- Inductance range 70 nH to 220 nH
- 4 package sizes available
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- 12 V / 1.8 V 16-phase buck voltage regulators
- · Datacenters, servers, storage systems
- · Graphics processing units (GPU)
- Central processing unit (CPU)
- Application specific integrated circuit (ASIC)

STANDARD ELECTRICAL SPECIFICATIONS								
		L ₀ INDUCTANCE	DCR	HEAT RATING	SATURATION CURRENT DC TYP. (A) ⁽²⁾		Q FACTOR	
PART NUMBER	TEST TERMINAL			CURRENT DC TYP. (A) (1)	AT 25 °C	AT 100 °C	AT 100 kHz MIN.	
9.6 mm x 6.4 mm x 10.5 mm MAX.								
IHTL3825JEEZR10L	1-4	0.100	0.125	75	98	83	21	
	2-3		0.33	40				
IHTL3825JEEZR12L	1-4	0.120	0.125	75	79	67	25	
	2-3		0.33	40				
IHTL3825JEEZR15L	1-4	0.150	0.125	75	62	53	27	
	2-3		0.33	40				
IHTL3825JEEZR18L	1-4	0.180	0.125	75	- 54	46	37	
	2-3		0.33	40				
IHTL3825JEEZR22L	1-4	0.220	0.125	75	50	41	46	
	2-3	0.220	0.33	40	50			

Notes

- All test data is referenced to 25 °C ambient
- Test condition: 100 kHz, 1 V
- Operating temperature range -40 °C to +125 °C
- Storage condition: -40 °C to +125 °C (on board); less than 40 °C and < 60 % RH (in component packaging)
- $^{(1)}$ DC current (A) that will cause an approximate ΔT of 40 $^{\circ}C$
- (2) DC current (A) that will cause L₀ to drop approximately 20 % at 25 °C, 100 °C, and 125 °C ambient

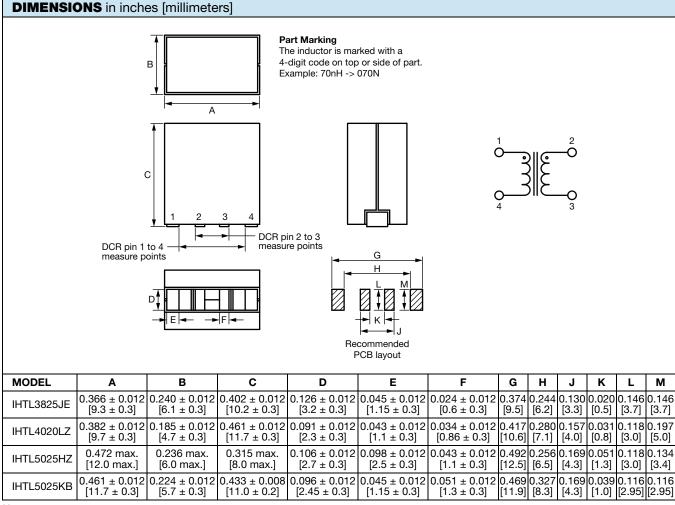


STANDARD ELECTRICAL SPECIFICATIONS								
		L ₀ INDUCTANCE	DCR	HEAT RATING		N CURRENT P. (A) ⁽²⁾	Q FACTOR	
PART NUMBER	TEST TERMINAL	AT 0 A ± 15 % (μH)	± 10 % (mΩ)	CURRENT DC TYP. (A) (1)	AT 25 °C	AT 100 °C	AT 100 kHz MIN.	
10.0 mm x 5.0 mm x 12	2.0 mm MAX.							
IHTL4020LZEZ70NL	1-4	0.070	0.125	75	127	110	15	
	2-3		0.45	35				
IHTL4020LZEZ80NL	1-4	0.080	0.125	75	111	96	17	
	2-3	0.080	0.45	35				
IHTL4020LZEZ90NL	1-4	0.090	0.125	75	98	85	19	
IIII L4020LZLZ90INL	2-3		0.45	35				
12.0 mm x 6.0 mm x 8.	0 mm MAX.							
IHTL5025HZEZ80NL	1-4	0.000	0.10	78	105	95	-	
IHTL3025HZEZ60INL	2-3	0.080	0.30	45				
IHTL5025HZEZR10L	1-4	0.100	0.10	78	85	75	-	
IHTL5025HZEZRTUL	2-3		0.30	45				
II ITI 6005117F7D101	1-4	0.120	0.10	78	70	63	-	
IHTL5025HZEZR12L	2-3		0.30	45				
12.0 mm x 6.0 mm x 11	I.2 mm MAX.							
	1-4	0.080	0.125	77	150	128	19	
IHTL5025KBEZ80NL	2-3		0.37	45				
ILITI FOOFIADEZDAOL	1-4	0.105	0.125	77	125	106	22	
IHTL5025KBEZR10L	2-3		0.37	45				
	1-4	0.120	0.125	77	102	87	23	
IHTL5025KBEZR12L	2-3		0.37	45				
IHTL5025KBEZR15L	1-4	0.150	0.125	77	84	71	26	
	2-3		0.37	45				
IHTL5025KBEZR17L	1-4	0.170	0.125	77	70	60	28	
	2-3		0.37	45				
ILITI FOOEKDEZDOO'	1-4	0.000	0.125	77	58	50	38	
IHTL5025KBEZR20L	2-3	0.200	0.37	45				

Notes

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- $^{(1)}$ DC current (A) that will cause an approximate ΔT of 40 $^{\circ}C$
- $^{(2)}$ DC current (A) that will cause L0 to drop approximately 20 % at 25 °C, 100 °C, and 125 °C ambient

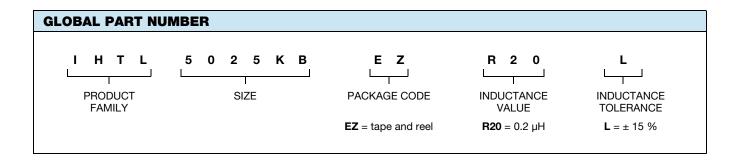




Note

• Coplanarity of four terminals: 0.004" = 0.1 mm max.

DESCRIPTION								
IHTL5025KB	0.2 μΗ	± 15 %	EZ	e3				
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC® LEAD (Pb)-FREE STANDARD				





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Vishay

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