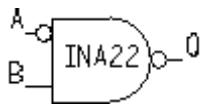


INA22 is a 2-input NAND gate with one inverted input 2x drive strength.

### Truth Table

A	B	Q
0	1	0
X	0	1
1	X	1



### Capacitance

Pin	Cap [pF]
A	0.008
B	0.018

### Area

0.141 mils<sup>2</sup>  
91  $\mu\text{m}^2$

### Power

0.832  $\mu\text{W}/\text{MHz}$

Delay [ns] = tpd.. = f(SL, L)

with SL = Input Slope [ns] ; L = Output Load [pF]

Output Slope [ns] = op\_sl.. = f(SL, L)

with L = Output Load [pF]

AC Characteristics: T<sub>j</sub> = 25°C VDD = 3.3V Typical Process

### AC Characteristics

Slope [ns]	Rise				Fall			
	0.1		2		0.1		2	
	0.03	0.3	0.03	0.3	0.03	0.3	0.03	0.3
Load [pF]								
Delay A => Q	0.25	0.71	0.5	0.95	0.23	0.63	0.42	0.82
Delay B => Q	0.11	0.57	0.35	0.87	0.11	0.49	0.15	0.68
Slew A => Q	0.31	1.81	0.35	1.82	0.22	1.1	0.29	1.12
Slew B => Q	0.32	1.84	0.73	2.07	0.18	1.08	0.6	1.38