

INS8216/8226 4-Bit Bidirectional Bus Transceivers

General Description

The INS8216 and INS8226 are four-bit bidirectional bus drivers for use in bus oriented applications. The non-inverting INS8216 and inverting INS8226 drivers are provided for flexibility in system design.

Each buffered line of the four-bit driver consists of two separate buffers that are TRI-STATE® to achieve direct bus interface and bidirectional capability. On one side of the driver the output of one buffer and the input of another are tied together (DB); this side is used to interface to the system side components such as memories, I/O, etc., because its interface is TTL compatible and it has high drive (50mA). On the other side of the driver the inputs and outputs are separated to provide maximum flexibility. Of course, they can be tied together so that the driver can be used to buffer a true bidirectional bus. The DO outputs on this side of the driver have a special high voltage output drive capability so that direct interface to the 8080 type CPUs is achieved with an adequate amount of noise immunity.

The CS input is a device enable. When it is "high" the output drivers are all forced to their high-impedance state. When it is a "low" the device is enabled and the direction of the data flow is determined by the DIEN input.

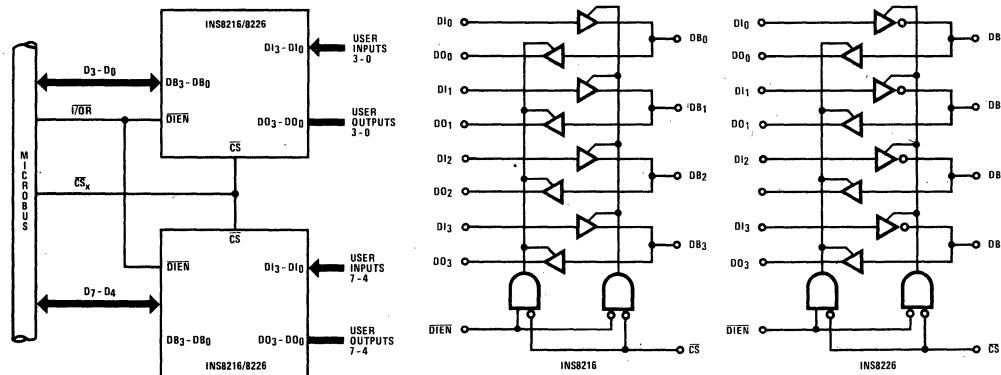
The DIEN input controls the direction of data flow, which is accomplished by forcing one of the pair of buffers into its high-impedance state and allowing the other to transmit its data. A simple two-gate circuit is used for this function.

Features

- Data bus buffer driver for 8080 type CPUs
- Low input load current — 0.25mA maximum
- High output drive capability for driving system data bus — 50mA at 0.5V
- Power up-down protection
- The INS8216 has non-inverting outputs.
- The INS8226 has inverting outputs.
- Output high voltage compatible with direct interface to MOS
- TRI-STATE outputs
- Advanced Schottky processing
- Available in military and commercial temperature ranges
- MICROBUSTM* compatible

INS8216/8226 MICROBUS Configuration

Logic Diagrams



*Trademark, National Semiconductor Corp.

Absolute Maximum Ratings (Note 1)

	Min	Max	Units
All Output and Supply Voltages	-0.5	+7.0	V
All Input Voltages	-1.0	+5.5	V
Output Currents	125	mA	
Lead Temperature (soldering, 10 seconds)	+300	°C	
Storage Temperature	-65	+150	°C
Power Dissipation*			
Cavity Package	1160	mW	
Molded Package	1000	mW	

*Derate Cavity Package at 80°C/W above 70°C; derate Molded Package at 90°C/W above 70°C.

Operating Conditions

	Min	Max	Units
Supply Voltage, V _{CC} INS8216, INS8226	4.75	5.25	V
Temperature, T _A INS8216, INS8226	0	+70	°C

DC Electrical Characteristics V_{CC} = 5V ± 5% (Notes 2, 3, and 4)

Symbol	Parameter	Conditions	Limits			Units
			Min	Typ	Max	
DRIVERS						
V _{IL}	Input Low Voltage				0.95	V
V _{IH}	Input High Voltage		2			V
I _F	Input Load Current	V _F = 0.45V		-0.03	-0.25	mA
I _R	Input Leakage Current	V _R = 5.25V			10	µA
V _C	Input Clamp Voltage	I _C = -5mA			-1.2	V
V _{OL1}	Output Low Voltage	I _{OL} = 25mA		0.3	0.45	V
V _{OL2}	Output Low Voltage	INS8216 — I _{OL} = 55mA INS8226 — I _{OL} = 50mA		0.5	0.6	V
V _{OH}	Output High Voltage	I _{OH} = -10mA	2.4	3.0		V
I _{SC}	Output Short Circuit Current	V _{CC} = 5.0V	-30	-75	-120	mA
I _{OL}	Output Leakage Current TRI-STATE	V _O = 0.45V/5.5V			100	µA
RECEIVERS						
V _{IL}	Input Low Voltage				0.95	V
V _{IH}	Input High Voltage		2			V
I _F	Input Load Current	V _F = 0.45V		-0.08	-0.25	mA
V _C	Input Clamp Voltage	I _C = -5mA			-1.2	V
V _{OL}	Output Low Voltage	I _{OL} = 15mA		0.3	0.45	V
V _{OH1}	Output High Voltage	I _{OH} = -1mA	3.65	4.0		V
I _{SC}	Output Short Circuit Current	V _O ≈ 0V	-15	-35	-65	mA
I _{OL}	Output Leakage Current TRI-STATE	V _O = 0.45V/5.5V			20	µA
CONTROL INPUTS (CS, DIEN)						
V _{IL}	Input Low Voltage				0.95	V
V _{IH}	Input High Voltage		2			V
I _F	Input Load Current	V _F = 0.45V		-0.15	-0.5	mA
I _R	Input Leakage Current	V _R = 5.25V			20	µA
I _{CC}	Power Supply Current INS8216 INS8226			95	130	mA
				85	120	mA

AC Electrical Characteristics (Notes 2, 3, and 4)

Symbol	Parameter	Conditions	Limits			Units
			Min	Typ	Max	
INS8216/8226 — $V_{CC} = 5.0V \pm 5\%$						
t_{PD1}	Input to Output Delay, DO Outputs	$C_L = 30pF, R_1 = 300\Omega, R_2 = 600\Omega$		15	25	ns
t_{PD2}	Input to Output Delay, DB Outputs INS8216 INS8226	$C_L = 300pF, R_2 = 90\Omega, R_2 = 180\Omega$		20 16	30 25	ns ns
t_E	Output Enable Time INS8216 INS8226	DO Outputs: $C_L = 30pF, R_1 = 300\Omega/10k\Omega, R_2 = 600\Omega/1k\Omega$ DB Outputs: $C_L = 300pF, R_1 = 90\Omega/10k\Omega, R_2 = 180\Omega/1k\Omega$		45 35	65 54	ns ns
t_D	Output Disable Time	$C_L = 5pF, R_1 = 300\Omega/10k\Omega, R_2 = 600\Omega/1k\Omega$ DB Outputs: $C_L = 5pF, R_1 = 90\Omega/10k\Omega, R_2 = 180\Omega/1k\Omega$		20	35	ns

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The tables of "Electrical Characteristics" provide conditions for actual device operation.

Note 2: Unless otherwise specified, min/max limits apply across the $0^\circ C$ to $+70^\circ C$ temperature range for the INS8216 and INS8226. All typical values are given for $V_{CC} = 5V$ and $T_A = 25^\circ C$.

Note 3: All currents into device pins are positive; all currents out of device pins are negative. All voltages are referenced to ground unless otherwise specified.

Note 4: Only one output at a time should be shorted.

Capacitance $T_A = 25^\circ C$

Symbol	Parameter	Limit			Units
		Min	Typ	Max	
C_{IN}	Input Capacitance		4	6	pF
C_{OUT}	Output Capacitance DO Outputs DB Outputs		6 13	10 18	pF

Note: This parameter is periodically sampled and is not 100% tested. Condition of measurement is $f = 1MHz$, $V_{BIAS} = 2.5V$, $V_{CC} = 5.0V$, and $T_A = 25^\circ C$.

Test Conditions

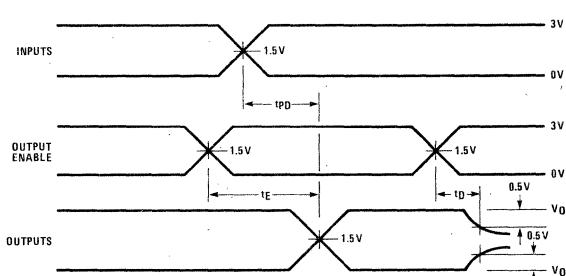
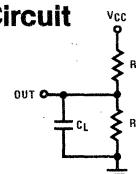
Input pulse amplitude of 2.5V.

Input rise and fall times of 5.0ns between 1.0V and 2.0V.

Output loading is 5.0mA and 10pF.

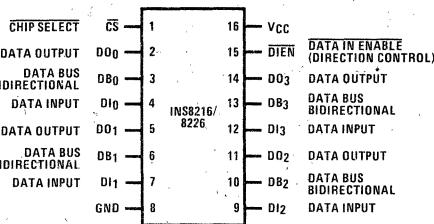
Speed measurements are made at 1.5V levels.

Test Load Circuit



D.2

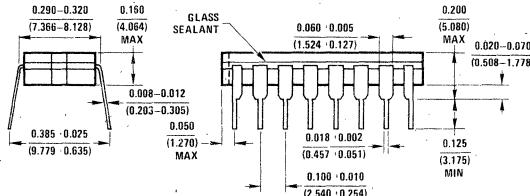
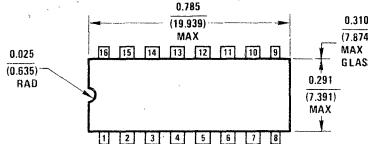
Pin Configuration



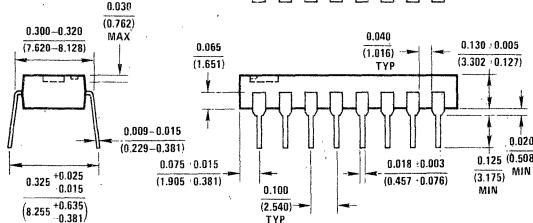
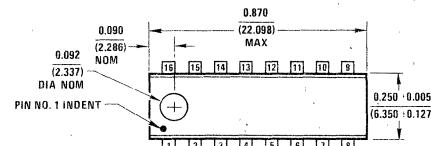
NOTE: THE INS8216/8226 ARE IDENTICAL TO THE DP8216/6226.

Physical Dimensions

inches (millimeters)



16-Lead Cavity DIP (J)
Order Numbers INS8216J, INS8226J
NS Package Number J16A



16-Lead Molded DIP (N)
Order Numbers INS8216N, INS8226N
NS Package Number N16A

Note: The INS8216/8226 are identical to the DP8216/8226.



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