

## Fast Charging Physical Layer IC for USB Interfaces

Supports 8 standards: Type-C, QC3.0/QC2.0, FCP, AFC, SFCP, Apple® 2.4A, BC1.2

### 1. Features

- Support several charging standards including : QC3.0/QC2.0, FCP, AFC, SFCP and Apple® 2.4A, Samsung® 2.0A, BC1.2
- Support USB Type-C DFP
  - CC1, CC2 output pull-up automatically
  - Support broadcast 3A maximum current
- Support QC3.0&QC2.0 Class B (Compatible with Class A)
  - QC3.0 Class B: 3.6V~20V (0.2V/step) output voltage
  - QC2.0 Class B: 5 V, 9 V, 12 V or 20V
- Support Huawei® FCP of 5V and 9V
- Support Samsung® AFC of 5V and 9V
- Support Spreadtrum® SFCP of 12V, 9V and 5V
- Support Apple® 2.4A: DP=2.7V, DM=2.7V
- Support Samsung® 2.0A: DP=1.2V, DM=1.2V
- Support BC1.2: Automatic shorting D+ to D- line
- SEL configure the maximum voltage, allowed to applied for, as 20V or 12V or 5V
- Default 5 V mode operation
- Support auto-detect and auto-switching fast charging standards
- FB for voltage regulation
- Working voltage: 3V~5.5V
- Very low power consumption  $I_Q = 66\mu\text{A}(\text{Typ.})$
- Package: SOP8

### 2. Typical Applications

- USB power output ports for AC adapters, Power Bank, Car chargers
- Battery chargers for smart phones, tablets, netbooks, digital cameras, and Bluetooth accessories

### 3. Description

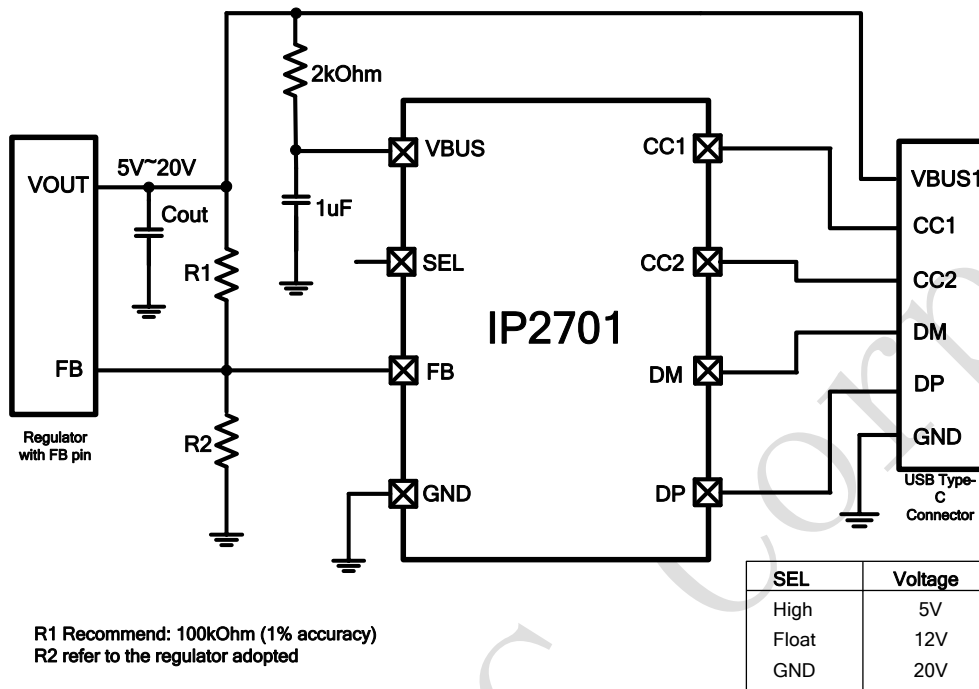
IP2701 is a low-cost fast charging Physical Layer IC dedicated for USB ports, which supports 8 kinds of fast charging standards, including Type-C DFP, HVDCP QC3.0/QC2.0 (Quick Charge) Class A&B, FCP (Hisilicon® Fast Charge Protocol), AFC (Samsung® Adaptive Fast Charge), SFCP (Spreadtrum® Fast Charge Protocol), Apple® 2.4A, BC1.2 and Samsung® 2.0A.

IP2701 integrated USB Type-C DFP port controller, which can co-work with Type-C UFP/DRP devices.

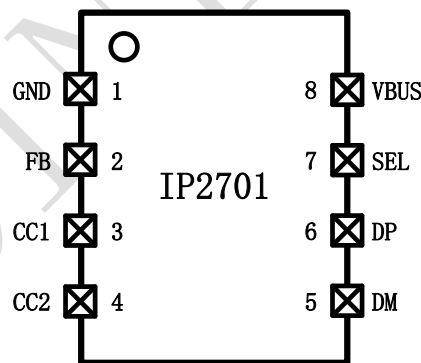
IP2701 support automatically detecting the connected device's type and switching standards type to responding for fast charging requirements.

FB control line is integrated to source/sink current with precise 2uA/step in minimum, for accurate voltage regulation.

## 4. Typical Application Schematic



## 5. PIN Description



Pin Name	Pin No.	Pin Description
GND	1	Ground
FB	2	Connect to the Regulator's FB line, current source/sink for voltage regulation
CC1	3	Connect to USB Type-C CC1 pin
CC2	4	Connect to USB Type-C CC2 pin
DP	5	Connect to USB DP data line
DM	6	Connect to USB DM data line
SEL	7	Configure the maximum voltage allowed to apply for: <ul style="list-style-type: none"> <li><math>V_{SELH}</math> for 5V output</li> <li>Floating for 12V output</li> </ul>

		• GND for 20V output
VBUS	8	Power supply input, connect with 1uF capacitor to GND, a resistor of 2kOhm should be applied between VOUT and VBUS

## 6. IP Series Products List

### Power Bank IC

IC Part No.	Charge /Discharge		Features							Package	
	Charge	Dis-charge	LED Num	Lighting	Keys	I2C	DCP	Type-C	QC Certificate	Package	Compa tibility
IP5303	1.0A	1.2A	1,2	√	√	-	-	-	-	eSOP8	PIN2PIN
IP5305	1.0A	1.2A	1,2,3,4	√	√	-	-	-	-	eSOP8	
IP5306	2.4A	2.1A	1,2,3,4	√	√	-	-	-	-	eSOP8	
IP5206	2A (Max)	1.5A	3,4,5	√	√	-	-	-	-	eSOP16	PIN2PIN
IP5108E	2.0A	1.0A	3,4,5	√	√	-	-	-	-	eSOP16	
IP5108	2.0A	2.0A	3,4,5	√	√	√	-	-	-	eSOP16	
IP5207	1.2A	1.2A	3,4,5	√	√	-	-	-	-	QFN24	PIN2PIN
IP5207T	1.2A	1.2A	1,2,3,4	√	√	√	√	-	-	QFN24	
IP5109	2.1A	2.1A	3,4,5	√	√	√	-	-	-	QFN24	
IP5209	2.4A	2.1A	3,4,5	√	√	√	√	-	-	QFN24	
IP5219	2.4A	2.1A	1,2,3,4	√	√	√	√	√	-	QFN24	
IP5310	3.1A	3.0A	1,2,3,4	√	√	√	√	√	-	QFN32	
IP5312	15W	3.6A	2,3,4,5	√	√	√	√	-	-	QFN32	
IP5318Q	18W	4.0A	2,3,4,5	√	√	√	√	-	√	QFN40	PIN2 PIN
IP5318	18W	4.0A	2,3,4,5	√	√	√	√	√	√	QFN40	
IP5322	18W	4.0A	1,2,3,4	√	√	√	√	-	√	QFN32	
IP5328	18W	4.0A	1,2,3,4	√	√	√	√	√	√	QFN40	

### USB Charging Port Control IC

IC Part No.	Channel Num	Standards Supported											Package
		BC1.2 & APPLE	QC3.0 & QC2.0	FCP	SCP	AFC	SFCP	MTK PE+ 2.0&1.1	Type-C	NTC	QC Certificate	PD3.0	
IP2110	1	√	-	-	-	-	-	-	-	-	-	-	SOT23-5
IP2111	1	√	-	-	-	-	-	-	-	-	-	-	SOT23-6
IP2112	2	√	-	-	-	-	-	-	-	-	-	-	SOT23-6
IP2161	1	√	√	-	-	-	√	-	-	-	√	-	SOT23-6
IP2163	1	√	√	√	-	√	√	√	-	√	√	-	SOP8
IP2701	1	√	√	√	-	√	√	-	√	-	-	-	SOP8
IP2703	1	√	√	√	-	√	√	√	√	√	-	-	DFN10

IP2705	1	√	√	√	-	√	√	√	√	√	-	-	DFN12
IP2707	2	√	√	√	-	√	√	√	√	√	-	-	QFN16
IP2716	1	√	√	√	√	√	-	1.1	√	-	√	√	QFN32

## 7. Absolute Maximum Ratings

Parameters	Symbol	Value	Unit
VBUS Input Voltage Range	VBUS	-0.3 ~ 7	V
DP, DM Input Voltage Range	V <sub>DP</sub> , V <sub>DM</sub>	-0.3~11	V
CC1,CC2 input Voltage Range	V <sub>CC1</sub> , V <sub>CC2</sub>	-0.3~12	V
Junction Temperature Range	T <sub>J</sub>	-40 ~ 150	°C
Storage Temperature Range	T <sub>stg</sub>	-60 ~ 150	°C
Ambient Temperature Range	T <sub>A</sub>	-40~150	°C
Human Body Model (HBM)	ESD	4	KV

\*Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to Absolute Maximum Rated conditions for extended periods may affect device reliability.

\*Voltages are referenced to GND unless otherwise noted.

## 8. Recommended Operating Conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit
Input Voltage	VBUS	3		5.5	V
Ambient Temperature	T <sub>A</sub>	-40		85	°C

\*Devices' performance cannot be guaranteed when working beyond those Recommended Operating Conditions.

## 9. Electrical Characteristics

Unless otherwise specified, T<sub>A</sub>=25°C, 4.5V ≤ VBUS ≤ 5.5V

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Quiescent Current	I <sub>Q</sub>	No load, VBUS=5V	50	66	100	uA
Startup Time	T <sub>S</sub>		8	10	12	ms
SEL Input High Voltage Range	V <sub>SELH</sub>		3	5	5.5	V
SEL Default Output Voltage	V <sub>SELO</sub>		1.35	1.5	1.65	V

## 10. Function Description

### Charging Standards

IP2701 is a high-voltage, fast charging Physical Layer IC dedicated for charging applications where charging standards required to be negotiated between USB ports. IP2701 is needed at the host-side, when the attached portable client-side device negotiate the power allotment from the power source host-side, IP2701 can auto-detect and respond to the those charging standards and may grant or deny the request based on the available voltage/current. IP2701 will inform the power source host-side to adjust the output voltage by FB line once charging request granted.

IP2701 support analysis several charging standards, including HVDCP QC3.0/QC2.0 (Quick Charge) Class A, FCP (Hisilicon® Fast Charge Protocol), AFC (Samsung® Adaptive Fast Charge), SFCP (Spreadtrum® Fast Charge Protocol), Apple® 2.4A, BC1.2 and Samsung® 2.0A.

IP2701 monitors the real-time voltage on DP line and DM line, when the attached device is not the fast charging type, IP2701 will change the voltage on the DP, DM line to fulfill the negotiation process. When fast charging client-side device connected, IP2701 auto-detect the type of charging standard and analysis the power requirements, source/sink current on FB line to grant the request voltage. When the output voltage is default 5V, FB line neither source nor sink current. IP2701 is not in control of the charging power loop, the actual charging loop and charging current is determined by the host-side power source and the client-side USB port device.

IP2701 integrated USB Type-C DFP port controller, support discharge to devices, at which moment CC1, CC2 will output 330uA current, for 3A maximum current capability broadcasting, IP2701 can work with Type-C UFP and DRP devices.

### SEL

SEL line is used to configure the maximum voltage allotment that can be request, when SEL line is pull up to high-voltage of  $V_{SELH}$ , the IP2701 will not respond to any fast charging requirements and output default 5V; When SEL line is floating, the maximum voltage allotment is 12V; When SEL line is pull down to GND, the maximum voltage allotment is 20V.

SEL	Voltage
High ( $V_{SELH}$ )	5V
Float	12V
GND	20V

### FB

IP2701 integrated FB control line used for accurate voltage regulation by source/sink current with precise 2uA/step in minimum. FB source 40uA current for 9V output voltage; FB source 70uA current for 12V output voltage; FB source 150uA current for 20V output voltage; when the output voltage is default 5V, FB neither source

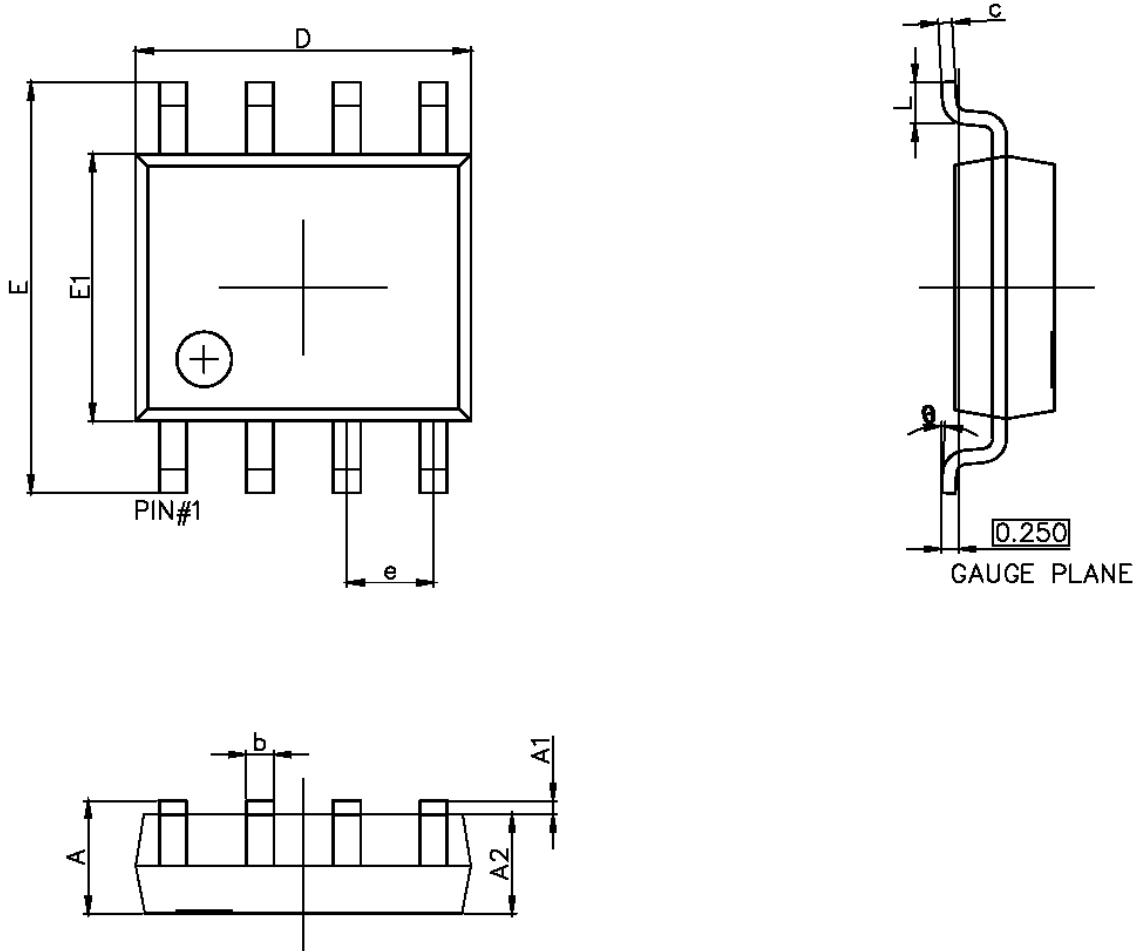
nor sink current.

In typical applications, IP2701 FB connects to the regulator's FB line, resistor (R1) between VOUT and FB should apply 100kOhm with high precision (1%), resistor (R2) value between FB and GND should refer to the regulator adopted, resistance of R2 can be calculated by equation:

$$V_{FB} = \frac{V_{OUT}}{R1+R2} * R2$$

INJOINIC Corp.

## 11. Package



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.800	5.000	0.189	0.197
e	1.270 (BSC)		0.050 (BSC)	
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
L	0.400	1.270	0.016	0.050
$\theta$	0°	8°	0°	8°

## 12.IMPORTANT NOTICE

INJOINIC TECHNOLOGY and its subsidiaries reserve the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as “components”) are sold subject to INJOINIC TECHNOLOGY's terms and conditions of sale supplied at the time of order acknowledgment.

INJOINIC TECHNOLOGY assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using INJOINIC TECHNOLOGY's components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

Buyer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of INJOINIC TECHNOLOGY's components in its applications, notwithstanding any applications-related information or support that may be provided by INJOINIC TECHNOLOGY. Buyer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences, lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Buyer will fully indemnify INJOINIC TECHNOLOGY and its representatives against any damages arising out of the use of any INJOINIC TECHNOLOGY's components in safety-critical applications.

Reproduction of significant portions of INJOINIC TECHNOLOGY's information in INJOINIC TECHNOLOGY's data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. INJOINIC TECHNOLOGY is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

INJOINIC TECHNOLOGY will update this document from time to time. The actual parameters of the product may vary due to different models or other items. This document voids all express and any implied warranties.

Resale of INJOINIC TECHNOLOGY's components or services with statements different from or beyond the parameters stated by INJOINIC TECHNOLOGY for that component or service voids all express and any implied warranties for the associated INJOINIC TECHNOLOGY's component or service and is an unfair and deceptive business practice. INJOINIC TECHNOLOGY is not responsible or liable for any such statements.