Fast Charging Physical Layer IC for USB Interfaces

TypeC PD2.0/PD3.1/PPS,QC3.0/QC2.0,FCP,SCP,AFC, Apple 2.4A, BC1.2

1. Features

- Support charging standards including :
  - Support QC4/QC4+
    - Compatible with QC2.0/QC3.0
    - Support Class B
  - USB PD2.0/PD3.1/PPS DFP
  - SCP, FCP
  - AFC
  - MTK PE+ 1.1&2.0
  - Apple 2.4A, Samsung 2.0A and BC1.2
- Support USB PD2.0/PD3.1/PPS
  - Auto detect USB PD device plug in or out
  - Configurable SRC_CAP package broadcast
- Support Samsung® AFC
- Support Huawei® SCP (option)
- Support Huawei® FCP
- Support Apple 2.4A: DP=2.7V, DM=2.7V
- Support Samsung 2.0A: DP=1.2V, DM=1.2V
- Support BC1.2: DP short DM automatically
- Default 5 V mode operation
- Support NTC protection
- Support 100KHz~400KHz IIC interface
- Support DP,DM,CC1,CC2 overvoltage protection
- Support DP,DM weak short to GND protection
- VIN working voltage: 3.3V~30V
- Package: QFN24

2. Description

IP2726H is a fast charging Physical Layer IC dedicated for USB ports, which supports 11 kinds of fast charging standards, including USB PD2.0/PD3.1 /PPS (Programmable Power Supply), HVDCP QC2.0/QC3.0 (Quick Charge), AFC (Samsung® Adaptive Fast Charge)，SCP (Hisilicon® Super Charge Protocol) and FCP (Hisilicon® Fast Charge Protocol), MTK PE+ 1.1&2.0，Apple 2.4A, Samsung 2.0A and BC1.2.

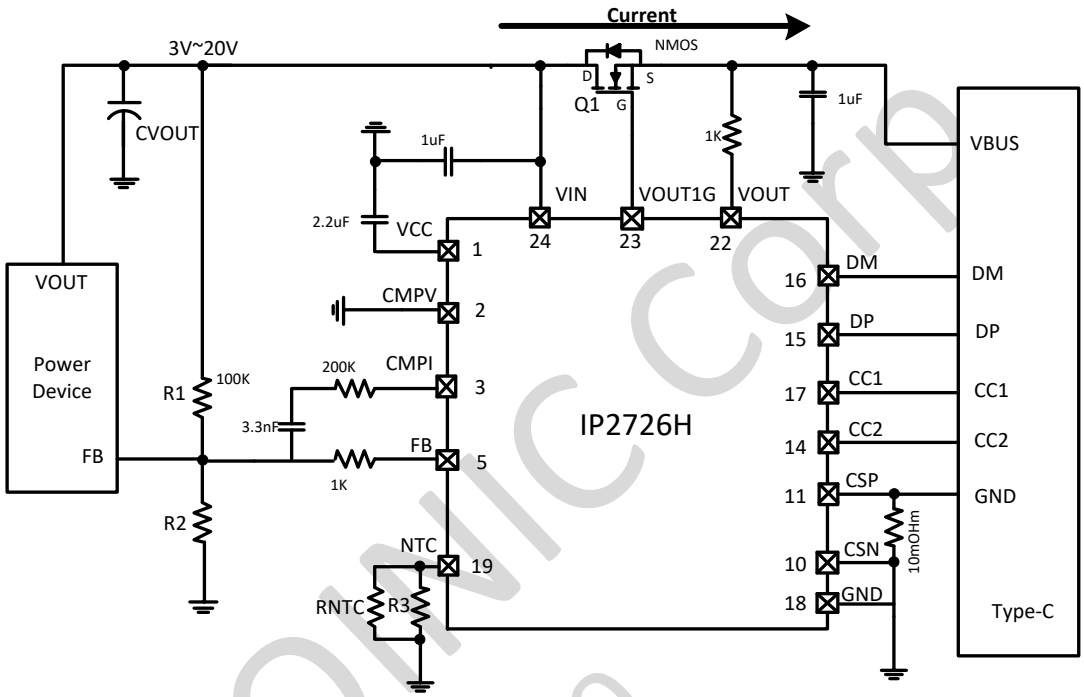
IP2726H support automatically detecting the connected device's type and switching standards type to responding for fast charging requirements.

3. Typical Applications

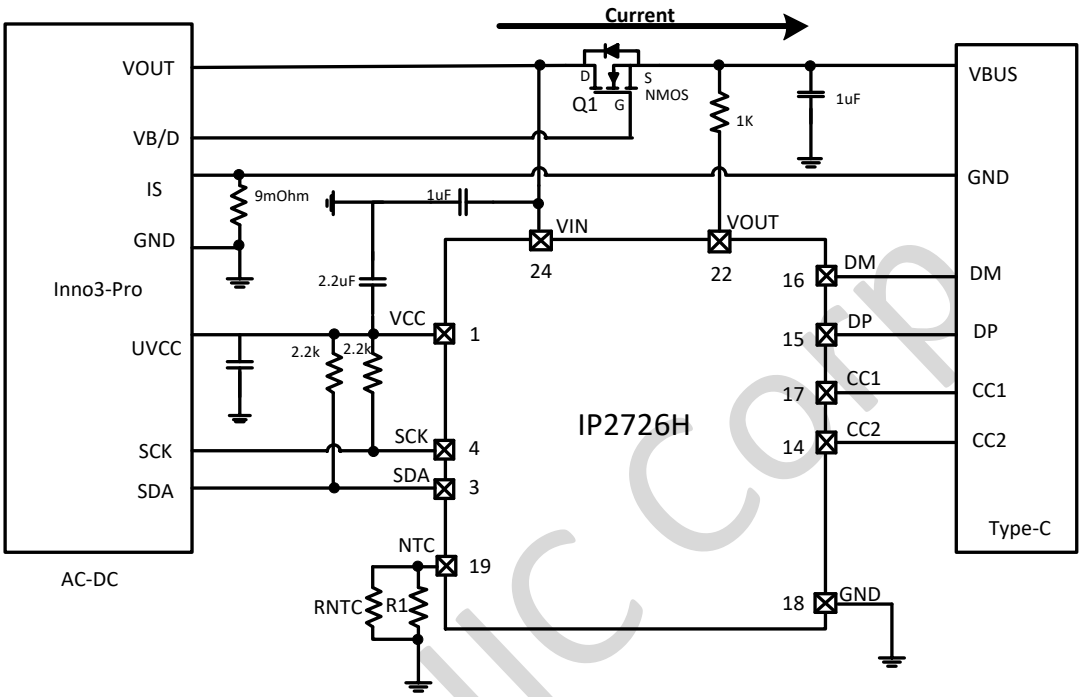
- USB power output ports for AC adapters, Power Banka, Car chargers
- Battery chargers for smart phones, tablets, netbooks, digital cameras, and Bluetooth accessories

4. Typical Application Schematic

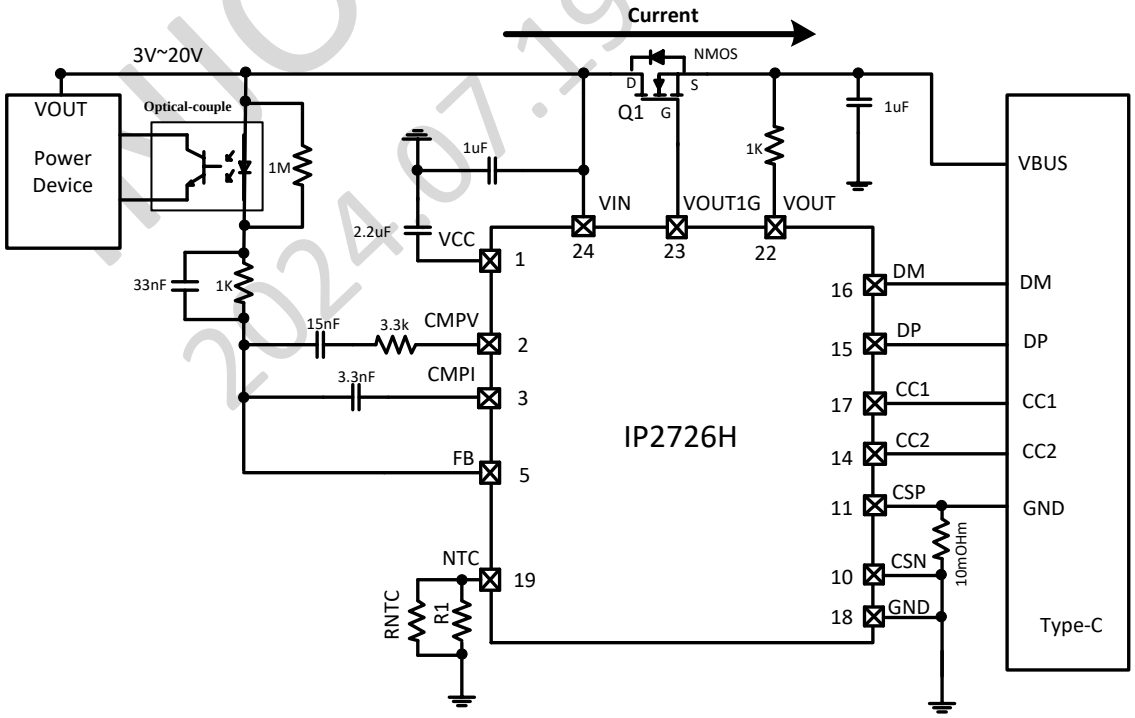
FB MODE



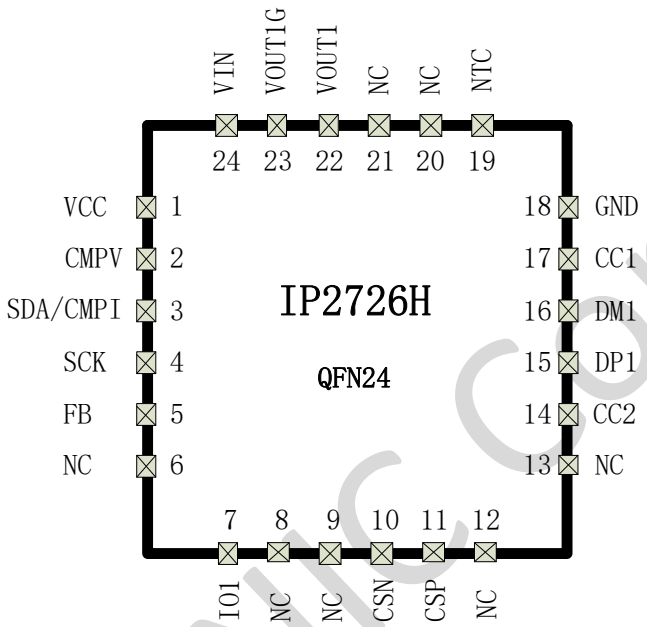
I2C MODE



Optocoupler MODE



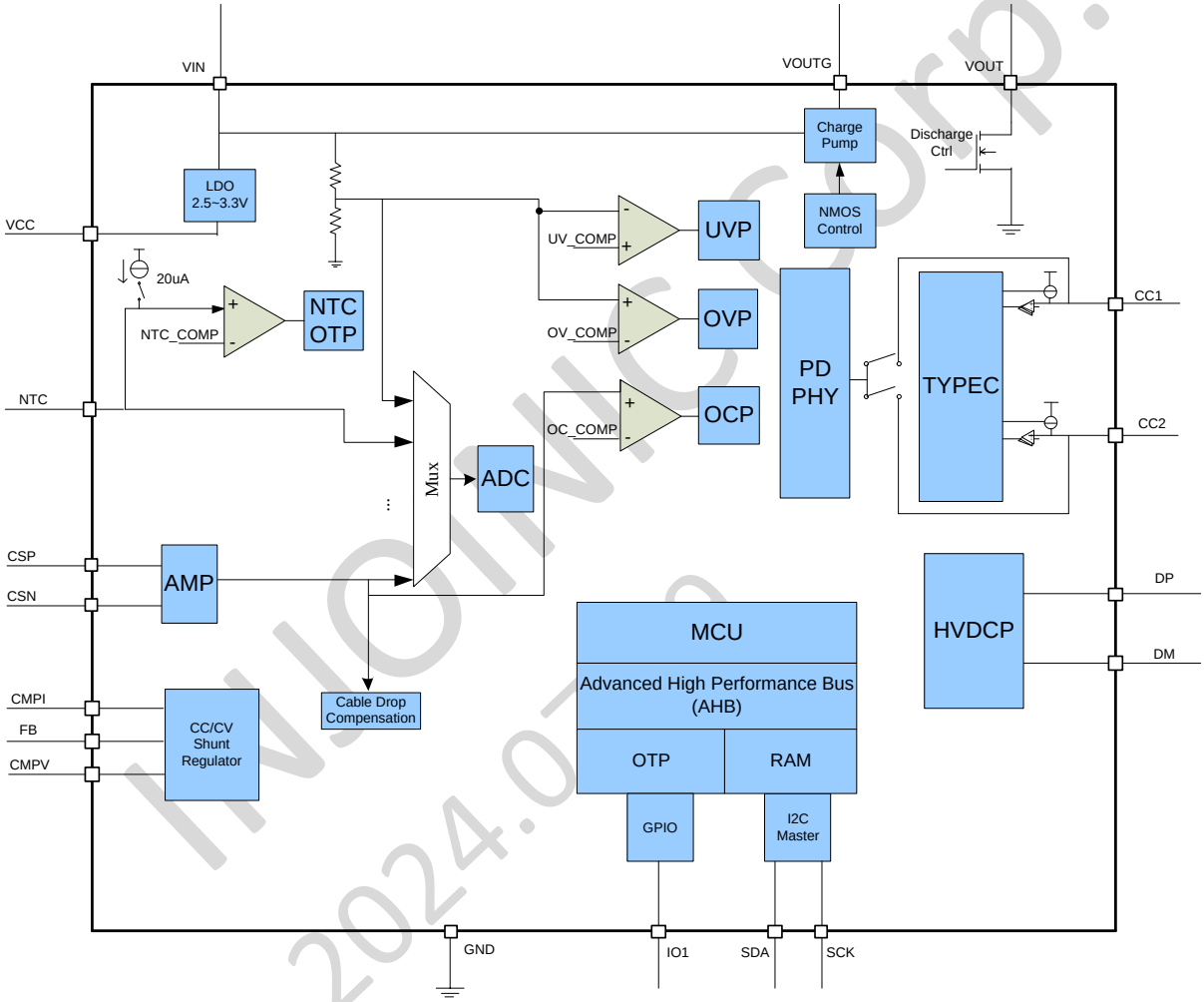
5. PIN Description



Pin No.	Pin name	Pin description
1	VCC	VCC Internal power supply output, need external 2.2 uF capacitance
2	CMPV	Feedback loop compensation of voltage
3	CMPI/SDA	Feedback loop compensation of current/I2C data
4	SCK	I2C clock
5	FB	Feedback PIN. Connect to the FB line of Regulator to the power device with FB control, current source/sink for voltage regulation
6	NC	Not connect
7	GPIO1	GPIO1
8	NC	Not connect
9	NC	Not connect
10	CSN	Current sense negative PIN
11	CSP	Current sense positive PIN
12	NC	Not connect
13	NC	Not connect
14	CC2	Type-C CC2 line
15	DP	USB DP data line
16	DM	USB DM data line
17	CC1	Type-C CC1 line
18	GND	ground
19	NTC	NTC Resistor input for temperature sense
20	NC	Not connect

21	NC	Not connect
22	VOUT	VOUT discharge pin
23	VOUTG	VOUT output path control on the NMOS
24	VIN	Power input

6. Internal Block Diagram



7. Absolute Maximum Ratings

Parameters	Symbol	Value	Unit
VIN Input Voltage Range	VIN	-0.3 ~ 30	V
VOUT Input Voltage Range	VOUT	-0.3 ~ 30	V
VOUTG Input Voltage Range	VOUTG	-0.3 ~ 30	V
VCC Input Voltage Range	VCC	-0.3 ~ 6	V
DP, DM Input Voltage Range	V_{DP}, V_{DM}	-0.3~25	V
CC1,CC2 Input Voltage Range	V_{CC1}, V_{CC2}	-0.3~30	V
Other Pins Input Voltage Range	V_{other}	-0.3~6	V
Junction Temperature Range	T_J	-40 ~ 150	℃
Storage Temperature Range	T_{STG}	-60 ~ 150	℃
Lead Temperature Range (Soldering, 10sec)	T_s	260	℃
Package Thermal Resistance	θ_{JA}	90	℃
Package Thermal Resistance	θ_{JC}	39	℃
Human Body Model (HBM)	ESD	2	KV

*Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device.

Exposure to Absolute Maximum Rated conditions for extended periods may affect device reliability.

*Voltages are referenced to GND unless otherwise noted.

8. Recommended Operating Conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit
Input Voltage	VIN	3		25	V
Ambient Temperature	T_A	-40		85	℃

*Devices' performance cannot be guaranteed when working beyond those Recommended Operating Conditions.

9. Electrical Characteristics

Unless otherwise specified, T A =25℃

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Input Voltage	VIN	Supplied directly	3		25	V
Input UVLO Threshold	UVLO	VIN/VCC Falling	2.5		2.9	V
Quiescent Current	I_Q	No load, VIN=5V		1		mA
		No load, VIN=20V		5		mA

Startup Time	Ts		20	37	50	us
HVDCP (QC2.0&QC3.0)						
Data Detect Voltage Threshold	V_{DATA_REF}		0.25	0.325	0.4	V
Output Voltage Selection Reference	V_{SEL_REF}		1.8	2	2.2	V
DP High Glitch Filter Time	$T_{GLITCH(BC)\_DP\_H}$		1000	1250	1500	ms
DM Low Glitch Filter Time	$T_{GLITCH(BC)\_DM\_L}$			2		ms
Output Voltage Glitch Filter Time	$T_{GLITCH(V)\_CHANGE}$		20	40	60	ms
Continuous Mode Glitch Filter Time	$T_{GLITCH\_CONT\_CHANGE}$		100		200	us
DM and DP Short Resistance	R_{SHORT}	V_{DP}=0.6V		30		Ohm
DM Pull-down Resistance	R_{DM_DOWN}	V_{DP}=0.6V		20		kOhm
DP Pull-down Resistance	R_{DAT_LKG}	V_{DP}=0.6V		500		kOhm
DCP						
Samsung DP/DM Output Voltage			1.08	1.2	1.32	V
Samsung DP/DM Output Impedance				100		kOhm
Apple 2.4A DP/DM Output Voltage			2.64	2.7	2.76	V
Apple 2.4A DP/DM Output Impedance				30		kOhm

10.Function Description

DP/DM Quick Charge

IP2726H can automatically detects Quick Charge 2.0/3.0 capable devices with handshake by USB D+/D- data line. It's also complaint with BC1.2.

- BC1.2: Shorting D+ Line to D- Line.
- Quick Charge 2.0/3.0: D+ and D- line configuration see **Table 1**.

Table 1 QC2.0/3.0 DP/DM configuration:

DP	DM	Result(Class A)	Result(Class B)
0.6 V	GND	5 V	5 V

3.3 V	0.6 V	9 V	9 V
0.6 V	0.6 V	12 V	12 V
0.6 V	3.3 V	Continue Mode	Continue Mode
3.3 V	3.3 V	Keep	20V

TYPE-C /PD

The USB Type-C connector uses CC pins for configuration including the ability for a Source to advertise to its port partner (Sink) the amount of current it can apply:

- Default values defined by the USB Specification (500 mA for USB 2.0 ports, 900 mA for USB 3.1 ports)
- 1.5A
- 3.0A

Integrated with certification standard USB Power Delivery (PD) controller

Support PD2.0/PD3.1/PPS

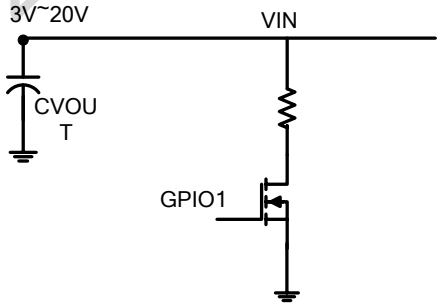
Discharge

When the output voltage is adjusted from high voltage to low voltage, especially from 20V to 5V, it requires a discharge current to fulfill the transition time specification of PD, and it cannot be greater than 285mS.

The internal 150mA(Max) pull-down current is turned on, when the output voltage needs to be quickly discharged.

If the capacitance is relatively large and the internal bleeder is not enough, VOUTD/GPIO1 can control the external NMOS to strengthen the bleed.

The resistance value is based on the actual situation.



Voltage regulation mode

➢ FB Mode

IP2726H integrated FB control line used for accurate voltage regulation by source/sink current with precise 2uA/step in minimum. FB sink 40uA current for 9V output voltage; FB sink 70uA current for 12V output voltage; FB sink 150uA current for 20V output voltage; when the output voltage is default 5V, FB neither source nor sink current.

In typical applications, IP2726H FB connects to the regulator's FB line, resistor (R1) between VOUT and FB should apply 100kOhm with high precision (1%), resistor (R2) value between FB and GND should refer to the regulator adopted, resistance of R2 can be calculated by equation:

$$VFB = \frac{VOUT}{R1 + R2} * R2$$

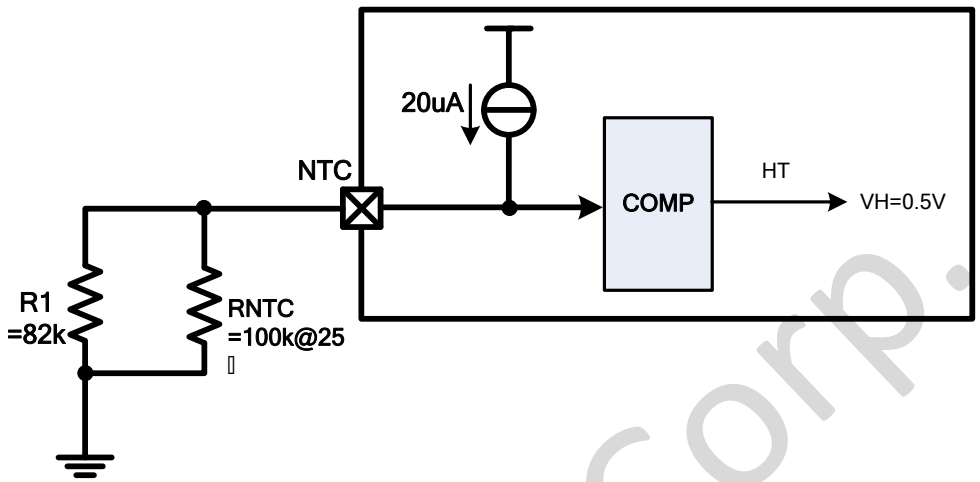
➢ Optocoupler Mode

IP2726H integrates an optocoupler driver circuit, and the FB pin is connected to the Cathode end of the optocoupler for use in isolated power supplies. The value of external compensation network capacitance, resistance of CMPV and CMPI needs to be adjusted according to the specific parameters of the front-end power network, The CMPV compensation network is used to achieve VOUT output voltage stability. The CMPI compensation network is used to achieve stability of power control.

➢ I2C Mode

IP2726H integrate I2C master control interface, can cooperate with the power supply chip, such as Inno3 pro etc.

NTC



IP2726H support NTC function used for temperature detection. NTC pin output 20uA current then detect the voltage on NTC pin to determine the present temperature.

OVP and UVP

IP2726H realizes over-voltage and under-voltage protection functions by detecting the voltage of VIN. The over-voltage threshold (100%~140%) and the under-voltage threshold (0~100%) can be flexibly set according to the percentage of the current output voltage. When an abnormal event of over-voltage or under-voltage occurs, the Gate will be pulled low and the external path NMOS transistor will be closed. If the abnormal event disappears, the Type-C handshake and PD communication will be re-established..

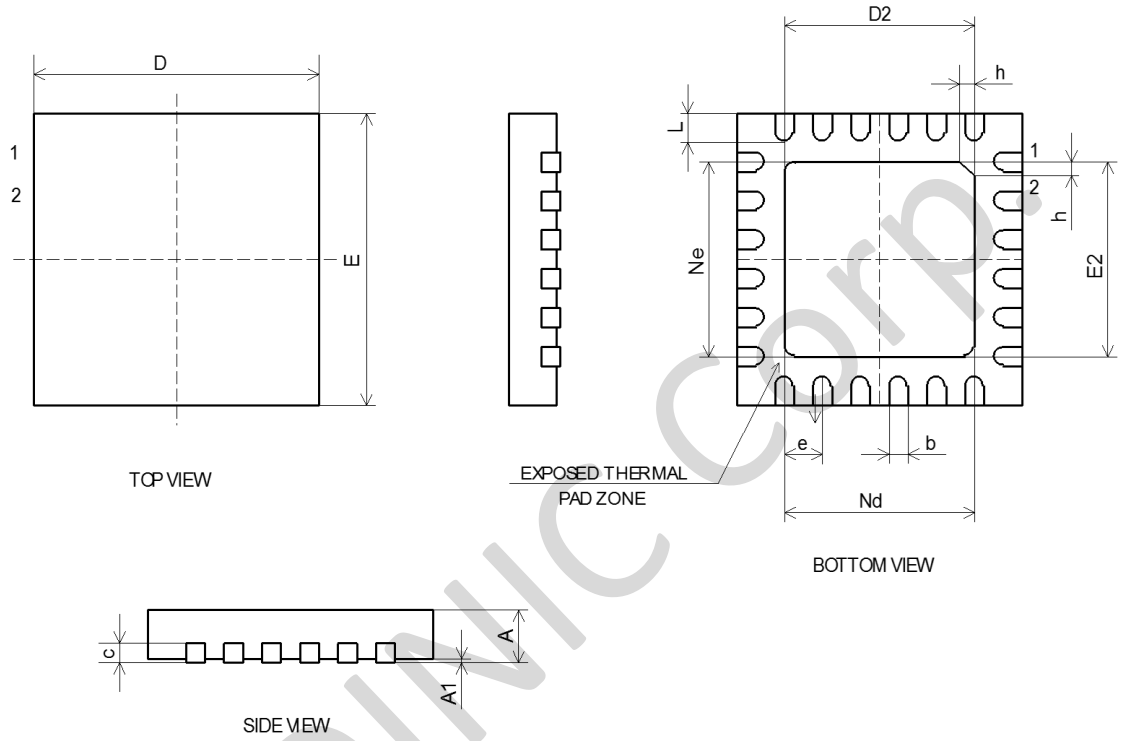
OTP

IP2726H integrates internal junction temperature OTP protection. Junction temperature protection occurs when the junction temperature exceeds 135°C

OCP

IP2726H realizes OCP overcurrent protection by detecting the current flowing through the sampling resistor. The overcurrent threshold can be flexibly set (100%~140%). When the current reaches the over-current threshold, the over-current abnormal protection occurs, and the Gate will be pulled low, closing the external path NMOS transistor. If the abnormal event disappears, the Type-C handshake and PD communication will be re-established.

11.Package



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	-	0.02	0.05
b	0.18	0.25	0.30
c	0.18	0.20	0.25
D	3.90	4.00	4.10
D2	2.40	2.50	2.60
e	0.50BSC		
Ne	2.50BSC		
Nd	2.50BSC		
E	3.90	4.00	4.10
E2	2.40	2.50	2.60
L	0.35	0.40	0.45
h	0.30	0.35	0.40

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