

INCHANGE SEMICONDUCTOR

isc N-Channel MOSFET Transistor IPD50R650CE, IIPD50R650CE



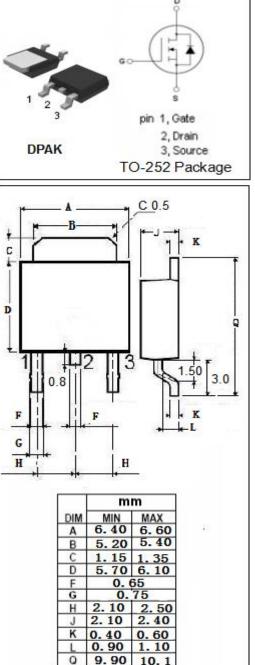
- Static drain-source on-resistance: Ros(on)≤650mΩ
- Enhancement mode:
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation
- DESCRITION
- · Fast switching

• ABSOLUTE MAXIMUM RATINGS/T=25°C

• ABSOLUTE MAXIMUM RATINGS(Ta=25 C)							
SYMBOL	PARAMETER	VALUE	UNIT				
V _{DSS}	Drain-Source Voltage	500	V				
V_{GS}	Gate-Source Voltage	±20	V				
lь	Drain Current-Continuous	9	А				
I _{DM}	Drain Current-Single Pulsed	19	А				
PD	Total Dissipation @T _c =25°C	69	W				
Tj	Max. Operating Junction Temperature	150	Ĉ				
T _{stg}	Storage Temperature	-55~150	°C				

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	МАХ	UNIT
Rth(j-c)	Channel-to-case thermal resistance	1.81	°C /W
Rth(j-a)	Rth(j-a) Channel-to-ambient thermal resistance		°C/W



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ELECTRICAL CHARACTERISTICS

T_c=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	ТҮР	МАХ	UNIT
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V; I _D =1mA	500			V
V _{GS(th)}	Gate Threshold Voltage	VDS=VGS; I _D =150 µ A	2.5		3.5	V
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} =13V; I _D =1.8A			650	mΩ
I _{GSS}	Gate-Source Leakage Current	V _{GS} =20V			0.1	μA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =500V; V _{GS} = 0V			1	μA
V _{SD}	Diode forward voltage	I _F =2.3A, V _{GS} = 0V		0.84		V

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