μHVIC™ SOT-23 Gate Driver IC

Features

- Wide VCC range (5V to 20V)
- CMOS Schmitt-triggered inputs
- Under voltage lockout
- 3.3V logic compatible
- Enable input
- Output in phase with inputs
- Leadfree, RoHS compliant

Product Summary

Topology	General Driver
IO+/- (typical)	1.5A

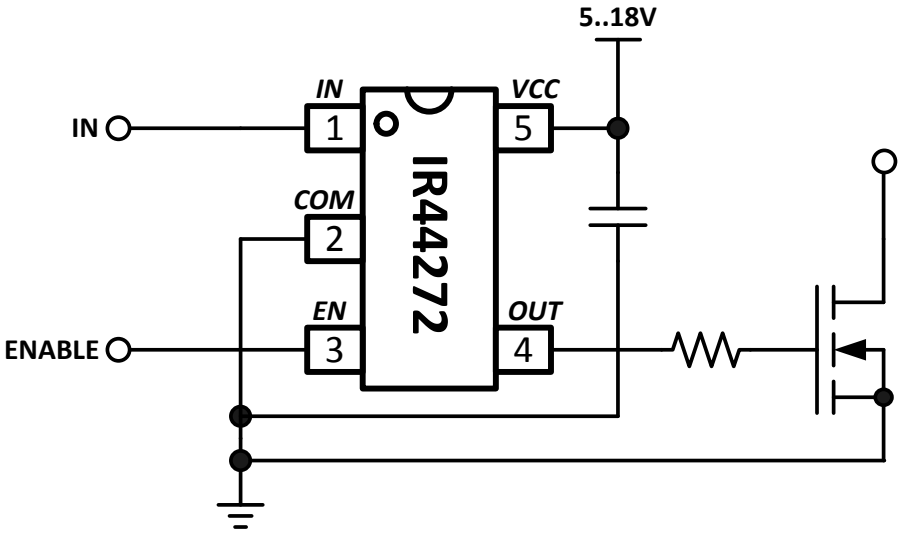
Package Options

5 Lead SOT23

Typical Applications

- General purpose gate driver
- Industrial applications
- Switched-mode power supplies

Typical Connection Diagram



Ordering Information

Base Part Number	Package Type	Standard Pack		Orderable Part Number
		Form	Quantity	
IR44272LPBF	SOT23-5	Tape and Reel	3000	IR44272LTRPBF

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Description

The IR44272L is a low-voltage, wide VCC range, power MOSFET and IGBT non-inverting gate driver. Proprietary latch immune CMOS technologies enable ruggedized monolithic construction. The logic input is compatible with standard CMOS or LSTTL output. The output driver features a current buffer stage. The design also includes an enable input with internal pull up.

Qualification Information†

Qualification Level		Industrial††
		Comments: This family of ICs has passed JEDEC's Industrial qualification. IR's Consumer qualification level is granted by extension of the higher Industrial level.
Moisture Sensitivity Level		MSL1††† 260°C (per IPC/JEDEC J-STD-020)
ESD	Machine Model	Class B (per JEDEC standard JESD22-A115)
	Human Body Model	Class 2 (per EIA/JEDEC standard EIA/JESD22-A114)
IC Latch-Up Test		Class 1 Level A (per JESD78)
RoHS Compliant		Yes

† Qualification standards can be found at International Rectifier's web site http://www.irf.com/

†† Higher qualification ratings may be available should the user have such requirements. Please contact your International Rectifier sales representative for further information.

††† Higher MSL ratings may be available for the specific package types listed here. Please contact your International Rectifier sales representative for further information.

Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. The device may not function or not be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. All voltage parameters are absolute voltages referenced to COM. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions.

Symbol	Definition	Min	Max	Units
V_{CC}	Fixed supply voltage	-0.3	20	V
V_O	Output voltage	-0.3	V_{CC} + 0.3	
V_{IN}	Logic input voltage	-0.3	V_{CC} + 0.3	
Rth_{JA}	Thermal resistance, junction to ambient	—	151	°C/W
T_J	Junction temperature	—	150	°C
T_S	Storage temperature	-55	150	
T_L	Lead temperature (soldering, 10 seconds)	—	300	

Recommended Operating Conditions

For proper operation, the device should be used within the recommended conditions. All voltage parameters are absolute voltages referenced to COM unless otherwise stated in the table.

Symbol	Definition	Min	Max	Units
V_{CC}	Fixed supply voltage	5.0	18	V
V_O	Output voltage	0	V_{CC}	
V_{IN}	Logic input voltage (IN and EN)	0	V_{CC}	
T_A	Ambient temperature	-40	125	°C

Static Electrical Characteristics

V_{CC} = 15V, T_A = 25°C unless otherwise specified. The $V_{IN,}$ and I_{IN} parameters are referenced to COM and are applicable to input leads: IN. The V_O and I_O parameters are referenced to COM and are applicable to the output leads: OUT.

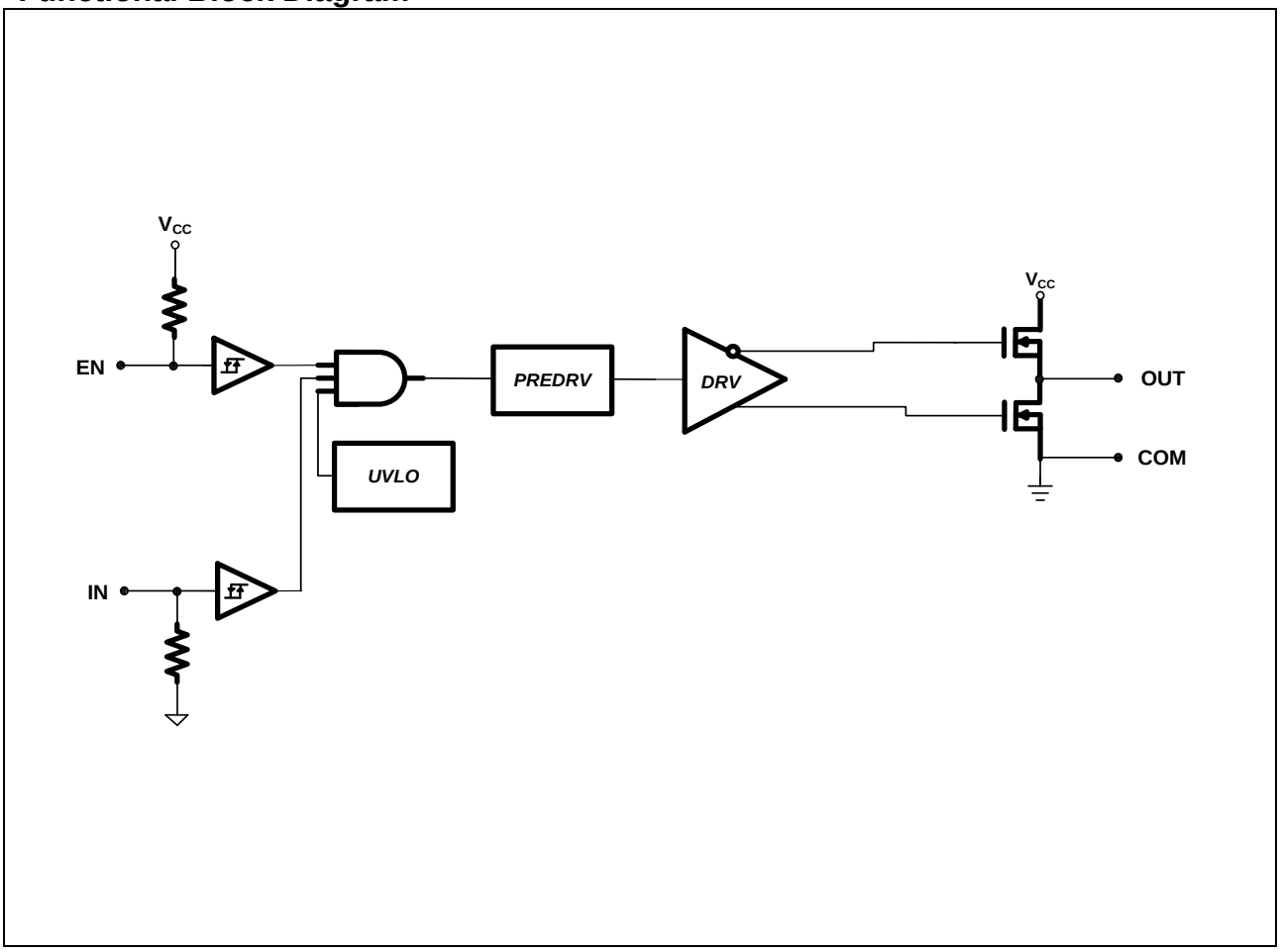
Symbol	Definition	Min	Typ	Max	Units	Test Conditions
V_{CCUV+}	Vcc supply UVLO positive going threshold	—	—	5.0	V	
V_{CCUV-}	Vcc supply UVLO negative going threshold	4.15	—	—		
$V_{CC\ UVH}$	Vcc supply UVLO hysteresis	—	0.3	—		
V_{CLAMP}	Vcc Zener clamp voltage	—	21.4	—		I_{CC}=5mA
V_{IL}	Logic “0” input voltage (OUT = LO)	—	—	0.6		
V_{IH}	Logic “1” input voltage (OUT = HI)	2.7	—	—		
V_{EN+}	EN input rising threshold	—	2.5	—		
V_{EN-}	EN input falling threshold	—	0.8	—		
V_{OH}	High level output voltage, V_{BIAS} -V_O	—	—	2.0		I_O = 0.1mA
V_{OL}	Low level output voltage, V_O	—	—	0.12		I_O = 20mA
I_{IN+}	Logic “1” input bias current	—	5	15	µA	V_{IN} = 5V
I_{IN-}	Logic “0” input bias current	-30	-10	—		V_{IN} = 0V
I_{QCC}	Quiescent V_{CC} supply current	—	—	400		V_{IN} = 0V or 5V
I_{O+}	Output high short circuit pulsed current	—	1.7	—	A	V_O = 0V, V_{IN} = 5V
I_{O-}	Output low short circuit pulsed current	—	1.5	—		V_O = 15V, V_{IN} = 0V

Dynamic Electrical Characteristics

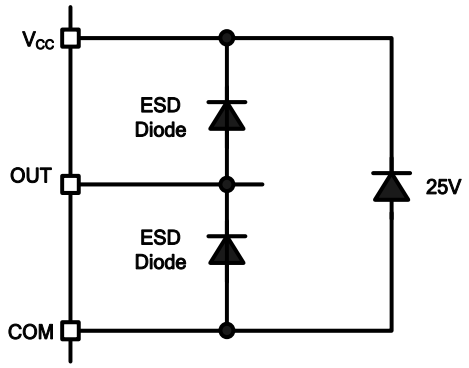
V_{CC} = 15V, T_A = 25°C, and C_L = 1000pF unless otherwise specified.

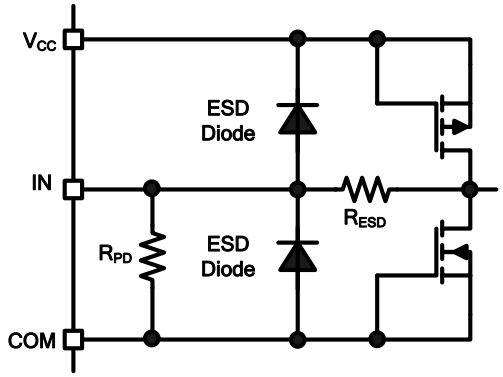
Symbol	Definition	Min	Typ	Max	Units	Test Conditions
t_{on}	Turn-on propagation delay	—	50	—	ns	Figure 2
t_{off}	Turn-off propagation delay	—	50	—		
t_r	Turn-on rise time	—	10	—		
t_f	Turn-off fall time	—	10	—		

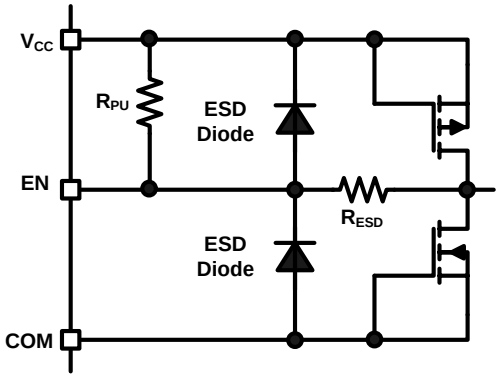
Functional Block Diagram



Input/Output Pin Equivalent Circuit Diagrams



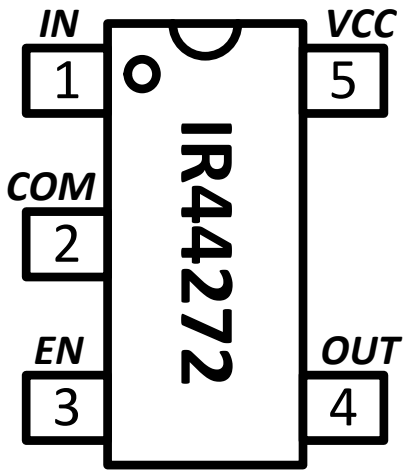




Lead Definitions

PIN	Symbol	Description
1	IN	Logic input for gate driver output (OUT), in phase
2	COM	Ground
3	EN	Enable input
4	OUT	Gate drive output
5	VCC	Supply Voltage

Lead Assignments



Timing Diagrams

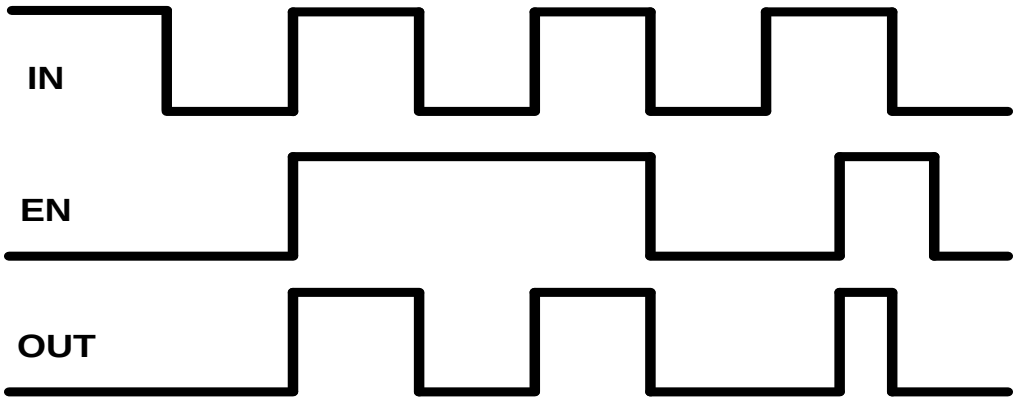


Figure 1: Input/output Timing Diagram

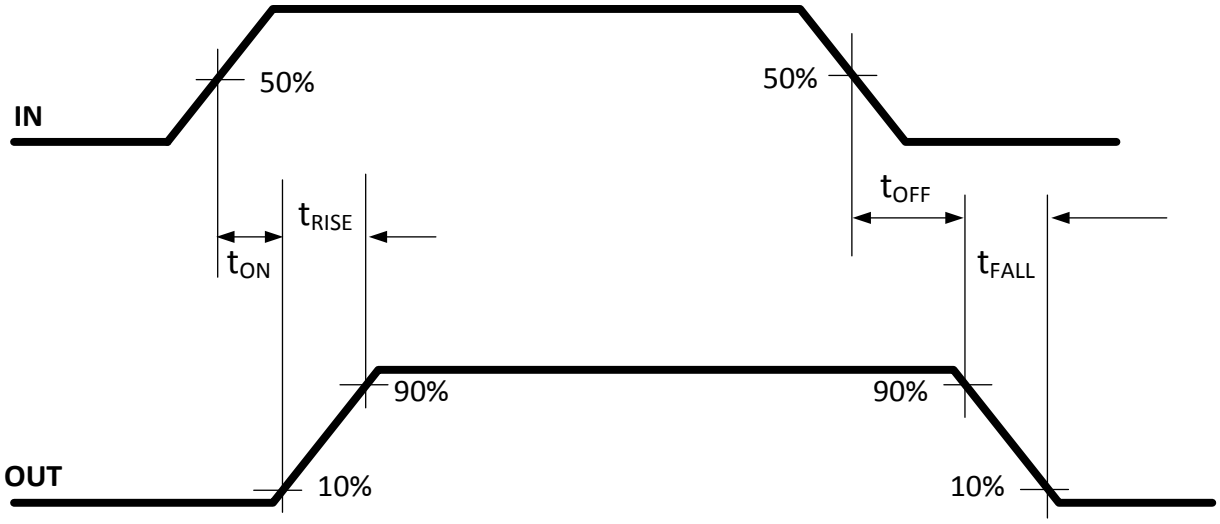
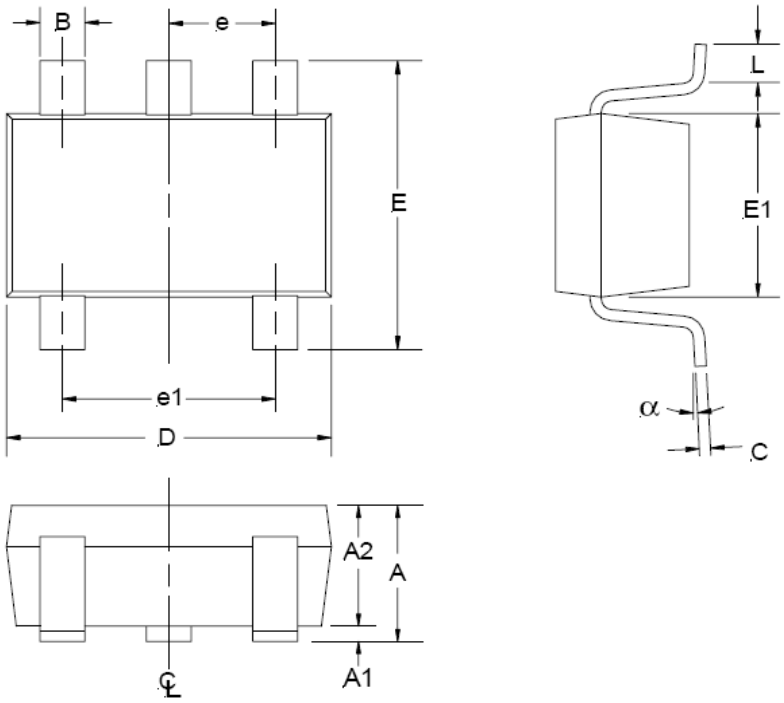


Figure 2: Switching Time Waveform Definitions

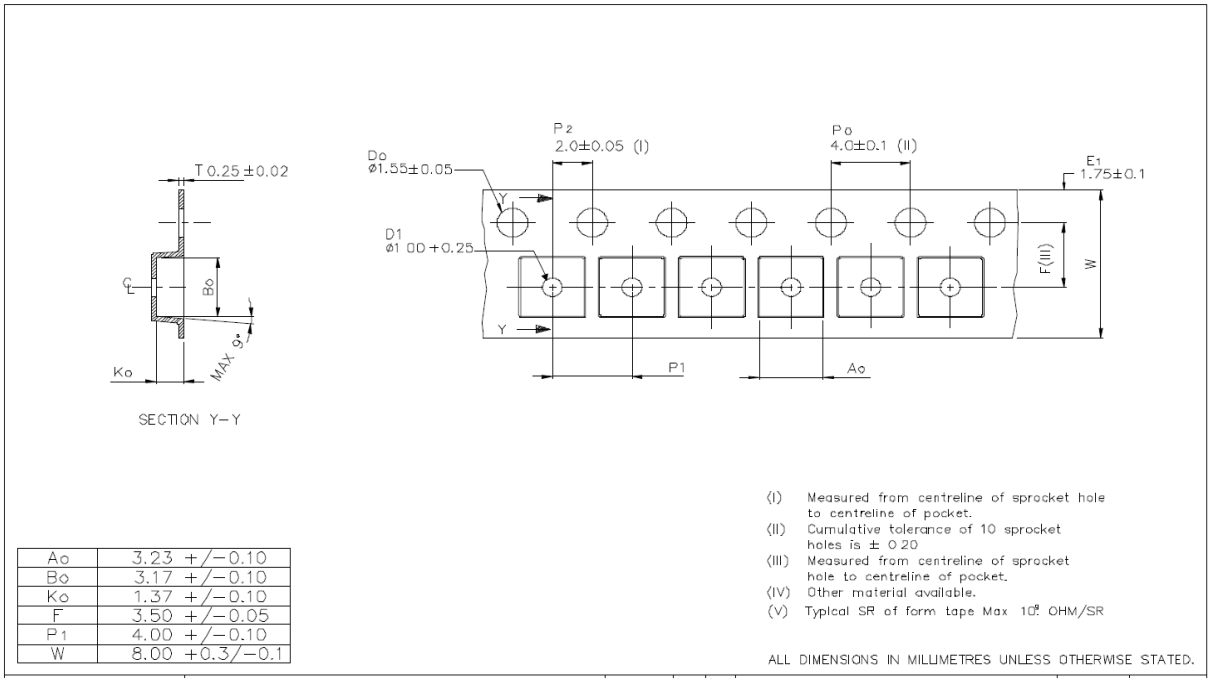
Package Details, SOT23-5



SYMBOL	MIN	MAX
A	0.90	1.45
A1	0.00	0.15
A2	0.90	1.30
B	0.25	0.50
C	0.09	0.20
D	2.80	3.00
E	2.60	3.00
E1	1.50	1.75
e	0.95 REF	
e1	1.90 REF	
L	0.35	0.55
α	08	108

NOTE: ALL MEASUREMENTS
ARE IN MILLIMETERS.

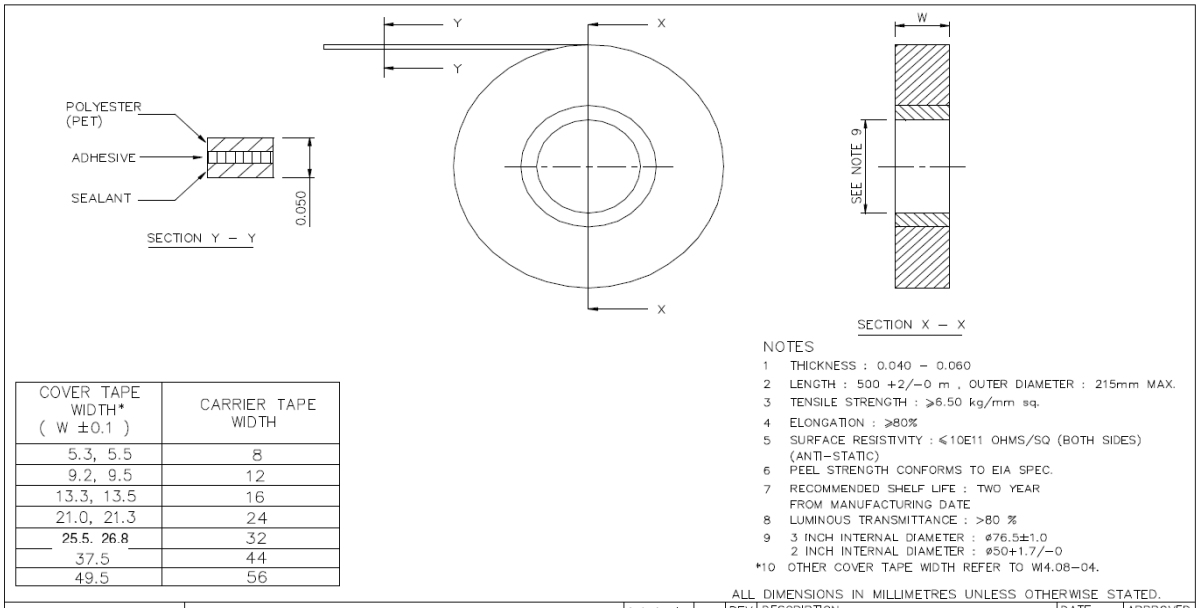
Package details: SOT23-5, Tape and Reel



Ao	3.23 +/−0.10
Bo	3.17 +/−0.10
Ko	1.37 +/−0.10
F	3.50 +/−0.05
P1	4.00 +/−0.10
W	8.00 +0.3/−0.1

(I) Measured from centreline of sprocket hole to centreline of pocket.
(II) Cumulative tolerance of 10 sprocket holes is ± 0.20
(III) Measured from centreline of sprocket hole to centreline of pocket.
(IV) Other material available.
(V) Typical SR of form tape Max 10^9 OHM/SR

ALL DIMENSIONS IN MILLIMETRES UNLESS OTHERWISE STATED.



COVER TAPE WIDTH* (W ±0.1)	CARRIER TAPE WIDTH
5.3, 5.5	8
9.2, 9.5	12
13.3, 13.5	16
21.0, 21.3	24
25.5, 26.8	32
37.5	44
49.5	56

NOTES
1 THICKNESS : 0.040 – 0.060
2 LENGTH : 500 +2/−0 m , OUTER DIAMETER : 215mm MAX.
3 TENSILE STRENGTH : ⩾6.50 kg/mm sq.
4 ELONGATION : ⩾80%
5 SURFACE RESISTIVITY : ⩽10E11 OHMS/SQ (BOTH SIDES) (ANTI–STATIC)
6 PEEL STRENGTH CONFORMS TO EIA SPEC.
7 RECOMMENDED SHELF LIFE : TWO YEAR FROM MANUFACTURING DATE
8 LUMINOUS TRANSMITTANCE : >80 %
9 3 INCH INTERNAL DIAMETER : ø76.5±1.0
2 INCH INTERNAL DIAMETER : ø50+1.7/−0
*10 OTHER COVER TAPE WIDTH REFER TO W14.08–04.

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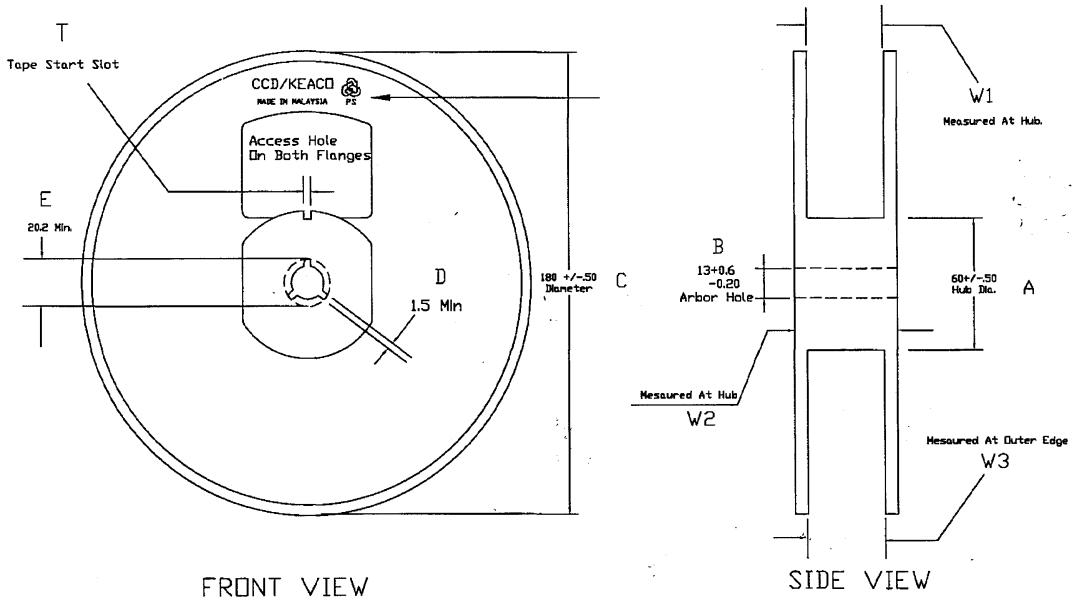
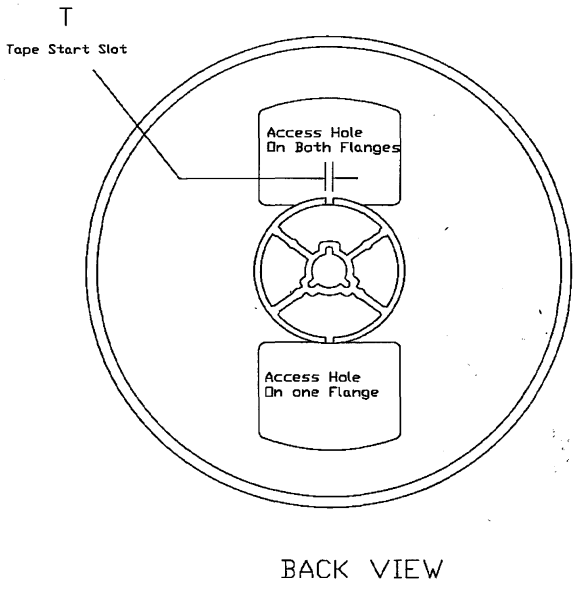
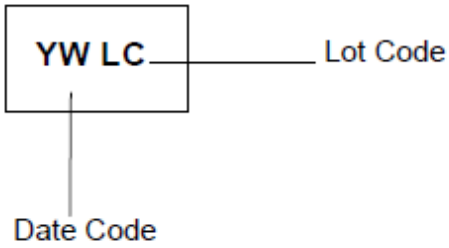




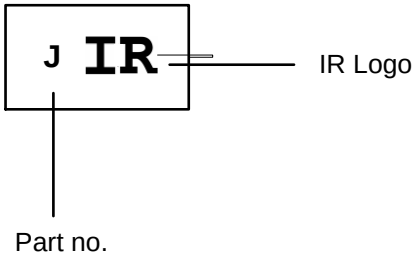
NOTE:
1. MATERIAL : POLYSTRENE
2. SURFACE RESISTIVITY : </= 10E11 OHMS/SQ
(EXTERNAL OR DIPPED)
3. STATIC DECAY : < 2SEC. AT 50%RH

Part Marking Information

Top Marking



Bottom Marking



For technical support, please contact IR's Technical Assistance Center
http://www.irf.com/technical-info/

WORLD HEADQUARTERS:
101N Sepulveda Blvd., El Segundo, California 90245
Tel: (310) 252-7105

