

### **INCHANGE SEMICONDUCTOR**

# isc N-Channel MOSFET Transistor

# **IRF1407L**

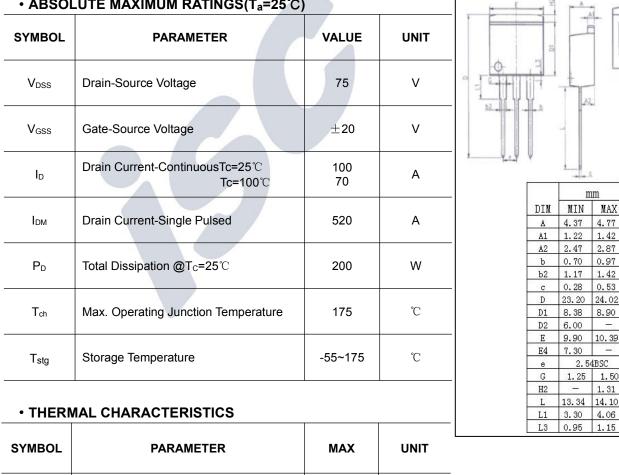
D(2)

### FEATURES

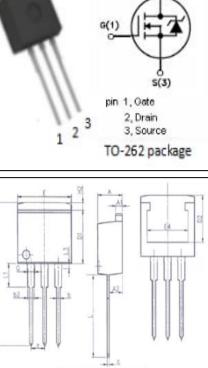
- · Static drain-source on-resistance: **R**DS(**on**) ≤7.8mΩ
- Enhancement mode
- Fast Switching Speed
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### DESCRITION

• reliable device for use in a wide variety of applications



#### ABSOLUTE MAXIMUM RATINGS(Ta=25°C)



mm MIN

4.37

22

2.47

0.70

1.17

0.28

23.20

MAX

4.77

1.42

2.87 0.97

1.42

0.53 24.02

8.90

10.39

1.50

1.31

4.06

1.15

2.54BSC

1.25

3.30

°C/W

0.75

1

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Channel-to-case thermal resistance

Rth(ch-c)



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#### **ELECTRICAL CHARACTERISTICS**

T<sub>c</sub>=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	ТҮР	МАХ	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> = 0.25mA	75			V
V <sub>GS</sub> (th)	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> ; I <sub>D</sub> =0.25mA	2.0		4.0	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> = 10V; I <sub>D</sub> =78A			7.8	mΩ
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±20V;V <sub>DS</sub> =0V			±0.2	μA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =75V; V <sub>GS</sub> = 0V;Tj=25℃ V <sub>DS</sub> =60V; V <sub>GS</sub> = 0V;Tj=150℃			20 250	μA
V <sub>SDF</sub>	Diode forward voltage	I <sub>SD</sub> =78A, V <sub>GS</sub> = 0 Vs			1.3	V

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