

# isc P-Channel MOSFET Transistor

# **IRF5305S**

### ·FEATURES

- Static drain-source on-resistance: R<sub>DS</sub>(on)≤60mΩ(@V<sub>GS</sub>= -10V; I<sub>D</sub>= -16A)
- · Advanced trench process technology
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

## APPLICATIONS

• Fast switching application.

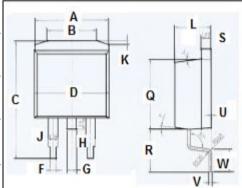


# pin 1, Gate 2, Drain 3, Source TO-263 Package

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage	-55	V
$V_{GS}$	Gate-Source Voltage	±20	V
I <sub>D</sub>	Drain Current-Continuous	31	А
P <sub>D</sub>	Total Dissipation @T <sub>C</sub> =25℃	110	W
Tj	Max. Operating Junction Temperature	-55~175	$^{\circ}$
T <sub>stg</sub>	Storage Temperature	-55~175	${\mathbb C}$

# • THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
Rth(j-c)	Channel-to-case thermal resistance	1.4	°C/W



	mm			
DIM	MIN	MAX		
Α	10			
В	6.6	6.8		
C	15.23	15.25		
D	10.15	10.17		
F	0.76	0.78		
G	1.26	1.28		
Н	1.4	1.6		
J	1.33	1.35		
K	0.4	0.6		
L	4.6	4.8		
0	8.69	8.71		
R	5.28	5.30		
S	1.26	1.28		
U	0.0	0.2		
٧	0.37	0.39		
W	2.80	2.82		



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## **ELECTRICAL CHARACTERISTICS**

T<sub>C</sub>=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	ТҮР	MAX	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> = -250 μ A	-55			V
$V_{\text{GS(th)}}$	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> ; I <sub>D</sub> = -250 μ A	-2		-4	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> = -10V; I <sub>D</sub> = -16A			60	mΩ
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±20V; V <sub>DS</sub> = 0V			±100	nA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> = -55V; V <sub>GS</sub> = 0V			-25	μА
V <sub>SD</sub>	Diode forward voltage	I <sub>S</sub> = -16A, V <sub>GS</sub> = 0V			-1.3	V



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