

# iscN-Channel MOSFET Transistor

### IRFB13N50A

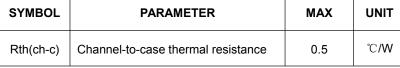
### • FEATURES

- Low drain-source on-resistance: RDs(ON) =0.45Ω (MAX)
- Enhancement mode:
  - Vth = 2.0 to 4.0V (VDS = 10 V, ID=0.25mA)
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

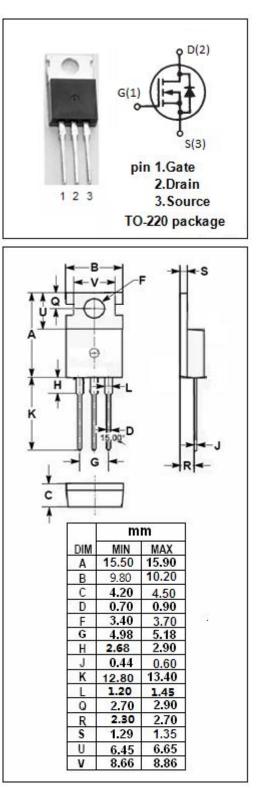
#### DESCRITION

Switching Voltage Regulators

#### ABSOLUTE MAXIMUM RATINGS(Ta=25°C) SYMBOL PARAMETER VALUE UNIT VDSS **Drain-Source Voltage** 500 V Gate-Source Voltage $\pm 30$ V $V_{GS}$ **Drain Current-Continuous** $I_D$ 14 А Drain Current-Single Pulsed 56 А **I**DM PD Total Dissipation @Tc=25°C 250 W Ti Max. Operating Junction Temperature -55~150 °C Storage Temperature -55~150 °C Tstg THERMAL CHARACTERISTICS



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### **ELECTRICAL CHARACTERISTICS**

#### $T_{\texttt{C}}\text{=}25^{\circ}\!\!\!\mathbb{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	ТҮР	МАХ	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> = 0.25mA	500			V
V <sub>GS</sub> (th)	Gate Threshold Voltage	V <sub>DS</sub> = 10V; I <sub>D</sub> =0.25mA	2.0		4.0	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =10V; I <sub>D</sub> =8.4A			0.45	Ω
lgss	Gate-Source Leakage Current	V <sub>GS</sub> = ±30V;V <sub>DS</sub> = 0V			±100	nA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =500V; V <sub>GS</sub> = 0V V <sub>DS</sub> =400V; V <sub>GS</sub> = 0V;T <sub>J</sub> =125°C			25 250	μA
V <sub>SDF</sub>	Diode forward voltage	I <sub>DR</sub> =14A, V <sub>GS</sub> = 0 V			1.5	V

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