

International IOR Rectifier

HEXFET® POWER MOSFET

IRFNG50 N-CHANNEL

1000 Volt, 2.0Ω HEXFET

HEXFET technology is the key to International Rectifier's advanced line of power MOSFET transistors. The efficient geometry achieves very low on-state resistance combined with high transconductance.

HEXFET transistors also feature all of the well-established advantages of MOSFETs, such as voltage control, very fast switching, ease of paralleling and electrical parameter temperature stability. They are well-suited for applications such as switching power supplies, motor controls, inverters, choppers, audio amplifiers, and high energy pulse circuits.

The Surface Mount Device (SMD-1) package represents another step in the continual evolution of surface mount technology. The SMD-1 will give designers the extra flexibility they need to increase circuit board density. International Rectifier has engineered the SMD-1 package to meet the specific needs of the power market by increasing the size of the termination pads, thereby enhancing thermal and electrical performance.

Product Summary

Part Number	BV _{DSS}	R _{DS(on)}	I _D
IRFNG50	1000V	2.0Ω	5.5A

Features:

- Avalanche Energy Rating
- Dynamic dv/dt Rating
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed
- Surface Mount
- Light-weight

Absolute Maximum Ratings

	Parameter	IRFNG50	Units
I _D @ V _{GS} = 10V, T _C = 25°C	Continuous Drain Current	5.5	A
I _D @ V _{GS} = 10V, T _C = 100°C	Continuous Drain Current	3.5	
I _{DM}	Pulsed Drain Current ①	22	
P _D @ T _C = 25°C	Max. Power Dissipation	150	W
	Linear Derating Factor	1.2	W/K ⑤
V _{GS}	Gate-to-Source Voltage	±20	V
E _{AS}	Single Pulse Avalanche Energy ②	860	mJ
I _{AR}	Avalanche Current ①	5.5	A
E _{AR}	Repetitive Avalanche Energy ①	15.0	mJ
dv/dt	Peak Diode Recovery dv/dt ③	1.0	V/ns
T _J	Operating Junction	-55 to 150	°C
T _{STG}	Storage Temperature Range		
	Package Mounting Surface Temperature		
	Weight	2.6 (typical)	g

Electrical Characteristics @ Tj = 25°C (Unless Otherwise Specified)

	Parameter	Min.	Typ.	Max.	Units	Test Conditions
BV_{DSS}	Drain-to-Source Breakdown Voltage	1000	—	—	V	$V_{GS} = 0V, I_D = 1.0 \text{ mA}$
$\Delta BV_{DSS}/\Delta T_J$	Temperature Coefficient of Breakdown Voltage	—	1.4	—	V/°C	Reference to 25°C, $I_D = 1.0 \text{ mA}$
RDS(on)	Static Drain-to-Source On-State Resistance	—	—	2.0	Ω	$V_{GS} = 10V, I_D = 3.5A$
		—	—	2.25		$V_{GS} = 10V, I_D = 5.5A$ ④
$V_{GS(th)}$	Gate Threshold Voltage	2.0	—	4.0	V	$V_{DS} = V_{GS}, I_D = 250\mu A$
g_{fs}	Forward Transconductance	5.2	—	—	S (r)	$V_{DS} > 15V, I_{DS} = 3.5A$ ④
IDSS	Zero Gate Voltage Drain Current	—	—	25	μA	$V_{DS} = 0.8 \times \text{Max Rating}, V_{GS} = 0V$
		—	—	250		$V_{DS} = 0.8 \times \text{Max Rating}, V_{GS} = 0V, T_J = 125^\circ C$
IGSS	Gate-to-Source Leakage Forward	—	—	100	nA	$V_{GS} = 20V$
IGSS	Gate-to-Source Leakage Reverse	—	—	-100	nA	$V_{GS} = -20V$
Qg	Total Gate Charge	87	—	200	nC	$V_{GS} = 10V, I_D = 5.5A$
Qgs	Gate-to-Source Charge	8.7	—	20		$V_{DS} = \text{Max. Rating} \times 0.5$
Qgd	Gate-to-Drain ("Miller") Charge	49	—	110		see figures 6 and 13
td(on)	Turn-On Delay Time	—	—	30	ns	$V_{DD} = 500V, I_D = 5.5A,$ $R_G = 2.35\Omega, V_{GS} = 10V$
tr	Rise Time	—	—	44		
td(off)	Turn-Off Delay Time	—	—	210		
tf	Fall Time	—	—	60		
LD	Internal Drain Inductance	—	2.0	—	nH	Measured from the drain lead, 6mm (0.25 in.) from package to center of die. Modified MOSFET symbol showing the internal inductances.
LS	Internal Source Inductance	—	6.5	—		
Ciss	Input Capacitance	—	2400	—	pF	$V_{GS} = 0V, V_{DS} = 25V$ $f = 1.0 \text{ MHz}$ see figure 5
Coss	Output Capacitance	—	240	—		
Crss	Reverse Transfer Capacitance	—	80	—		

Source-Drain Diode Ratings and Characteristics

	Parameter	Min.	Typ.	Max.	Units	Test Conditions
IS	Continuous Source Current (Body Diode)	—	—	5.5	A	Modified MOSFET symbol showing the integral reverse p-n junction rectifier.
ISM	Pulse Source Current (Body Diode) ①	—	—	22		
VSD	Diode Forward Voltage	—	—	1.8	V	$T_J = 25^\circ C, I_S = 5.5A, V_{GS} = 0V$ ④
trr	Reverse Recovery Time	—	—	1200	ns	$T_J = 25^\circ C, I_F = 5.5A, di/dt \leq 100A/\mu s$
QRR	Reverse Recovery Charge	—	—	8.4	μC	$V_{DD} \leq 50V$ ④
ton	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by LS + LD.				

Thermal Resistance

	Parameter	Min.	Typ.	Max.	Units	Test Conditions
RthJC	Junction-to-Case	—	—	0.83	K/W	Soldered to a copper clad PC board
RthPCB	Junction-to-PC Board	—	TBD	—		

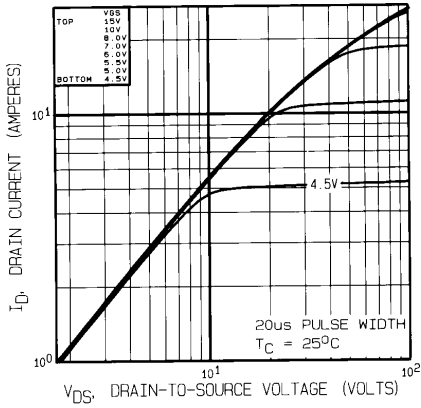


Fig. 1 — Typical Output Characteristics
 $T_C = 25^\circ\text{C}$

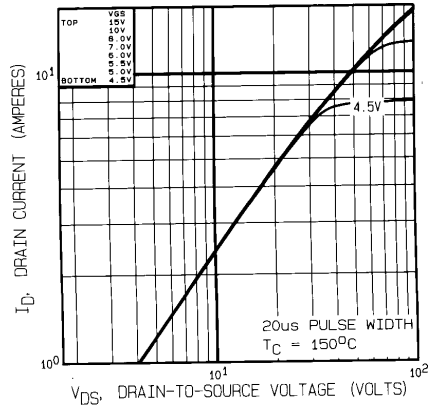


Fig. 2 — Typical Output Characteristics
 $T_C = 150^\circ\text{C}$

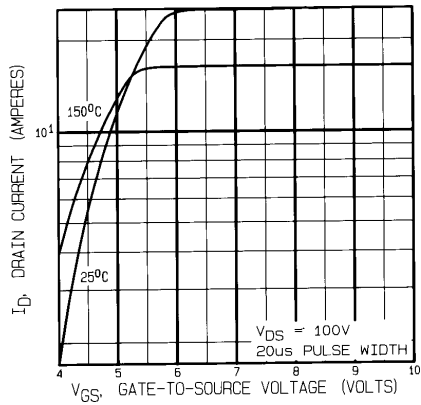


Fig. 3 — Typical Transfer Characteristics

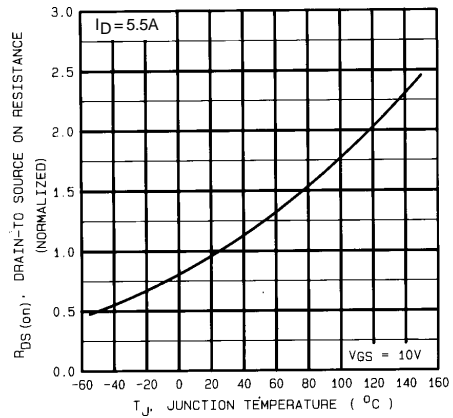


Fig. 4 — Normalized On-Resistance Vs. Temperature

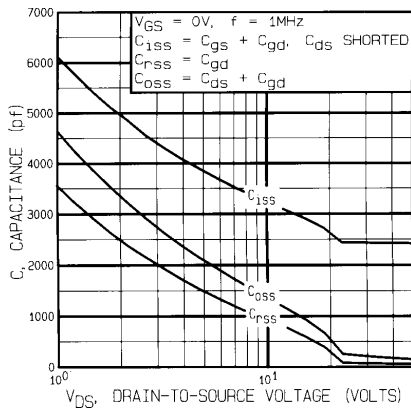


Fig. 5 — Typical Capacitance Vs. Drain-to-Source Voltage

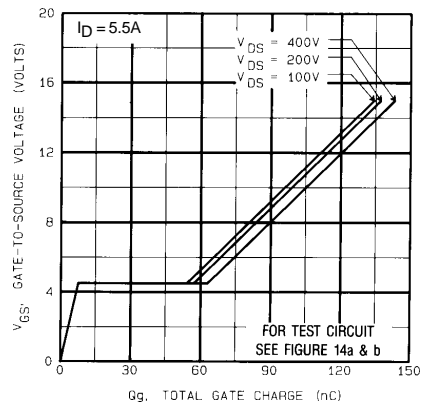


Fig. 6 — Typical Gate Charge Vs. Gate-to-Source Voltage

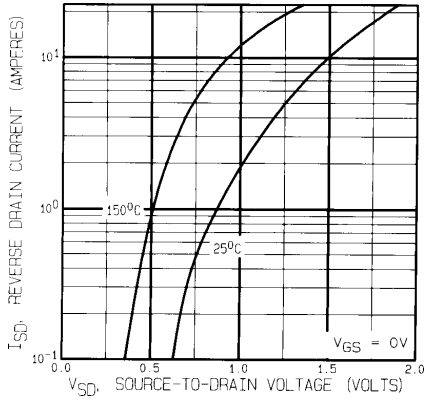


Fig. 7 — Typical Source-to-Drain Diode Forward Voltage

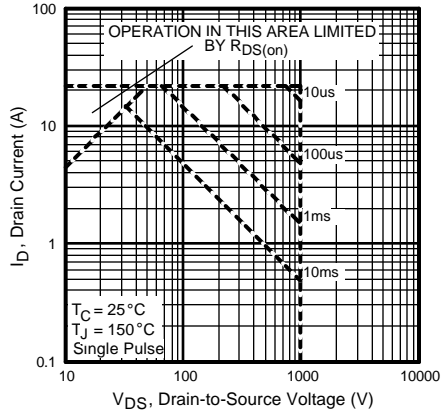


Fig. 8 — Maximum Safe Operating Area

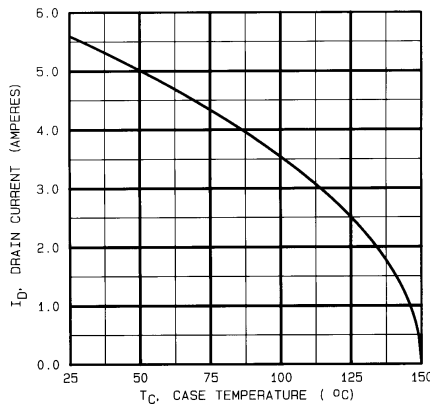


Fig. 9 — Maximum Drain Current Vs. Case Temperature

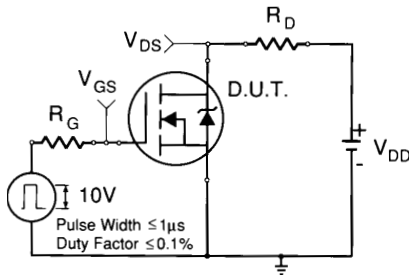


Fig. 10a — Switching Time Test Circuit

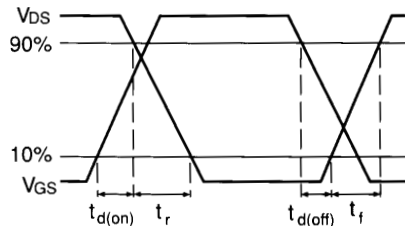


Fig. 10b — Switching Time Waveforms

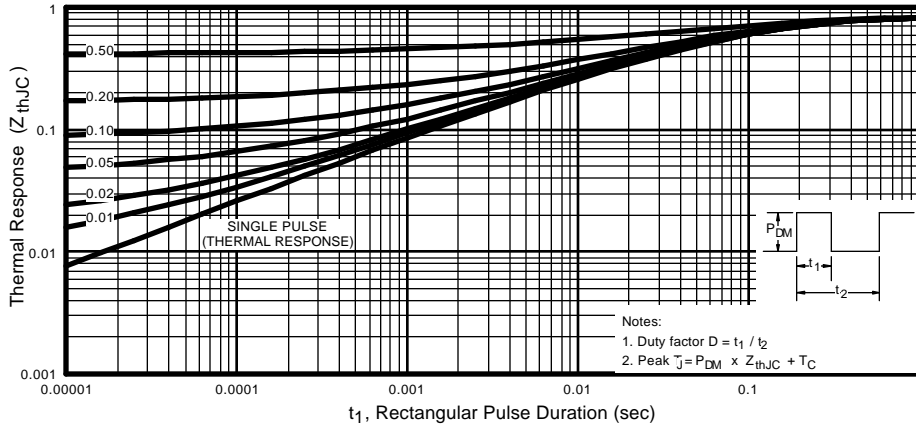


Fig. 11 — Maximum Effective Transient Thermal Impedance, Junction-to-Case Vs. Pulse Duration

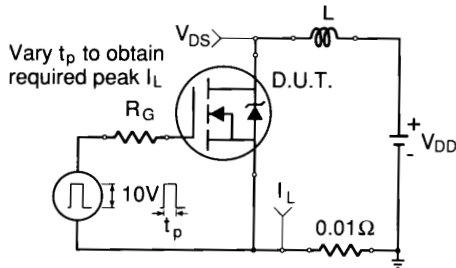


Fig. 12a — Unclamped Inductive Test Circuit

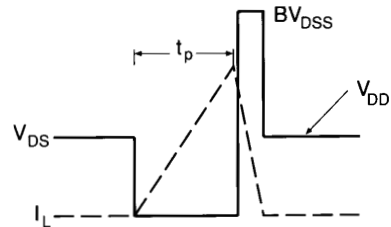


Fig. 12b — Unclamped Inductive Waveforms

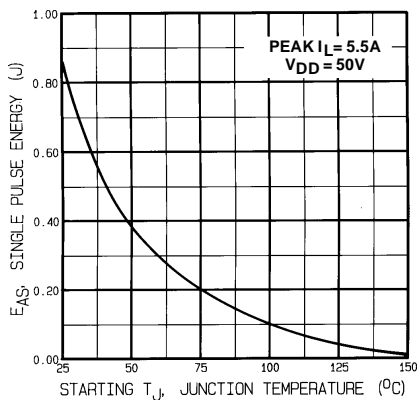


Fig. 12c — Max. Avalanche Energy vs. Current

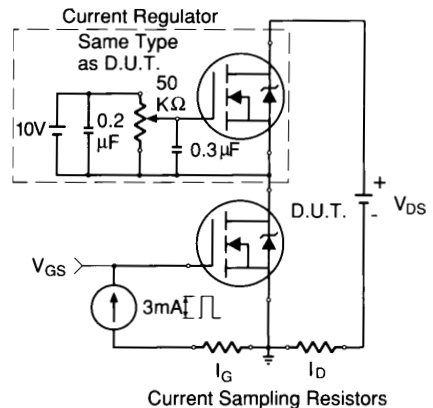


Fig. 13a — Gate Charge Test Circuit

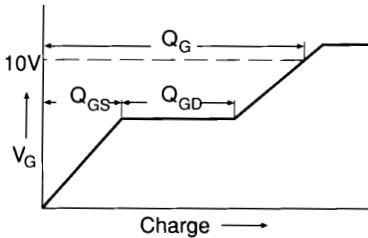
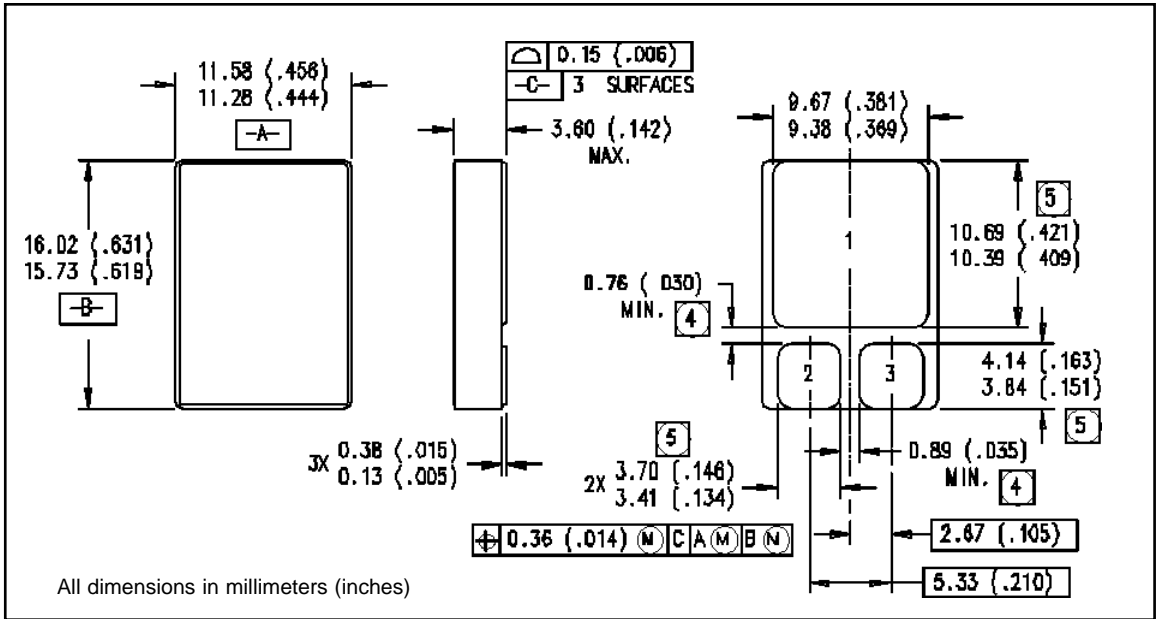


Fig. 13b — Basic Gate Charge Waveform

- ① Repetitive Rating; Pulse width limited by maximum junction temperature. (see figure 11)
- ② @ $V_{DD} = 50V$, Starting $T_J = 25^\circ C$,
 $EAS = [0.5 * L * (I_L^2) * [BV_{DSS}/(BV_{DSS}-V_{DD})]$
 Peak $I_L = 5.5A$, $V_{GS} = 10V$, $25 \leq R_G \leq 200\Omega$
- ③ $I_{SD} \leq 5.5A$, $di/dt \leq 120A/\mu s$,
 $V_{DD} \leq BV_{DSS}$, $T_J \leq 150^\circ C$
- ④ Pulse width $\leq 300 \mu s$; Duty Cycle $\leq 2\%$
- ⑤ $K/W = ^\circ C/W$
 $W/K = W/^\circ C$

Case Outline and Dimensions — SMD-1



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