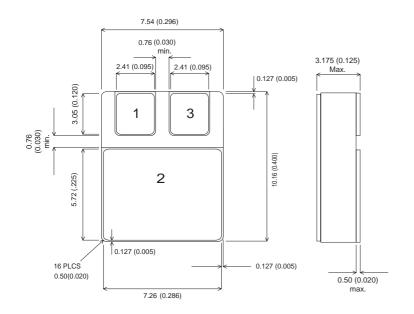


## IRFNJ9130 IRF9130SMD05

#### **MECHANICAL DATA**

Dimensions in mm (inches)



## **SMD05 (TO-276AA)**

### IRF9130SMD05

PAD1 = GATE PAD 2 DRAIN PAD3 = SOURCE

### **IRFNJ9130**

PAD1 = SOURCE PAD 2 = DRAIN PAD3 = GATE

# P-CHANNEL POWER MOSFET FOR HI-REL APPLICATIONS

 $V_{DSS}$  -100V  $I_{D(cont)}$  -11A  $R_{DS(on)}$  0.30 $\Omega$ 

## **FEATURES**

- HERMETICALLY SEALED
- SIMPLE DRIVE REQUIREMENTS
- LIGHTWEIGHT
- SCREENING OPTIONS AVAILABLE
- ALL LEADS ISOLATED FROM CASE

## **ABSOLUTE MAXIMUM RATINGS** (T<sub>case</sub> = 25°C unless otherwise stated)

$\overline{V_{GS}}$	Gate - Source Voltage	±20V	
$I_{D}$	Continuous Drain Current @ T <sub>case</sub> = 25°C	-11A	
$I_D$	Continuous Drain Current @ T <sub>case</sub> = 100°C	-7A	
$I_{DM}$	Pulsed Drain Current	-50A	
$P_{D}$	Power Dissipation @ T <sub>case</sub> = 25°C	45W	
	Linear Derating Factor	0.36W/°C	
$T_J$ , $T_stg$	Operating and Storage Temperature Range	−55 to 150°C	
$R_{\theta JC}$	Thermal Resistance Junction to Case	2.8°C/W max.	

Semelab Plc reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.

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# IRFNJ9130 IRF9130SMD05

# **ELECTRICAL CHARACTERISTICS** ( $T_C = 25$ °C unless otherwise stated)

Parameter		Test Cond	ditions	Min.	Тур.	Max.	Unit		
	STATIC ELECTRICAL RATINGS	•		•		1			
BV <sub>DSS</sub>	Drain – Source Breakdown Voltage	$V_{GS} = 0$	I <sub>D</sub> = -1mA	-100			V		
$\Delta BV_{DSS}$	Temperature Coefficient of	Reference to 25°C			-0.1		V/°C		
$\Delta T_{J}$	Breakdown Voltage	$I_D = -1 \text{mA}$			-0.1				
R <sub>DS(on)</sub>	Static Drain – Source On–State	V <sub>GS</sub> = -10V	I <sub>D</sub> = -7A			0.30	Ω		
	Resistance	V <sub>GS</sub> = -10V	I <sub>D</sub> = -11A			0.35			
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}$	$I_D = -250 \mu A$	-2		-4	V		
9 <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> ≥ -15V	I <sub>DS</sub> = -7A	3			S(\Omega)		
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{GS} = 0$	$V_{DS} = -80V$			-25	μΑ		
			T <sub>J</sub> = 125°C			-250			
I <sub>GSS</sub>	Forward Gate – Source Leakage	$V_{GS} = -20V$				-100	T		
I <sub>GSS</sub>	Reverse Gate – Source Leakage	$V_{GS} = 20V$				100	- nA		
	DYNAMIC CHARACTERISTICS	•				ı	.1		
C <sub>iss</sub>	Input Capacitance	$V_{GS} = 0$			860				
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> = 25V			350		pF		
C <sub>rss</sub>	Reverse Transfer Capacitance	f = 1MHz			125		1		
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> = -10V				29	nC		
Q <sub>gs</sub>	Gate - Source Charge	$V_{DS} = -50V$				7.1			
Q <sub>gd</sub>	Gate - Drain ("Miller") Charge	I <sub>D</sub> = -11A				21			
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = -50V				60			
t <sub>r</sub>	Rise Time	I <sub>D</sub> = -11A				140	1 nc		
t <sub>d(off)</sub>	Turn-Off Delay Time	$R_G = 7.5\Omega$				140	ns		
t <sub>f</sub>	Fall Time	1				140	]		
	SOURCE - DRAIN DIODE CHARAC	TERISTICS		•		1			
I <sub>S</sub>	Continuous Source Current					-11			
I <sub>SM</sub>	Pulse Source Current					-50	A		
$V_{SD}$	Diode Forward Voltage	$I_S = -11A$ $V_{GS} = 0$	T <sub>J</sub> = 25°C			-4.7	V		
t <sub>rr</sub>	Reverse Recovery Time	I <sub>S</sub> = -11A	T <sub>.J</sub> = 25°C			250	ns		
Q <sub>rr</sub>	Reverse Recovery Charge	_ ~	/μs V <sub>DD</sub> ≤ 150V			3	μC		

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