

isc N-Channel MOSFET Transistor
IRFP4310Z, IIRFP4310Z
• FEATURES

- Static drain-source on-resistance:
 $R_{DS(on)} \leq 6.0m\Omega$
- Enhancement mode:
 $V_{th} = 2.0$ to 4.0 V ($V_{DS}=V_{GS}$, $I_D=250 \mu$ A)
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• DESCRIPTION

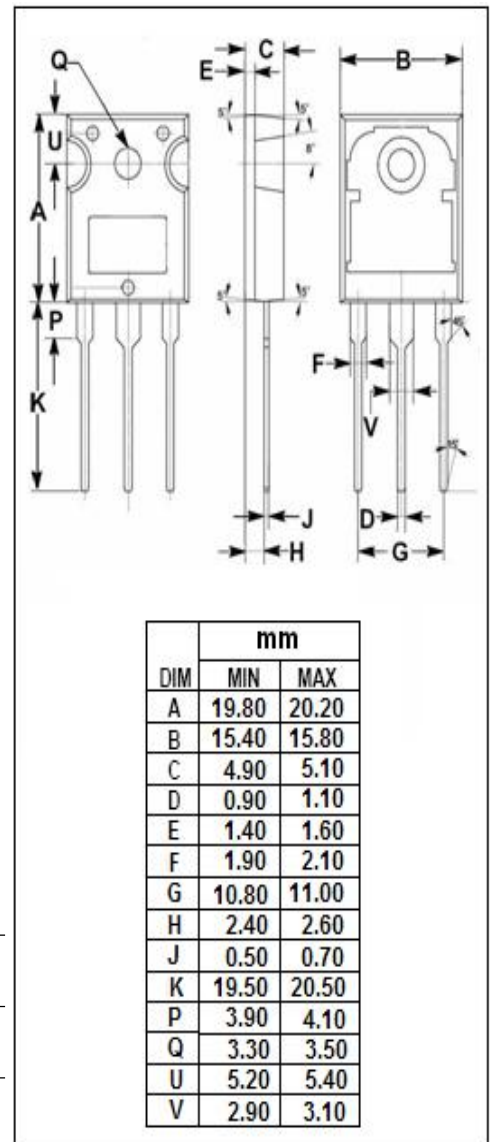
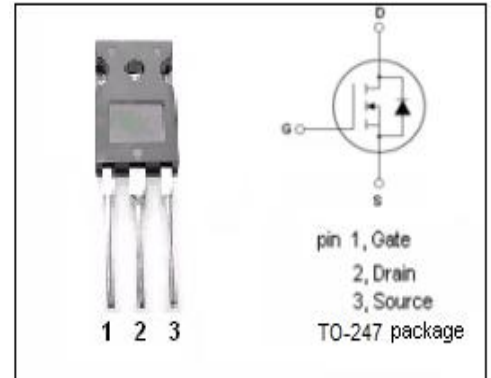
- High Efficiency Synchronous Rectification in SMPS
- Uninterruptible Power Supply
- High Speed Power Switching
- Hard Switched And High Frequency Circuits

• ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current-Continuous	120	A
I_{DM}	Drain Current-Single Pulsed	560	A
P_D	Total Dissipation @ $T_c=25^\circ\text{C}$	280	W
T_j	Max. Operating Junction Temperature	175	$^\circ\text{C}$
T_{stg}	Storage Temperature	-55~175	$^\circ\text{C}$

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(j-c)}$	Channel-to-case thermal resistance	0.54	$^\circ\text{C/W}$
$R_{th(j-a)}$	Channel-to-ambient thermal resistance	40	$^\circ\text{C/W}$



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ELECTRICAL CHARACTERISTICS

T_C=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V; I _D =250 μ A	100			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} ; I _D =250 μ A	2.0		4.0	V
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} =10V; I _D =75A			6.0	mΩ
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ±20V			±0.1	μ A
I _{DSS}	Drain-Source Leakage Current	V _{DS} =75V; V _{GS} = 0V			20	μ A
V _{SD}	Diode forward voltage	I _S =75A, V _{GS} = 0V			1.3	V

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