

**iscN-Channel MOSFET Transistor**
**IRFR310**
**• FEATURES**

- Low drain-source on-resistance:  
 $R_{DS(ON)} \leq 3.6\Omega @V_{GS}=10V$
- Enhancement mode:  
 $V_{th} = 2.0 \text{ to } 4.0V (V_{DS} = 10V, I_D=0.25mA)$
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**• DESCRIPTION**

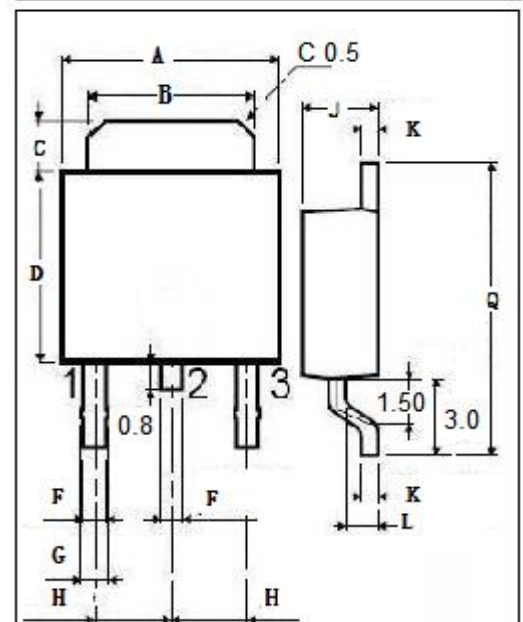
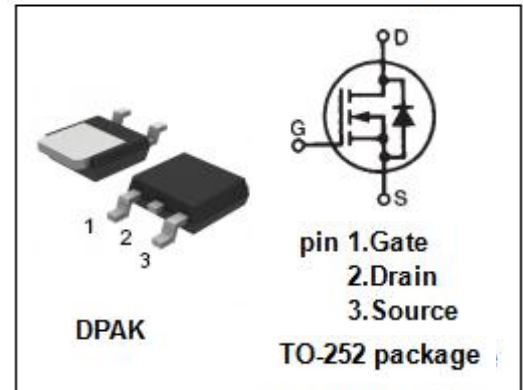
- Switching Voltage Regulators

**• ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ C$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage	400	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current-Continuous	1.7	A
$I_{DM}$	Drain Current-Single Pulsed	6.0	A
$P_D$	Total Dissipation @ $T_c=25^\circ C$	25	W
$T_j$	Max. Operating Junction Temperature	-55~150	$^\circ C$
$T_{stg}$	Storage Temperature	-55~150	$^\circ C$

**• THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	5.0	$^\circ C/W$



DIM	mm	
	MIN	MAX
A	6.40	6.60
B	5.20	5.40
C	1.15	1.35
D	5.70	6.10
F	0.65	
G	0.75	
H	2.10	2.50
J	2.10	2.40
K	0.40	0.60
L	0.90	1.10
Q	9.90	10.1

## iscN-Channel MOSFET Transistor

IRFR310

## ELECTRICAL CHARACTERISTICS

T<sub>c</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> = 0.25mA	400			V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = 10V; I <sub>D</sub> =0.25mA	2.0		4.0	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =10V; I <sub>D</sub> =1.0A			3.6	Ω
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±20V; V <sub>DS</sub> = 0V			± 100	nA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =400V; V <sub>GS</sub> = 0V V <sub>DS</sub> =320V; V <sub>GS</sub> = 0V; T <sub>J</sub> =125°C			25 250	uA
V <sub>SDF</sub>	Diode forward voltage	I <sub>DR</sub> =1.7A, V <sub>GS</sub> = 0 V			1.6	V

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