

International IR Rectifier

INSULATED GATE BIPOLAR TRANSISTOR

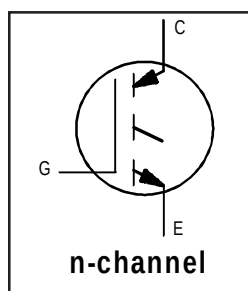
PD - 94443

IRG4PC60U

UltraFast Speed IGBT

Features

- UltraFast: Optimized for high operating frequencies up to 50 kHz in hard switching, >200 kHz in resonant mode.
- Generation 4 IGBT design provides tighter parameter distribution and higher efficiency.
- Industry standard TO-247AC package.



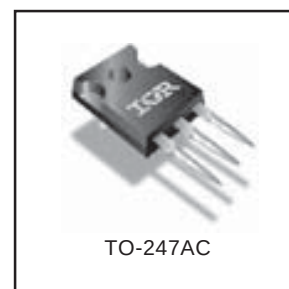
$$V_{CES} = 600V$$

$$V_{CE(on) typ.} = 1.6V$$

$$@ V_{GE} = 15V, I_C = 40A$$

Benefits

- Generation 4 IGBT's offer highest efficiency available.
- IGBT's optimized for specified application conditions.
- Designed for best performance when used with IR Hexfred & IR Fred companion diodes.



TO-247AC

Absolute Maximum Ratings

	Parameter	Max.	Units
V_{CES}	Collector-to-Emitter Breakdown Voltage	600	V
$I_C @ T_C = 25^\circ C$	Continuous Collector Current	75	A
$I_C @ T_C = 100^\circ C$	Continuous Collector Current	40	
I_{CM}	Pulsed Collector Current ①	300	
I_{LM}	Clamped Inductive Load Current ②	300	
V_{GE}	Gate-to-Emitter Voltage	± 20	V
E_{ARV}	Reverse Voltage Avalanche Energy ③	200	mJ
$P_D @ T_C = 25^\circ C$	Maximum Power Dissipation	520	W
$P_D @ T_C = 100^\circ C$	Maximum Power Dissipation	210	
T_J T_{STG}	Operating Junction and Storage Temperature Range	-55 to + 150	$^\circ C$
	Soldering Temperature, for 10 seconds	300 (0.063 in. (1.6mm from case)	
	Mounting torque, 6-32 or M3 screw.	10 lbf•in (1.1N•m)	

Thermal Resistance

	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case	----	0.24	$^\circ C/W$
$R_{\theta CS}$	Case-to-Sink, Flat, Greased Surface	0.24	----	
$R_{\theta JA}$	Junction-to-Ambient, typical socket mount	----	40	
Wt	Weight	6 (0.21)	----	g (oz)

Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)CES}$	Collector-to-Emitter Breakdown Voltage	600	----	----	V	$V_{GE} = 0V, I_C = 250\mu A$
$V_{(BR)ECS}$	Emitter-to-Collector Breakdown Voltage ④	17	----	----	V	$V_{GE} = 0V, I_C = 1.0A$
$\Delta V_{(BR)CES}/\Delta T_J$	Temperature Coeff. of Breakdown Voltage	----	0.28	----	V/ $^\circ\text{C}$	$V_{GE} = 0V, I_C = 1.0mA$
$V_{CE(ON)}$	Collector-to-Emitter Saturation Voltage	----	1.7	2.0	V	$I_C = 40A, V_{GE} = 15V$
		----	1.9	----		$I_C = 75A, V_{GE} = 15V$
		----	1.6	----		$I_C = 40A, T_J = 150^\circ\text{C}$
$V_{GE(th)}$	Gate Threshold Voltage	3.0	----	6.0		$V_{CE} = V_{GE}, I_C = 250\mu A$
$\Delta V_{GE(th)}/\Delta T_J$	Temperature Coeff. of Threshold Voltage	----	-12	----	mV/ $^\circ\text{C}$	$V_{CE} = V_{GE}, I_C = 250\mu A$
g_{fe}	Forward Transconductance ⑤	44	59	----	S	$V_{CE} \geq 100V, I_C = 40A$
I_{CES}	Zero Gate Voltage Collector Current	----	----	250	μA	$V_{GE} = 0V, V_{CE} = 600V$
		----	----	2.0		$V_{GE} = 0V, V_{CE} = 10V, T_J = 25^\circ\text{C}$
		----	----	5000		$V_{GE} = 0V, V_{CE} = 600V, T_J = 150^\circ\text{C}$
I_{GES}	Gate-to-Emitter Leakage Current	----	----	± 100	nA	$V_{GE} = \pm 20V$

Switching Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
Q_g	Total Gate Charge (turn-on)	----	310	320	nC	$I_C = 40A$
Q_{ge}	Gate - Emitter Charge (turn-on)	----	41	46		$V_{CC} = 480V$
Q_{gc}	Gate - Collector Charge (turn-on)	----	110	120		$V_{GE} = 15V$
$t_{d(on)}$	Turn-On Delay Time	----	39	----	ns	$T_J = 25^\circ\text{C}$
t_r	Rise Time	----	42	----		$I_C = 40A, V_{CC} = 480V$
$t_{d(off)}$	Turn-Off Delay Time	----	200	----		$V_{GE} = 15V, R_G = 5.0\Omega$
t_f	Fall Time	----	100	----		Energy losses include "tail"
E_{on}	Turn-On Switching Loss	----	0.28	----	mJ	See Fig. 10, 11, 13, 14
E_{off}	Turn-Off Switching Loss	----	1.1	----		
E_{is}	Total Switching Loss	----	1.3	1.8		
$t_{d(on)}$	Turn-On Delay Time	----	36	----	ns	$T_J = 150^\circ\text{C}$
t_r	Rise Time	----	42	----		$I_C = 40A, V_{CC} = 480V$
$t_{d(off)}$	Turn-Off Delay Time	----	300	----		$V_{GE} = 15V, R_G = 5.0\Omega$
t_f	Fall Time	----	160	----		Energy losses include "tail"
E_{is}	Total Switching Loss	----	2.6	----	mJ	See Fig. 13, 14
L_E	Internal Emitter Inductance	----	13	----	nH	Measured 5mm from package
C_{ies}	Input Capacitance	----	5860	----	pF	$V_{GE} = 0V$
C_{oes}	Output Capacitance	----	370	----		$V_{CC} = 30V$
C_{res}	Reverse Transfer Capacitance	----	75	----		$f = 1.0MHz$

Notes:

- ① Repetitive rating; $V_{GE} = 20V$, pulse width limited by max. junction temperature. (See fig. 13b)
- ② $V_{CC} = 80\%(V_{CES})$, $V_{GE} = 20V$, $L = TBD \mu H$,
 $R_G = 5.0W$. (See fig. 13a)
- ③ Repetitive rating; pulse width limited by maximum junction temperature.
- ④ Pulse width $\leq 80\mu s$; duty factor $\leq 0.1\%$.
- ⑤ Pulse width $5.0\mu s$, single shot.

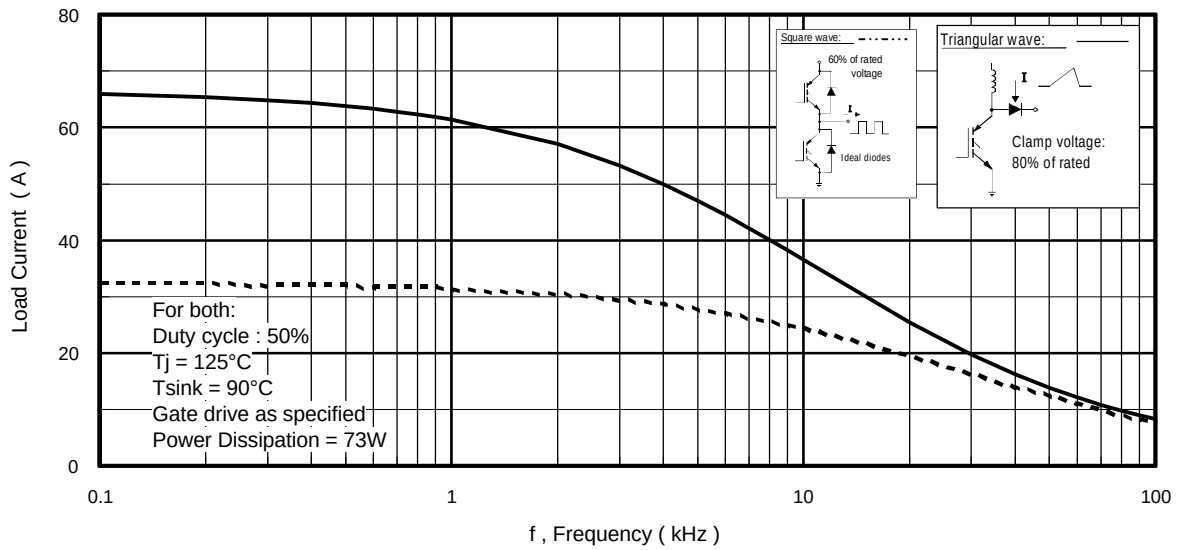


Fig. 1 - Typical Load Current vs. Frequency
(For square wave, $I = I_{\text{RMS}}$ of fundamental; for triangular wave, $I = I_{\text{PK}}$)

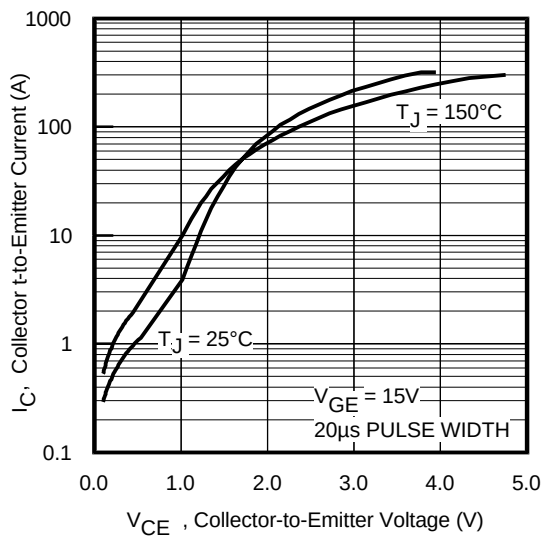


Fig. 2 - Typical Output Characteristics

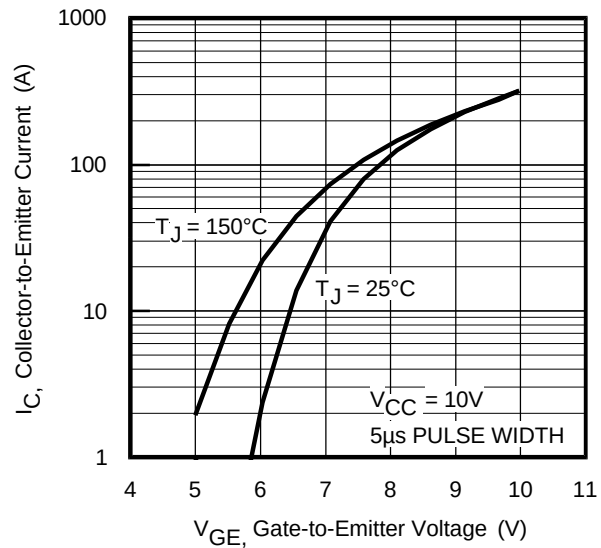


Fig. 3 - Typical Transfer Characteristics

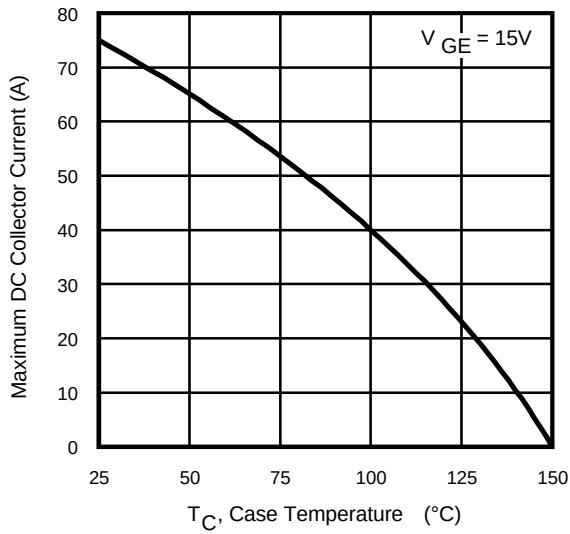


Fig. 4 - Maximum Collector Current vs. Case Temperature

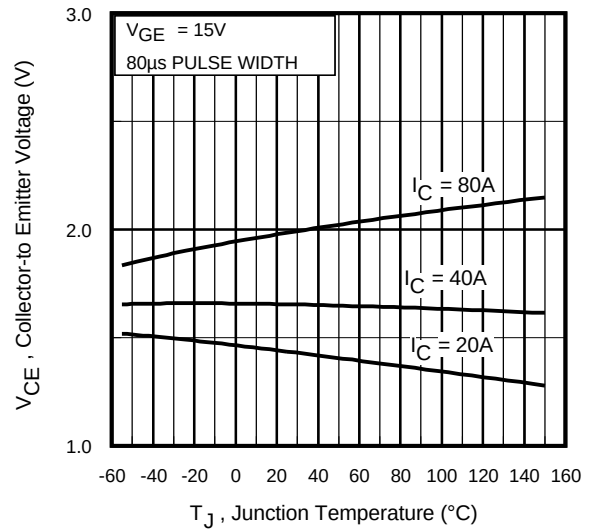


Fig. 5 - Collector-to-Emitter Voltage vs. Junction Temperature

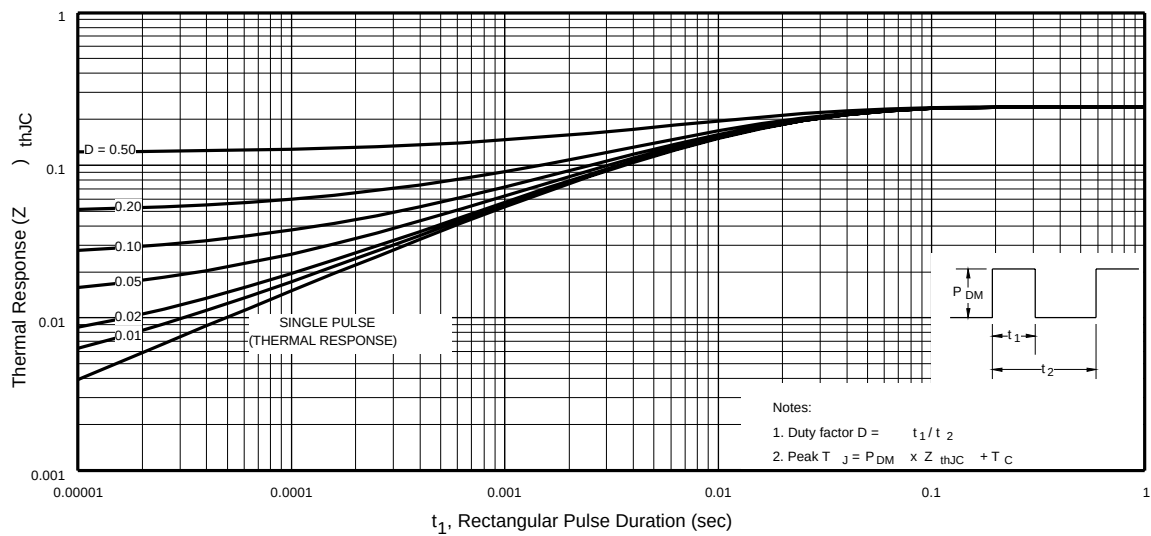


Fig. 6 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

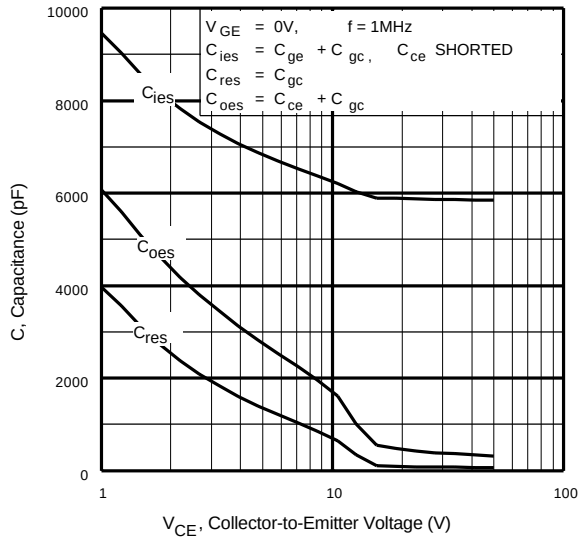


Fig. 7 - Typical Capacitance vs. Collector-to-Emitter Voltage

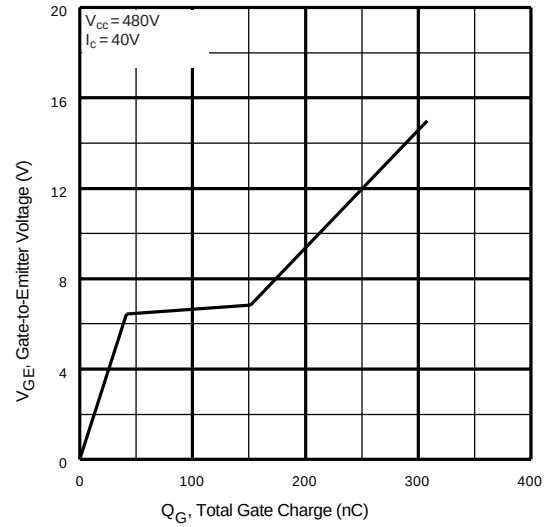


Fig. 8 - Typical Gate Charge vs. Gate-to-Emitter Voltage

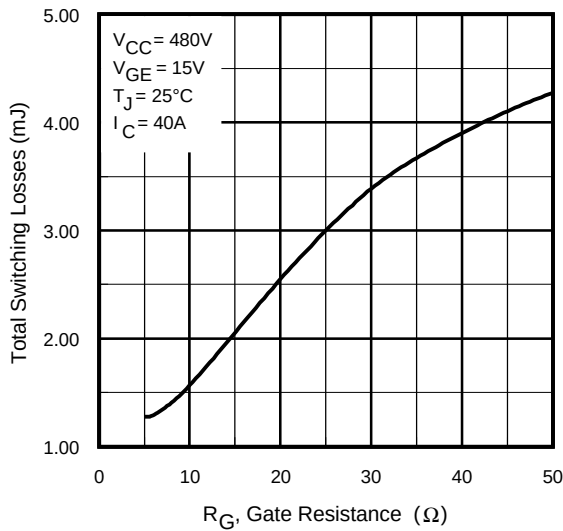


Fig. 9 - Typical Switching Losses vs. Gate Resistance

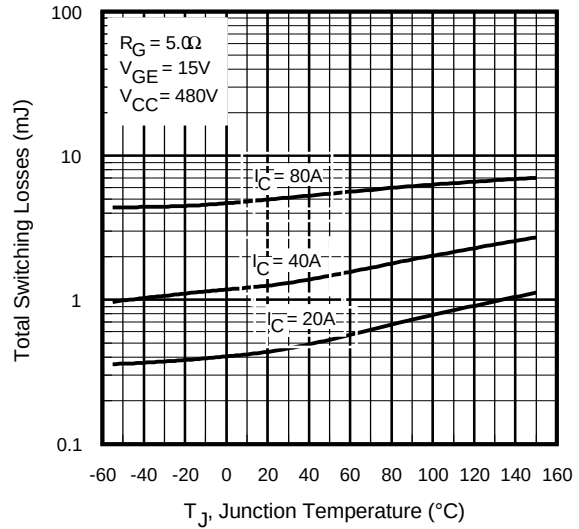


Fig. 10 - Typical Switching Losses vs. Junction Temperature

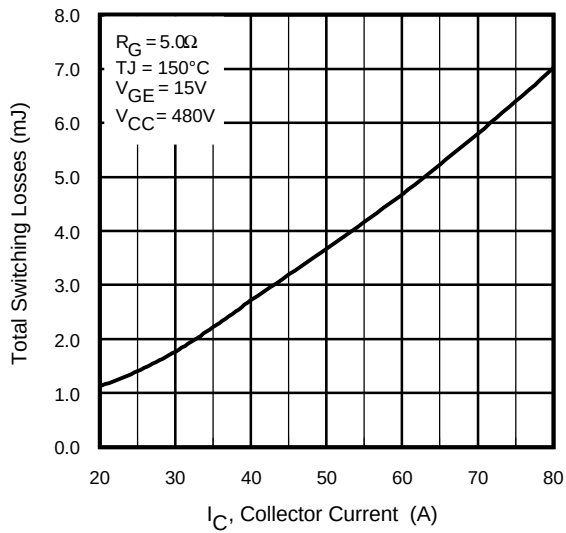


Fig. 11 - Typical Switching Losses vs. Collector-to-Emitter Current

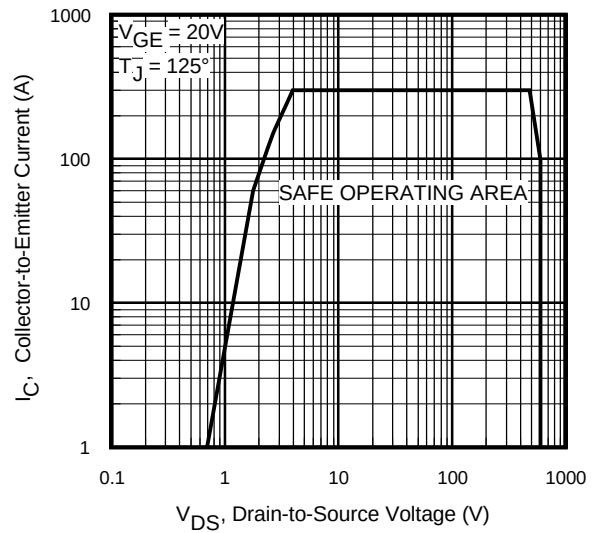


Fig. 12 - Turn-Off SOA

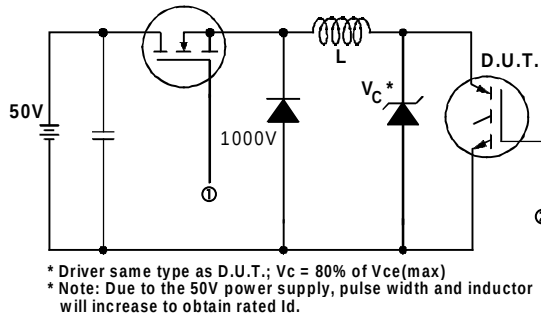


Fig. 13a - Clamped Inductive Load Test Circuit

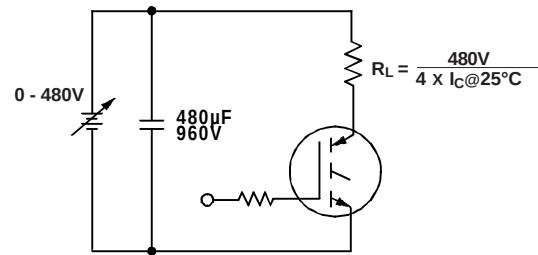


Fig. 13b - Pulsed Collector Current Test Circuit

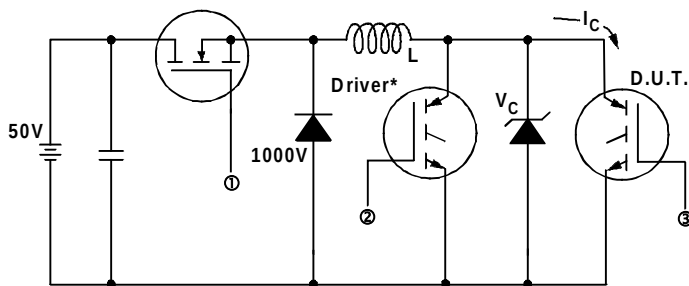


Fig. 14a - Switching Loss Test Circuit

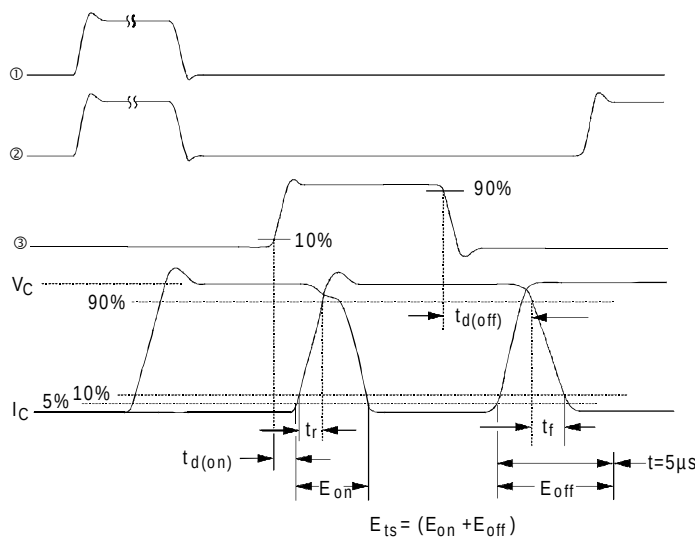


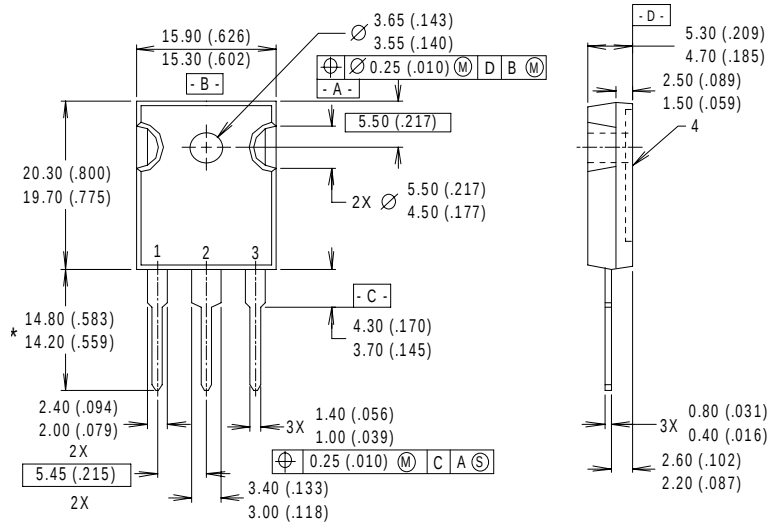
Fig. 14b - Switching Loss Waveforms

IRG4PC60U

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TO-247AC Package Outline

Dimensions are shown in millimeters (inches)



NOTES:

- 1 DIMENSIONS & TOLERANCING PER ANSI Y14.5M, 1982.
- 2 CONTROLLING DIMENSION : INCH.
- 3 DIMENSIONS ARE SHOWN MILLIMETERS (INCHES).
- 4 CONFORMS TO JEDEC OUTLINE TO-247AC.

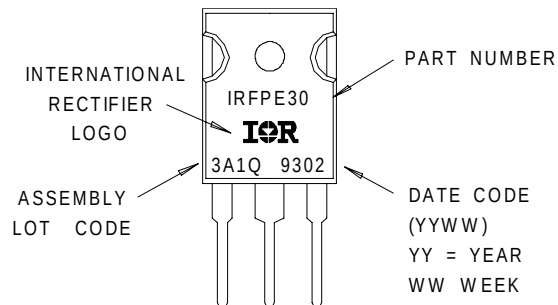
LEAD ASSIGNMENTS

- 1 - GATE
- 2 - COLLECTOR
- 3 - EMITTER
- 4 - COLLECTOR

* LONGER LEADED (20mm) VERSION AVAILABLE (TO-247AD) TO ORDER ADD "E" SUFFIX TO PART NUMBER

TO-247AC Part Marking Information

EXAMPLE : THIS IS AN IRFPE30
WITH ASSEMBLY
LOT CODE 3A1Q



Data and specifications subject to change without notice.
This product has been designed and qualified for the Industrial market.
Qualification Standards can be found on IR's Web site.

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