

Isc N-Channel MOSFET Transistor

IRL520NL

• FEATURES

- With To-262 package
- Low input capacitance and gate charge
- Low gate input resistance
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• APPLICATIONS

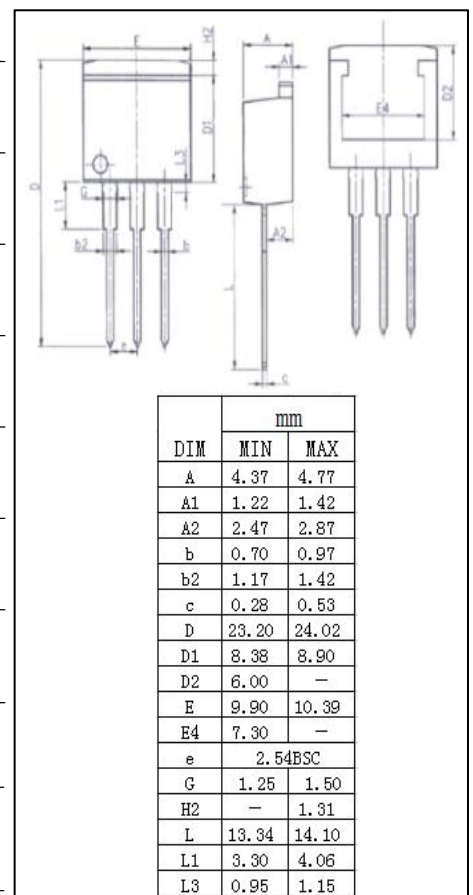
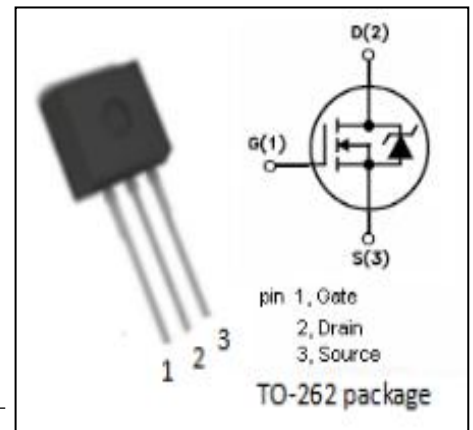
- Switching applications

• ABSOLUTE MAXIMUM RATINGS(T_a=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V _{DSS}	Drain-Source Voltage	100	V
V _{GSS}	Gate-Source Voltage	±16	V
I _D	Drain Current-Continuous T _c =25°C T _c =100°C	10 7.1	A
I _{DM}	Drain Current-Single Pulsed	35	A
P _D	Total Dissipation @T _c =25°C	48	W
T _{ch}	Max. Operating Junction Temperature	175	°C
T _{stg}	Storage Temperature	-55~175	°C

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th(ch-c)}	Channel-to-case thermal resistance	3.1	°C/W



Isc N-Channel MOSFET Transistor**IRL520NL****ELECTRICAL CHARACTERISTICS** $T_C=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V; I_D=0.25mA$	100			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}; I_D=0.25mA$	1.0		2.0	V
$R_{DS(on)}$	Drain-Source On-Resistance	$V_{GS}=10V; I_D=6A$			180	$m\Omega$
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 16V; V_{DS}=0V$			± 0.1	μA
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=100V; V_{GS}=0V; T_j=25^{\circ}\text{C}$ $V_{DS}=80V; V_{GS}=0V; T_j=125^{\circ}\text{C}$			25 250	μA
V_{SDF}	Diode forward voltage	$I_{SD}=6.0A, V_{GS}=0V$			1.3	V

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