

# International IR Rectifier

PD - 95222

## IRLL110PbF

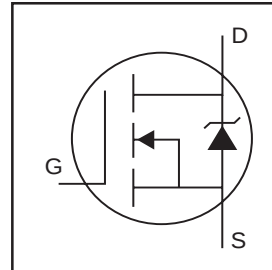
HEXFET® Power MOSFET

- Surface Mount
- Available in Tape & Reel
- Dynamic dv/dt Rating
- Repetitive Avalanche Rated
- Logic-Level Gate Drive
- RDS(on) Specified at VGS = 4V & 5V
- Fast Switching
- Lead-Free

### Description

Third Generation HEXFETs from International Rectifier provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

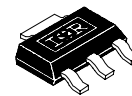
The SOT-223 package is designed for surface-mount using vapor phase, infra red, or wave soldering techniques. Its unique package design allows for easy automatic pick-and-place as with other SOT or SOIC packages but has the added advantage of improved thermal performance due to an enlarged tab for heatsinking. Power dissipation of greater than 1.25W is possible in a typical surface mount application.



$$V_{DS} = 100V$$

$$R_{DS(on)} = 0.54\Omega$$

$$I_D = 1.5A$$



SOT-223

### Absolute Maximum Ratings

|                                   | Parameter                                  | Max.                  | Units |
|-----------------------------------|--------------------------------------------|-----------------------|-------|
| $I_D$ @ $T_c = 25^\circ\text{C}$  | Continuous Drain Current, $V_{GS}$ @ 5.0 V | 1.5                   | A     |
| $I_D$ @ $T_c = 100^\circ\text{C}$ | Continuous Drain Current, $V_{GS}$ @ 5.0 V | 0.93                  |       |
| $I_{DM}$                          | Pulsed Drain Current ①                     | 12                    |       |
| $P_D$ @ $T_c = 25^\circ\text{C}$  | Power Dissipation                          | 3.1                   | W     |
| $P_D$ @ $T_A = 25^\circ\text{C}$  | Power Dissipation (PCB Mount)**            | 2.0                   |       |
|                                   | Linear Derating Factor                     | 0.025                 |       |
|                                   | Linear Derating Factor (PCB Mount)**       | 0.017                 | W/°C  |
| $V_{GS}$                          | Gate-to-Source Voltage                     | -/+10                 |       |
| $E_{AS}$                          | Single Pulse Avalanche Energy ②            | 50                    | mJ    |
| $I_{AR}$                          | Avalanche Current ①                        | 1.5                   | A     |
| $E_{AR}$                          | Repetitive Avalanche Energy ①              | 0.31                  | mJ    |
| dv/dt                             | Peak Diode Recovery dv/dt ③                | 5.5                   | V/ns  |
| $T_J, T_{STG}$                    | Junction and Storage Temperature Range     | -55 to +150           | °C    |
|                                   | Soldering Temperature, for 10 seconds      | 300 (1.6mm from case) |       |

### Thermal Resistance

|                 | Parameter                          | Typ. | Max. | Units |
|-----------------|------------------------------------|------|------|-------|
| $R_{\theta JC}$ | Junction-to-PCB                    | —    | 40   | °C/W  |
| $R_{\theta JA}$ | Junction-to-Ambient. (PCB Mount)** | —    | 60   |       |

\*\* When mounted on 1" square PCB (FR-4 or G-10 Material).

For recommended footprint and soldering techniques refer to application note #AN-994.

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1

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|                                 | Parameter                            | Min. | Typ. | Max. | Units               | Conditions                                                        |
|---------------------------------|--------------------------------------|------|------|------|---------------------|-------------------------------------------------------------------|
| $V_{(BR)DSS}$                   | Drain-to-Source Breakdown Voltage    | 100  | —    | —    | V                   | $V_{GS} = 0V, I_D = 250\mu A$                                     |
| $\Delta V_{(BR)DSS}/\Delta T_J$ | Breakdown Voltage Temp. Coefficient  | —    | 0.12 | —    | V/ $^\circ\text{C}$ | Reference to $25^\circ\text{C}$ , $I_D = 1mA$                     |
| $R_{DS(on)}$                    | Static Drain-to-Source On-Resistance | —    | —    | 0.54 | $\Omega$            | $V_{GS} = 5.0V, I_D = 0.90A$ ④                                    |
|                                 |                                      | —    | —    | 0.76 |                     | $V_{GS} = 4.0V, I_D = 0.75A$                                      |
| $V_{GS(th)}$                    | Gate Threshold Voltage               | 1.0  | —    | 2.0  | V                   | $V_{DS} = V_{GS}, I_D = 250\mu A$                                 |
| $g_{fs}$                        | Forward Transconductance             | 0.57 | —    | —    | S                   | $V_{DS} = 25V, I_D = 0.90A$                                       |
| $I_{DSS}$                       | Drain-to-Source Leakage Current      | —    | —    | 25   | $\mu A$             | $V_{DS} = 100V, V_{GS} = 0V$                                      |
|                                 |                                      | —    | —    | 250  |                     | $V_{DS} = 80V, V_{GS} = 0V, T_J = 125^\circ\text{C}$              |
| $I_{GSS}$                       | Gate-to-Source Forward Leakage       | —    | —    | 100  | nA                  | $V_{GS} = 10V$                                                    |
|                                 | Gate-to-Source Reverse Leakage       | —    | —    | -100 |                     | $V_{GS} = -10V$                                                   |
| $Q_g$                           | Total Gate Charge                    | —    | —    | 6.1  | nC                  | $I_D = 5.6A$                                                      |
| $Q_{gs}$                        | Gate-to-Source Charge                | —    | —    | 2.6  |                     | $V_{DS} = 80V$                                                    |
| $Q_{gd}$                        | Gate-to-Drain ("Miller") Charge      | —    | —    | 3.3  |                     | $V_{GS} = 5.0V$ , See Fig. 6 and 13 ④                             |
| $t_{d(on)}$                     | Turn-On Delay Time                   | —    | 9.3  | —    | ns                  | $V_{DD} = 50V$                                                    |
| $t_r$                           | Rise Time                            | —    | 47   | —    |                     | $I_D = 5.6A$                                                      |
| $t_{d(off)}$                    | Turn-Off Delay Time                  | —    | 16   | —    |                     | $R_G = 12\Omega$                                                  |
| $t_f$                           | Fall Time                            | —    | 18   | —    |                     | $R_D = 8.4\Omega$                                                 |
| $L_D$                           | Internal Drain Inductance            | —    | 4.0  | —    | nH                  | Between lead, 6mm(0.25in) from package and center of die contact. |
| $L_S$                           | Internal Source Inductance           | —    | 6.0  | —    |                     |                                                                   |
| $C_{iss}$                       | Input Capacitance                    | —    | 250  | —    | pF                  | $V_{GS} = 0V$                                                     |
| $C_{oss}$                       | Output Capacitance                   | —    | 80   | —    |                     | $V_{DS} = 25V$                                                    |
| $C_{rss}$                       | Reverse Transfer Capacitance         | —    | 15   | —    |                     | $f = 1.0MHz$ , See Fig. 5                                         |



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## Source-Drain Ratings and Characteristics

|          | Parameter                              | Min.                                                                        | Typ. | Max. | Units   | Conditions                                                     |
|----------|----------------------------------------|-----------------------------------------------------------------------------|------|------|---------|----------------------------------------------------------------|
| $I_S$    | Continuous Source Current (Body Diode) | —                                                                           | —    | 1.5  | A       | MOSFET symbol showing the integral reverse p-n junction diode. |
| $I_{SM}$ | Pulsed Source Current (Body Diode) ①   | —                                                                           | —    | 12   |         |                                                                |
| $V_{SD}$ | Diode Forward Voltage                  | —                                                                           | —    | 2.5  | V       | $T_J = 25^\circ\text{C}, I_S = 1.5A, V_{GS} = 0V$ ④            |
| $t_{rr}$ | Reverse Recovery Time                  | —                                                                           | 110  | 130  | ns      | $T_J = 25^\circ\text{C}, I_F = 5.6A$                           |
| $Q_{rr}$ | Reverse Recovery Charge                | —                                                                           | 0.50 | 0.65 | $\mu C$ | $di/dt = 100A/\mu s$ ④                                         |
| $t_{on}$ | Forward Turn-On Time                   | Intrinsic turn-on time is negligible (turn-on is dominated by $L_S + L_D$ ) |      |      |         |                                                                |

## Notes:

① Repetitive rating; pulse width limited by max. junction temperature. ( See fig. 11 )

②  $V_{DD} = 25V$ , starting  $T_J = 25^\circ\text{C}$ ,  $L = 25\text{ mH}$   
 $R_G = 25\Omega$ ,  $I_{AS} = 1.5A$ . (See Figure 12)③  $I_{SD} \leq 5.6A$ ,  $di/dt \leq 75A/\mu s$ ,  $V_{DD} \leq V_{(BR)DSS}$ ,  
 $T_J \leq 150^\circ\text{C}$ ④ Pulse width  $\leq 300\mu s$ ; duty cycle  $\leq 2\%$ .

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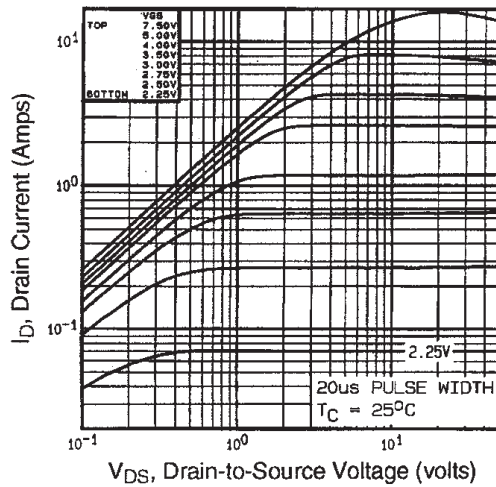


Fig 1. Typical Output Characteristics,

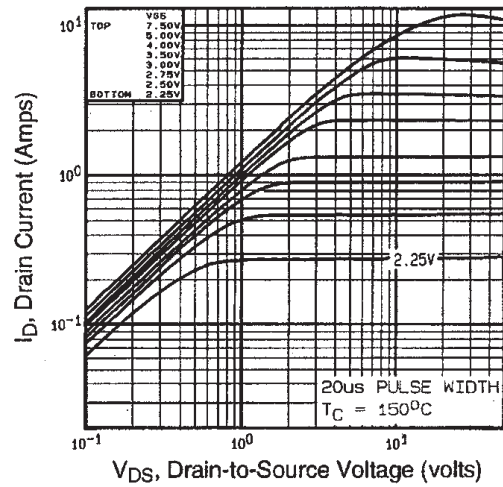


Fig 2. Typical Output Characteristics,

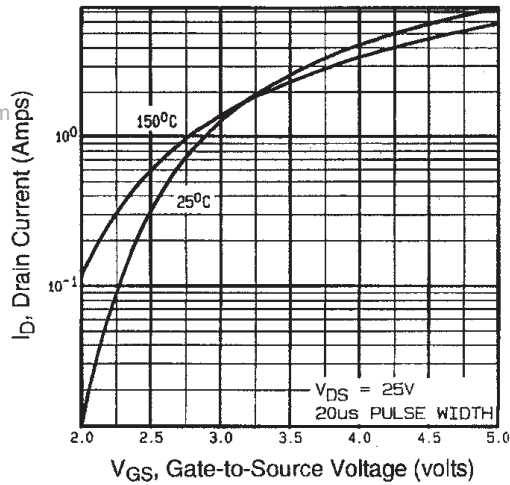
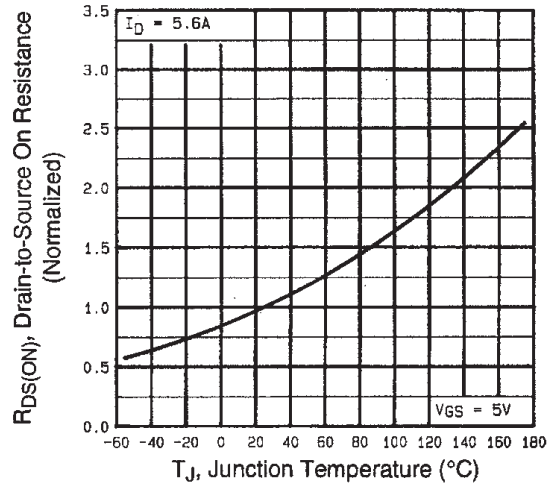
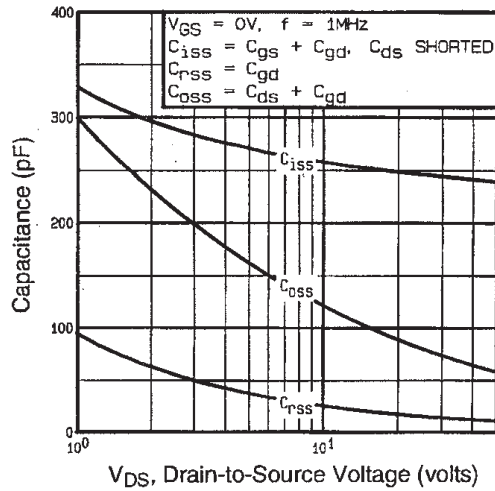


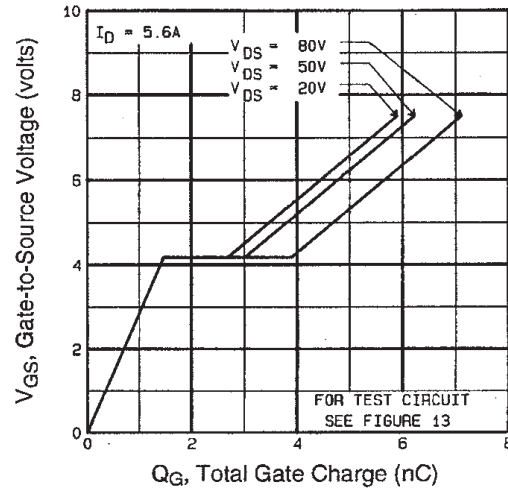
Fig 3. Typical Transfer Characteristics

Fig 4. Normalized On-Resistance  
Vs. Temperature

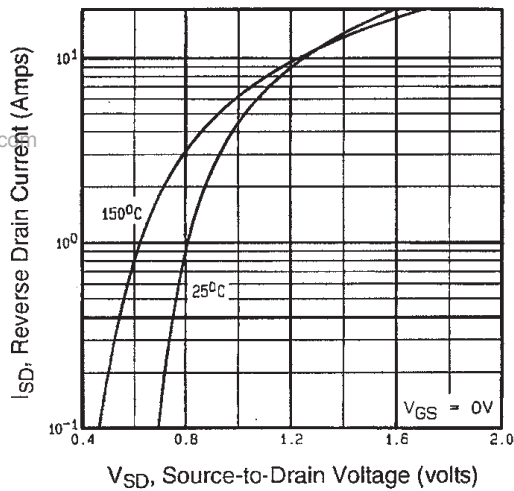
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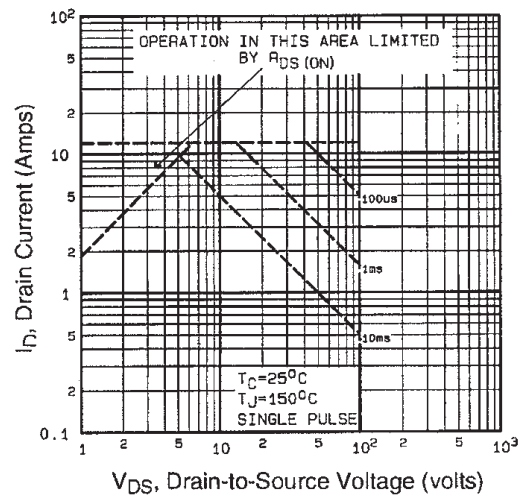
**Fig 5.** Typical Capacitance Vs. Drain-to-Source Voltage



**Fig 6.** Typical Gate Charge Vs. Gate-to-Source Voltage



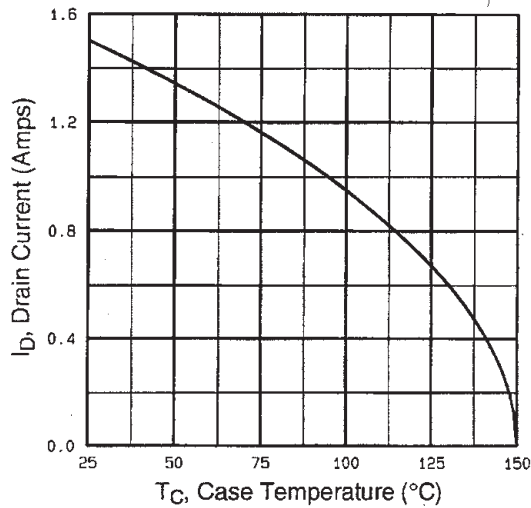
**Fig 7.** Typical Source-Drain Diode Forward Voltage



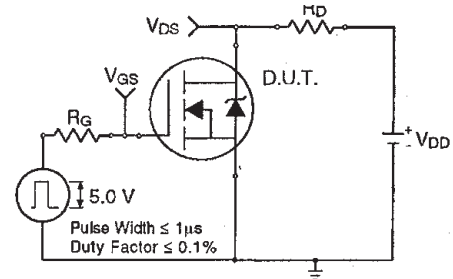
**Fig 8.** Maximum Safe Operating Area

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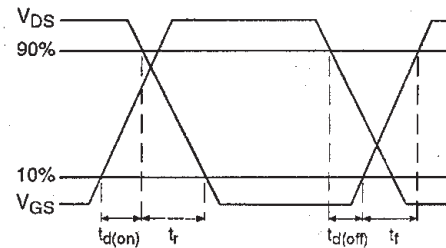
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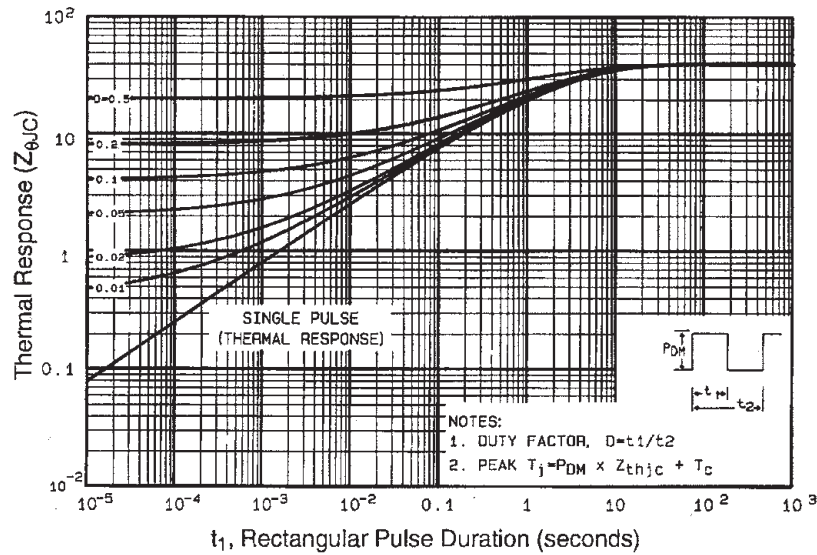
**Fig 9.** Maximum Drain Current Vs. Case Temperature



**Fig 10a.** Switching Time Test Circuit



**Fig 10b.** Switching Time Waveforms



**Fig 11.** Maximum Effective Transient Thermal Impedance, Junction-to-Case

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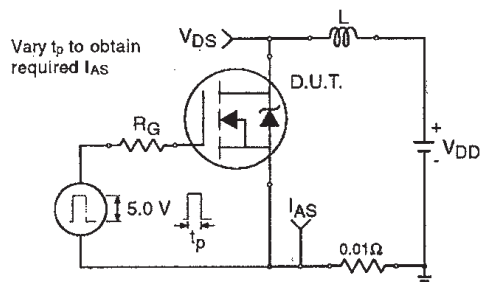
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Fig 12a. Unclamped Inductive Test Circuit

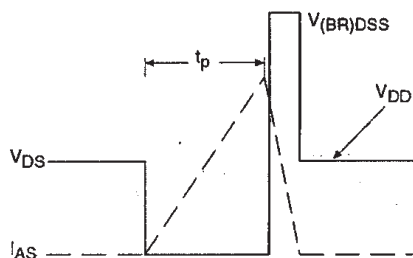


Fig 12b. Unclamped Inductive Waveforms

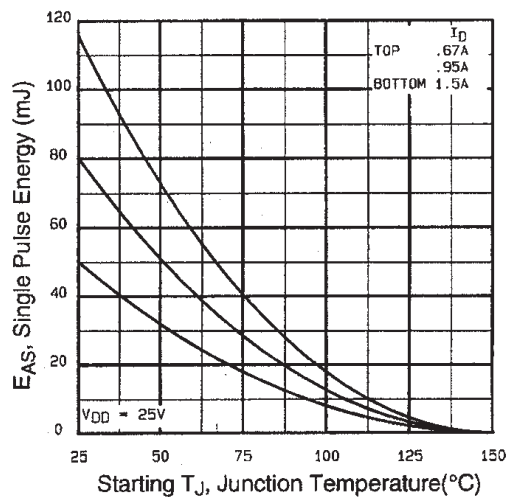
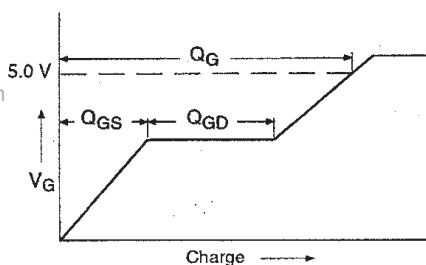
Fig 12c. Maximum Avalanche Energy  
Vs. Drain Current

Fig 13a. Basic Gate Charge Waveform

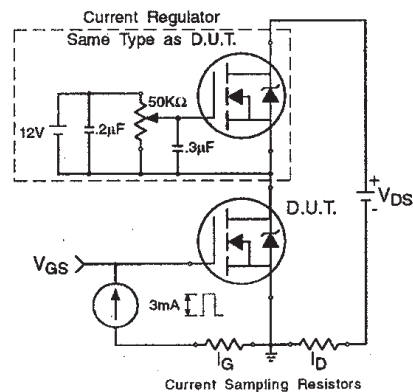


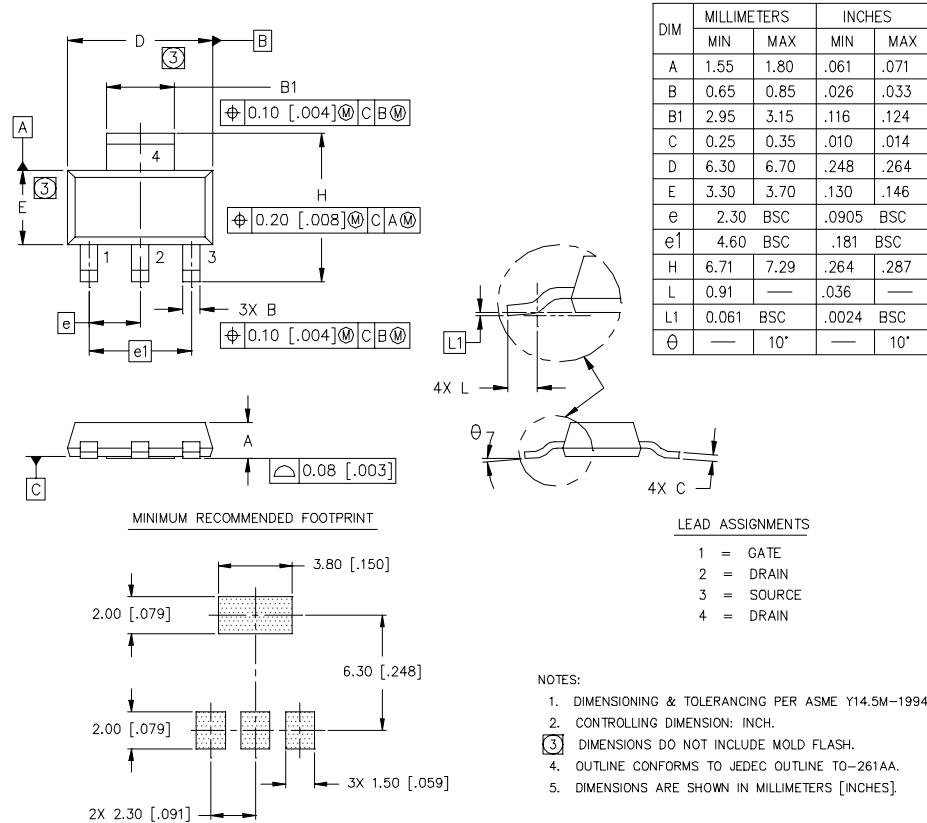
Fig 13b. Gate Charge Test Circuit

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## SOT-223 (TO-261AA) Package Outline

Dimensions are shown in millimeters (inches)

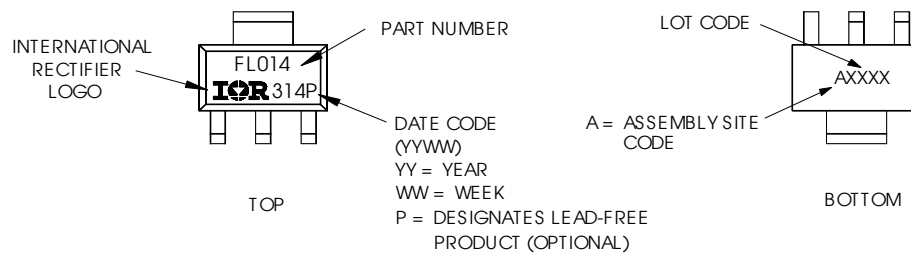


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## SOT-223 (TO-261AA) Part Marking Information

### HEXFET PRODUCT MARKING

EXAMPLE: THIS IS AN IRFL014



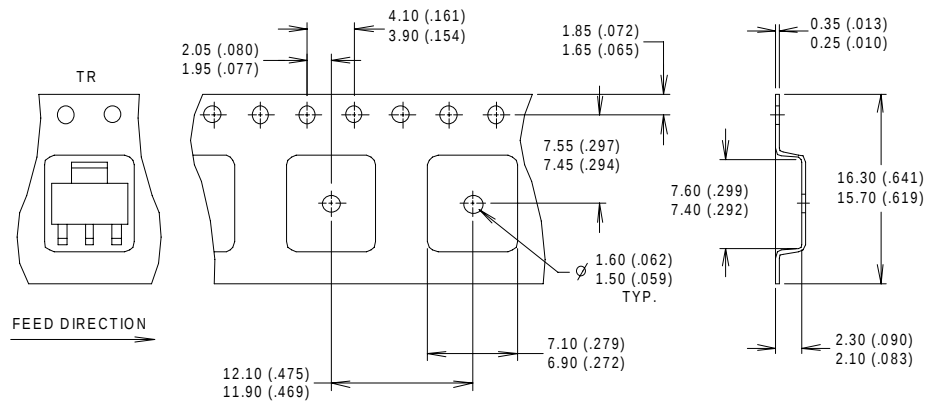
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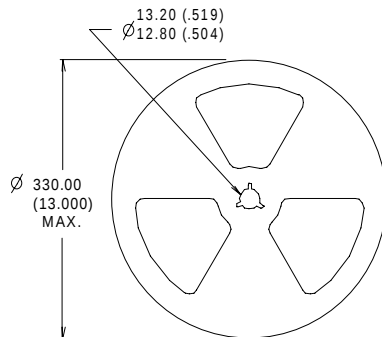
## SOT-223 (TO-261AA) Tape & Reel Information

Dimensions are shown in millimeters (inches)



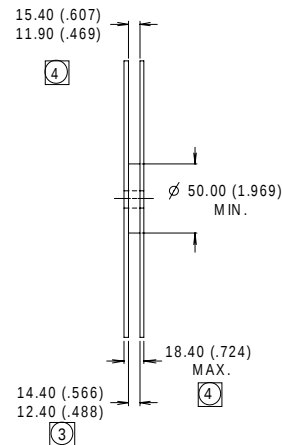
### NOTES :

1. CONTROLLING DIMENSION: MILLIMETER.
2. OUTLINE CONFORMS TO EIA-481 & EIA-541.
3. EACH  $\phi$ 330.00 (13.00) REEL CONTAINS 2,500 DEVICES.



### NOTES :

1. OUTLINE COMFORMS TO EIA-418-1.
2. CONTROLLING DIMENSION: MILLIMETER..
- ③ DIMENSION MEASURED @ HUB.
- ④ INCLUDES FLANGE DISTORTION @ OUTER EDGE.



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Data and specifications subject to change without notice.

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IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105

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