

**iscN-Channel MOSFET Transistor**
**IRLR120**
**• FEATURES**

- Low drain-source on-resistance:  
 $R_{DS(ON)} \leq 0.27\Omega @ V_{GS}=5V$
- Enhancement mode:  
 $V_{th} = 1.0 \text{ to } 2.0V (V_{DS} = 10V, I_D=0.25mA)$
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**• DESCRIPTION**

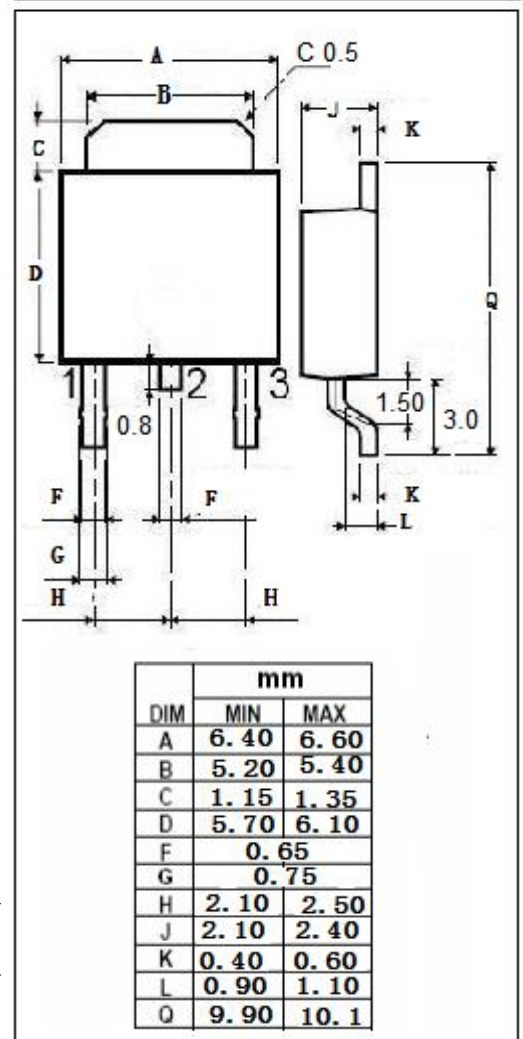
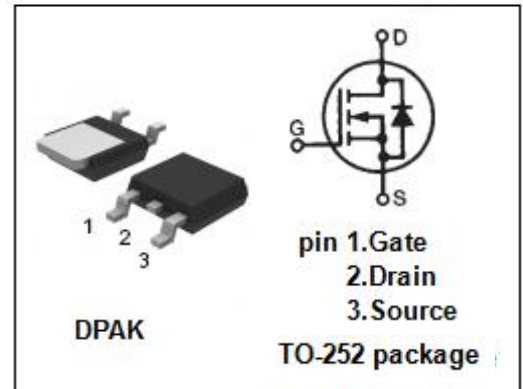
- Switching Voltage Regulators

**• ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ C$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DS}$	Drain-Source Voltage	100	V
$V_{GS}$	Gate-Source Voltage	$\pm 10$	V
$I_D$	Drain Current-Continuous	7.7	A
$I_{DM}$	Drain Current-Single Pulsed	31	A
$P_D$	Total Dissipation @ $T_c=25^\circ C$	42	W
$T_j$	Max. Operating Junction Temperature	-55~150	$^\circ C$
$T_{stg}$	Storage Temperature	-55~150	$^\circ C$

**• THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	3.0	$^\circ C/W$



## iscN-Channel MOSFET Transistor

## IRLR120

## ELECTRICAL CHARACTERISTICS

T<sub>c</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> = 0.25mA	100			V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = 10V; I <sub>D</sub> =0.25mA	1.0		2.0	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =5V; I <sub>D</sub> =4.6A V <sub>GS</sub> =4V; I <sub>D</sub> =3.9A			0.27 0.38	Ω
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ± 10V; V <sub>DS</sub> = 0V			± 100	nA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =100V; V <sub>GS</sub> = 0V V <sub>DS</sub> =80V; V <sub>GS</sub> = 0V; T <sub>J</sub> =125°C			25 250	uA
V <sub>SDF</sub>	Diode forward voltage	I <sub>DR</sub> =7.7A, V <sub>GS</sub> = 0 V			2.5	V

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