

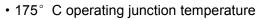
## isc P-Channel MOSFET Transistor

# IRLR9343, IIRLR9343

#### • FEATURES

- Static drain-source on-resistance: R<sub>D</sub>s(on)≤105mΩ
- Enhancement mode:
- · 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

#### DESCRITION



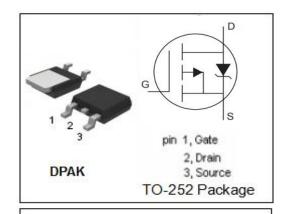
· Repetitive avalanche capability

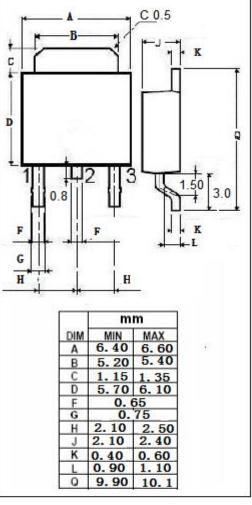
### • ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V <sub>DSS</sub>	Drain-Source Voltage	-55	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
Ι <sub>D</sub>	Drain Current-Continuous	-20	Α
$I_{DM}$	Drain Current-Single Pulsed	-60	Α
$P_D$	Total Dissipation @T <sub>C</sub> =25°C	79	W
Tj	Max. Operating Junction Temperature	175	$^{\circ}$
T <sub>stg</sub>	Storage Temperature	-40~175	$^{\circ}$ C

### • THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
Rth(j-c)	Channel-to-case thermal resistance	1.9	°C/W
Rth(j-a)	Channel-to-ambient thermal resistance	110	°C/W







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#### **ELECTRICAL CHARACTERISTICS**

T<sub>C</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	ТҮР	MAX	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> = -0.25mA	-55			V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> ; I <sub>D</sub> = -0.25mA	-1.0			V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> = -10V; I <sub>D</sub> =-3.4A			105	mΩ
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±20V			±0.1	μ <b>A</b>
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> = -55V; V <sub>GS</sub> = 0V			-2	μ <b>А</b>
V <sub>SD</sub>	Diode forward voltage	Is= -14A, V <sub>GS</sub> = 0V			-1.2	V

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