

#### 1.0MHZ BOOST CONVERTER WITH 35V INTERNAL MOSFET SWITCH

April 2024

#### **GENERAL DESCRIPTION**

IS31BL3506B is a boost converter featuring an integrated MOSFET switch designed for driving series strings of LED or other device. Input operating voltage range is 2.7V~5.5V. The device regulates the LED load current is set using an external, low value sensing resistor. The device adjusts the PWM duty cycle of the switch to maintain the voltage at the FB pin at a value of 0.2V. The IS31BL3506B operates at a constant switching frequency of 1.0MHz to allow for small component size. IS31BL3506B features an over voltage shutdown pin which prevents the output voltage from exceeding 35V (typ.) in the case of an open circuit condition. The device is offered in a tiny TSOT-23-6 and SOT-23-6 packages temperature rating from -40°C ~ +85°C.

#### **FEATURES**

- Supply voltage: 2.7V~5.5V
- Built-in soft start
- Drive series strings of 8 LEDs
- PWM or DC voltage dimming
- Frequency of PWM is 500Hz ~ 500kHz
- 1.0MHz high speed switching frequency
- 200mV feedback voltage
- Integrated high power MOSFET
- Over voltage protection
- -40°C ~ +85°C working temperature range
- TSOT-23-6 and SOT-23-6 packages
- RoHS & Halogen-Free Compliance
- TSCA Compliance

#### **APPLICATIONS**

- Small LCD Backlights
- Notebook PC
- GPS

#### TYPICAL APPLICATION CIRCUIT

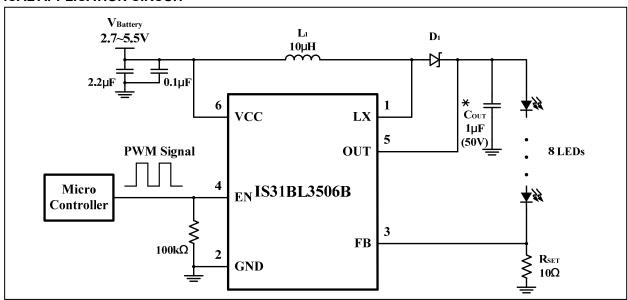


Figure 1 Constant Current White LED Driver Using PWM At The EN Pin To Control Intensity

Note 1: Cour must be rated for 50V. Smaller values of output capacitance will cause the LED current ripple to increase.

Note 2: The resistor R<sub>EST</sub> should be placed as close as possible to the FB pin to prevent the EMI.

Note 3: The high level of PWM signal for EN pin should last at least 200ns when power on, or the IC won't start up.



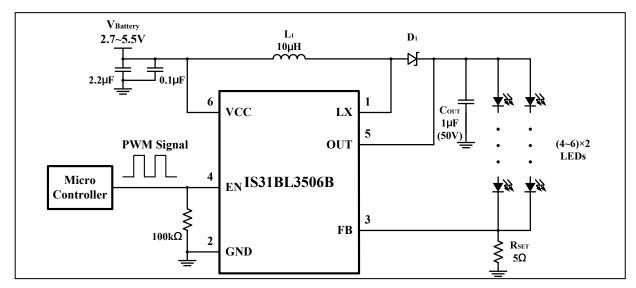


Figure 2 Constant Current White LED Driver Using PWM At The EN Pin To Control Intensity (4~6)×2 LEDs

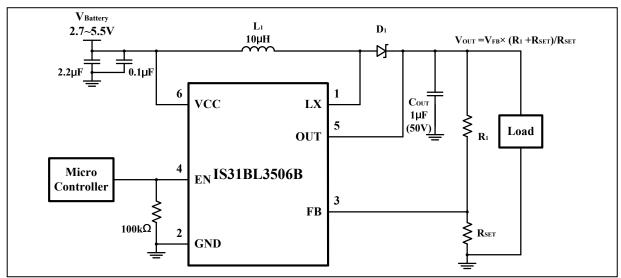


Figure 3 Constant Output Voltage For General Purpose Use

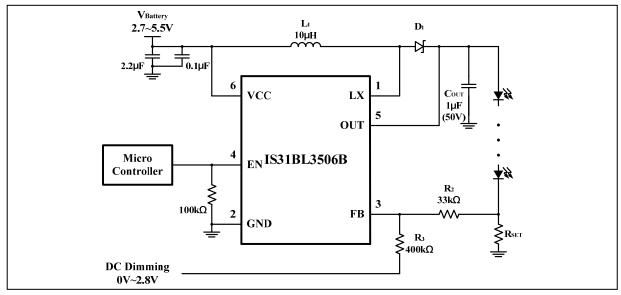


Figure 4 Constant Output Current With DC Dimming Input



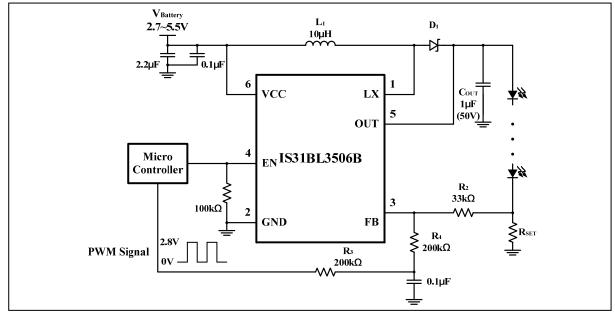


Figure 5 Constant Output Current With PWM Dimming Control



## **PIN CONFIGURATION**

Package	Pin Configurations (Top View)		
TSOT-23-6 SOT-23-6	LX		

## PIN DESCRIPTION

No.	Pin	Description
1	LX	Switch input, connect to the inductor.
2	GND	Ground.
3	FB	Feedback input pin.
4	EN	Device enable pin, active high.
5	OUT	Output voltage sense pin for over voltage protection.
6	VCC	Supply voltage input pin.



ORDERING INFORMATION Industrial Range: -40°C to +85°C

Order Part No.	Package	QTY/Reel
IS31BL3506B-TTLS2-TR	TSOT-23-6, Lead-free	3000
IS31BL3506B-STLS2-TR	SOT-23-6, Lead-free	3000

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#### **ABSOLUTE MAXIMUM RATINGS**

Supply voltage, Vcc	-0.3V ~ +6.0V
LX pin voltage	-0.3V ~ +40V
Voltage at any input pin	-0.3V ~ +6.0V
Maximum junction temperature, T <sub>JMAX</sub>	+150°C
Storage temperature range, T <sub>STG</sub>	-65°C ~ +150°C
Operating temperature range, T <sub>A</sub>	-40°C ~ +85°C

**Note:** Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other condition beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### **ELECTRICAL CHARACTERISTICS**

Test conditions:  $T_A = -40$ °C ~ +85°C,  $V_{CC} = 3.6$ V, unless otherwise noted. Typical values at  $T_A = 25$ °C.

Symbol	Parameter	Condition	Min	Тур	Max	Unit
Vcc	Supply voltage		2.7		5.5	V
Icc	Quiescent current	Switching		1.2		mA
I <sub>SD</sub>	Shutdown current	$V_{EN} = 0V$		5.5		μA
V <sub>EN_ON</sub>	EN Input logic High	V <sub>EN</sub> rising	1.4			V
V <sub>EN_OFF</sub>	EN Input logic Low	V <sub>EN</sub> falling			0.4	V
Fosc	Operating frequency			1		MHz
D <sub>MAX</sub>	Maximum duty cycle			90		%
R <sub>DS_ON</sub>	LX Switch On resistance			0.8		Ω
I <sub>SW_LK</sub>	LS Switch Leakage current	V <sub>SW</sub> = 35V			1	μA
Isw_LIMIT	Switch over current limit	Duty = 90%	1.8	2.2	2.6	Α
V <sub>OVP</sub>	OUT over voltage protection			35		V
$V_{FB}$	Feedback voltage			0.2		V
Tst	Over temperature protection	(Note 1)		160		°C
t <sub>PWM_H</sub>	EN pin PWM high level time (when power on)	(Note 1)	200			ns

Note 1: Guaranteed by design.



#### TYPICAL PERFORMANCE CHARACTERISTIC

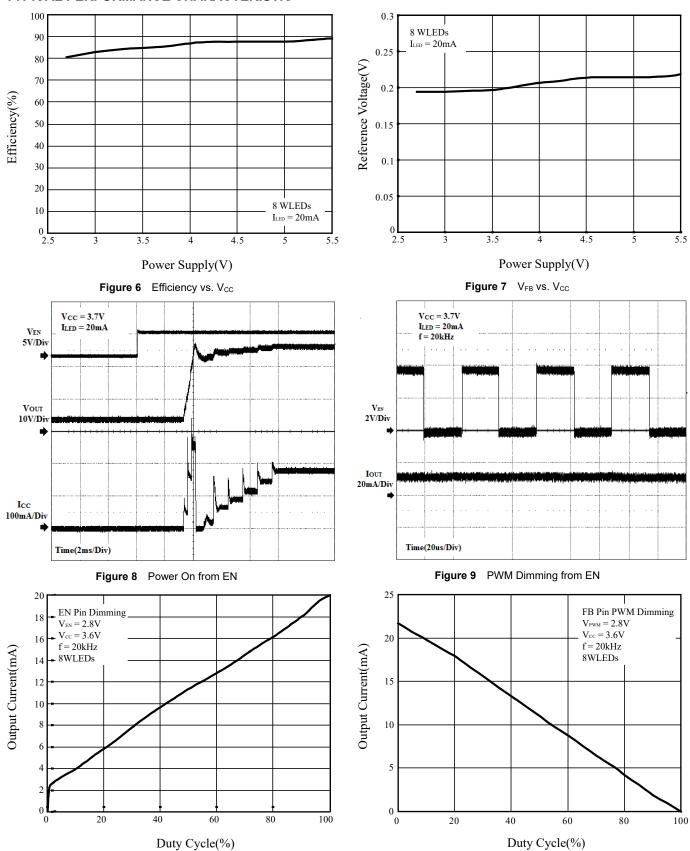


Figure 10 Enable Pin Dimming

Figure 11 FB pin PWM Dimming



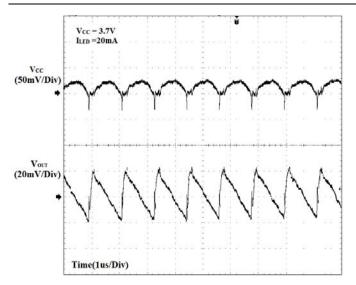
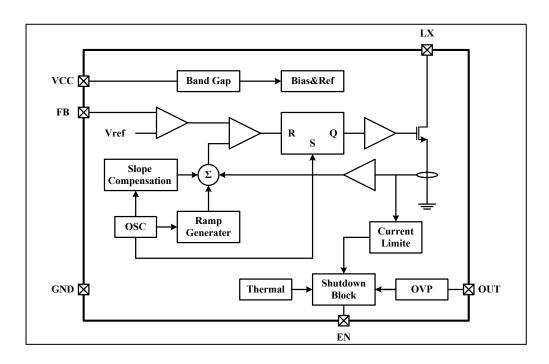


Figure 12 Ripple Voltage



## **FUNCTIONAL BLOCK DIAGRAM**



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#### **APPLICATION INFORMATION**

#### INPUT AND OUTPUT CAPACITOR

A 2.2µF input capacitor is used to reduce input ripple and noise, where the input ripple amplitude is inversely proportional to the value of the input capacitance. The input and output capacitors should be placed as close to the device as possible, so as to reduce the effect of voltage ripple. The value of the output capacitor is dependent upon the output ripple. A 1µF or 0.22µF ceramic capacitor with a 50V rating is sufficient for the majority of applications.

#### **INDUCTOR VALUE**

The choice of the inductor value will affect the performance of the device. Large value inductors will reduce the current ripple in the inductor, thus reducing output voltage ripple. However large value inductors of the same current rating, typically also have larger DC resistance, thus reducing the system efficiency. For the majority of applications 4.7µH or 10µH is sufficient.

#### LED CURRENT SETPOINT

When the IS31BL3506B is used for LED driving applications (Figure 1), the LED current flows through the external feedback resistor R<sub>SET</sub> to regulate the LED current. The internal feedback regulation point is 0.2V. The maximum LED current may be calculated by the following equation, (1).

$$I_{MAX} = V_{FB} / R_{SET} \tag{1}$$

For accurate LED current, resistors should be 1% or better tolerance.

#### LED INTENSITY CONTROL

#### **EN PIN DIMMING**

The EN pin of the IS31BL3506B can accept a PWM signal to implement LED dimming (Figure 1). LED current may be computed using the following equation, (2).

$$I_{LED} = I_{MAX} \cdot D_{PWM} \tag{2}$$

 $I_{MAX}$  is computed using equation (1). Recommended PWM frequency is in the range of 500Hz ~ 500kHz. So as to guarantee a reasonably good dimming effect, the minimum duty cycle should be about 1%.

Driving the EN pin with a PWM signal can effectively adjust the LED intensity. The PWM signal voltage levels must meet the EN pin input voltage levels,  $V_{\text{EN ON}}$  and  $V_{\text{EN OFF}}$ .

IS31BL3506B can also use a DC voltage or PWM signal to directly control the LED current, and thus provide fine adjustment for the LED intensity.

#### **FB PIN DC VOLTAGE DIMMING**

The schematic of Figure 4 shows an example of using a DC input voltage at the FB pin to implement LED dimming. When the DC input voltage increases, the current through  $R_2$  and  $R_3$  increases producing an offset voltage across  $R_2$ , thereby causing the LED current to be reduced. The LED current may be computed using the following equation, (3).  $V_{FB}$  is 0.2V (Typ.).

$$I_{LED} = \frac{V_{FB} - \frac{R_2 \times (V_{DC} - V_{FB})}{R_3}}{R_{SET}}$$
(3)

When the DC voltage range is  $0V\sim1.8V$ , a good value for  $R_2$  is  $56k\Omega$ , and for  $R_3$  is  $400k\Omega$ .

#### **FB PIN PWM DIMMING**

A PWM signal connected to the FB pin can also be used to directly control the LED current (Figure 5). A low pass filter converts the PWM duty cycle to a DC level, which is in turn summed together with the feedback voltage of RSET to effectively reduce the LED current proportionally. Due to the filtering of the PWM signal, the frequency of the PWM signal can have an affect on the output voltage ripple. For a PWM signal input signal of 2.8V, the recommended frequency is greater than 2kHz. Using a fixed frequency PWM signal and adjusting the PWM duty cycle effectively adjusts the LED intensity. LED current may be computed using the following equation, (4).  $V_{\text{FB}}$  is 0.2V (Typ.).

$$I_{LED} = \frac{V_{FB} - \frac{R_2 \times (V_{PWM} \times Duty - V_{FB})}{R_4 + R_3}}{R_{SET}}$$
(4)

For a PWM signal range of  $0V\sim1.8V$ , a suitable value for  $R_2$  is  $56k\Omega$ ,  $R_3$  is  $200k\Omega$ ,  $R_4$  is  $200k\Omega$ . To ensure good dimming effect, the minimum duty cycle is about 10%.

#### **SETTING THE OUTPUT VOLTAGE**

The IS31BL3506B can be configured to provide a constant output voltage (Figure 3). The output voltage level can be computed using the following equation, (5). The device internal  $V_{FB}$  is 0.2V (Typ).

$$V_{OUT} = V_{FB} \times (R_1 + R_{SET}) / R_{SET}$$
 (5)

### **OVERVOLTAGE THRESHOLD**

Device open circuit protection is realized using the overvoltage protection function (OVP). If the output voltage surpasses the overvoltage threshold, the overvoltage protection circuit is activated and the device will stop working.



## **CLASSIFICATION REFLOW PROFILES**

Profile Feature	Pb-Free Assembly
Preheat & Soak Temperature min (Tsmin) Temperature max (Tsmax) Time (Tsmin to Tsmax) (ts)	150°C 200°C 60-120 seconds
Average ramp-up rate (Tsmax to Tp)	3°C/second max.
Liquidous temperature (TL) Time at liquidous (tL)	217°C 60-150 seconds
Peak package body temperature (Tp)*	Max 260°C
Time (tp)** within 5°C of the specified classification temperature (Tc)	Max 30 seconds
Average ramp-down rate (Tp to Tsmax)	6°C/second max.
Time 25°C to peak temperature	8 minutes max.

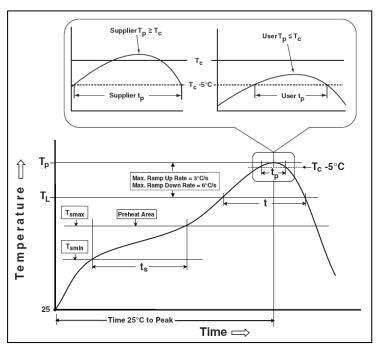
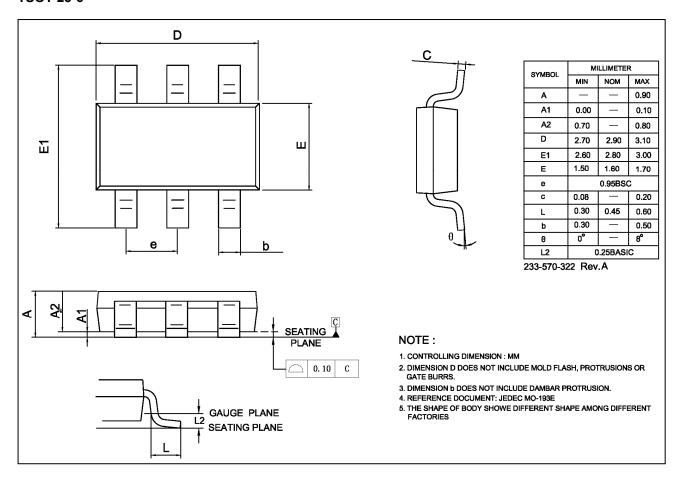


Figure 13 Classification Profile



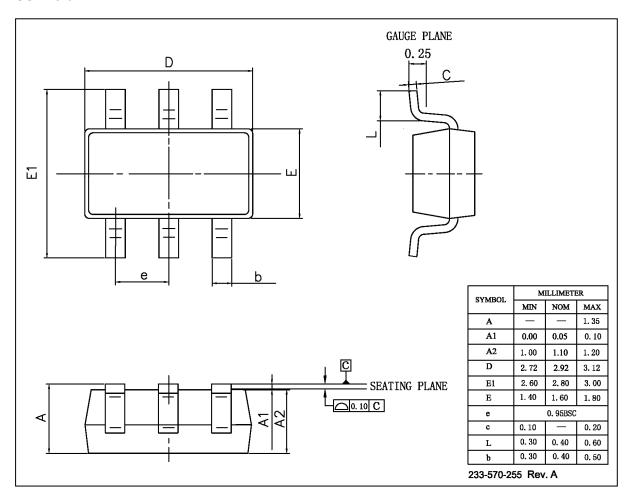
#### **PACKAGE OUTLINE DRAWING**

#### **TSOT-23-6**





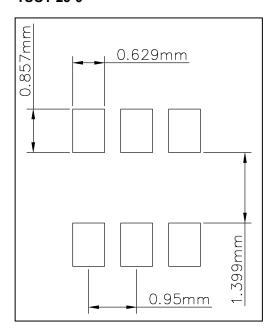
#### SOT-23-6



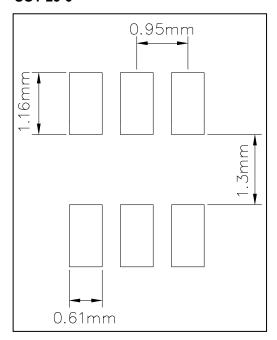


#### **RECOMMENDED LAND PATTERN**

#### **TSOT-23-6**



#### SOT-23-6



#### Note

- 1. Land pattern complies to IPC-7351.
- 2. All dimensions in MM.
- 3. This document (including dimensions, notes & specs) is a recommendation based on typical circuit board manufacturing parameters. Since land pattern design depends on many factors unknown (eg. user's board manufacturing specs), user must determine suitability for use.



## **REVISION HISTORY**

Revision	Detail Information	Date
Α	Initial release	2013.04.15
В	1.Update to new Lumissil logo     2.Update POD and add LP, RoHS	2024.04.11