

# ISG56525

## 5 TO 65 MHz SILICON CATV 25 dB HYBRID AMPLIFIER



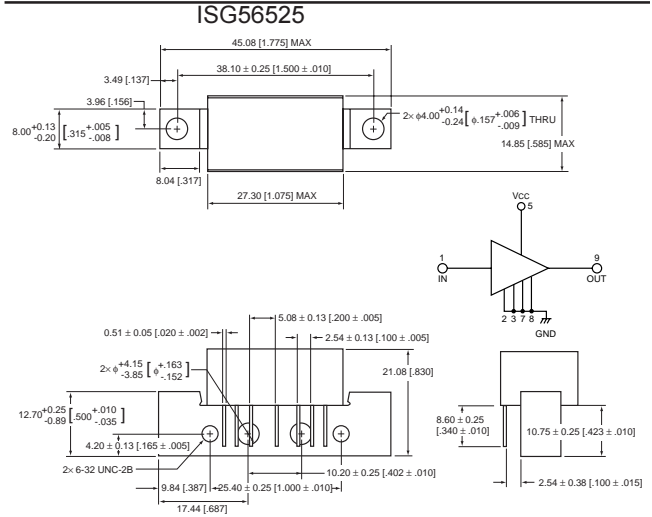
### FEATURES

- FLAT GAIN RESPONSE FROM 5 TO 65 MHz:  $f = \pm 0.2$  dB
- INPUT AND OUTPUT MATCHING TO 75 OHMS:  $R_L = > 19$  dB
- LOW DISTORTION:  $P_{1dB} = 78$  dBmV
- LOW NOISE: 3.1 dB
- AUTOMATED SURFACE MOUNT CONSTRUCTION

### DESCRIPTION

The ISG56525 is a low noise, low distortion hybrid amplifier specified for use in return path HFC Cable TV applications. The ISG56525 is comprised of 100% surface mount components, including high performance silicon transistors. It features excellent noise, gain, and thermal stability across a wide range of operating conditions and frequencies. The amplifiers are manufactured to ISO9002 standards are very rugged and exhibit excellent unit to unit uniformity.

### OUTLINE DIMENSIONS (Units in mm [inches])



### ELECTRICAL CHARACTERISTICS ( $V_{CC} = 24$ V, $\pm 10\%$ $T_A = 25^\circ\text{C}$ , 75 $\Omega$ System)

PART NUMBER				ISG56525		
SYMBOLS	PARAMETERS	CONDITIONS	UNITS	MIN	TYP	MAX
GA	Gain		dB	24.5	25.2	26
$\Delta G$	Gain Flatness		dB			$\pm 0.15$
RLIN	Input Return Loss		dB	20	25	
RLOUT	Output Return Loss		dB	19	21	
NF	Noise Figure		dB		3.1	3.5
CTB	Composite triple beat	See Note 1	dBc		-64	-60
XM	Cross Modulation (+50 dBmV/CH)	See Note 1	dBc		-54	-51
CSO	2nd Order Intermodulation Product	See Note 1	dBc		-70	-65
$P_{1dB}$	Output Power at 1dB Gain Compression point		dBmV	75	76	
$V_{CC}$	Supply Voltage		V		24	
$I_{OP}$	Operating Current		mA	105	117	130
BW	Bandwidth		MHz	5		65
$\Omega$	Input & Output Impedance		ohms		75	

Note:

1. Composite Triple Beat, Cross Modulation, 2nd Order Distortion are all measured with 7 channels (T7 through T13) at 50 dBmV/ch output and at 25°C.

Performance tests and ratings for Sirenza Microdevices' products were performed internally by Sirenza and measured using specific computer systems and/or components and reflect the approximate performance of the products as measured by those tests. Any difference in circuit implementation, test software, or test equipment may affect actual performance. The information provided herein is believed to be reliable at press time and Sirenza Microdevices assumes no responsibility for the use of this information. All such use shall be entirely at the user's own risk. Prices and specifications for Sirenza Microdevices' products are subject to change without notice. Buyers should consult Sirenza Microdevices' standard terms and conditions of sale for Sirenza's limited warranty with regard to its products. No patent rights or licenses to any of the circuits described herein are implied or granted to any third party. Sirenza Microdevices does not authorize or warrant any product for use in life-support devices and/or systems.

### ABSOLUTE MAXIMUM RATINGS<sup>1</sup>

(T<sub>c</sub> = 25 °C unless otherwise noted)

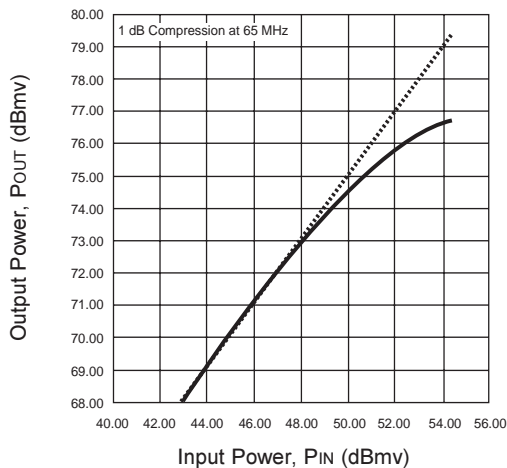
SYMBOLS	PARAMETERS	UNITS	RATINGS
V <sub>CC</sub>	DC Supply	V <sub>DC</sub>	+28
V <sub>IN</sub>	RF Input Voltage (Single Tone)	dBmV	+70
T <sub>c</sub>	Operating Case Temperature Range	°C	-40 to +100
T <sub>STG</sub>	Storage Temperature Range	°C	-40 to +100

Note:

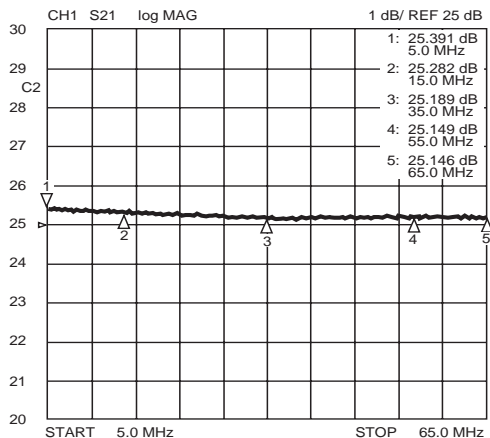
1. Operation in excess of any one of these parameters may result in permanent damage.

### TYPICAL PERFORMANCE CURVES (T<sub>A</sub> = 25°C)

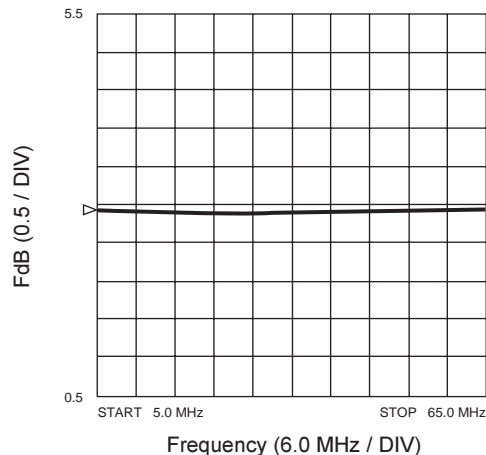
power in vs power out @ 65 MHz



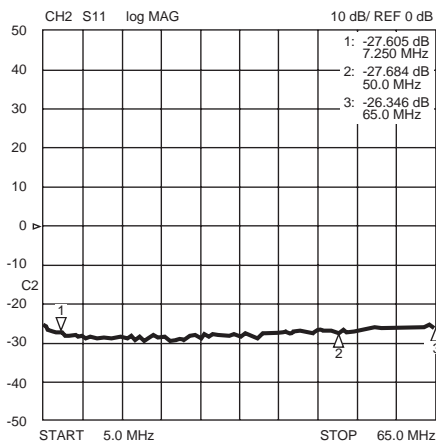
GAIN vs. FREQUENCY



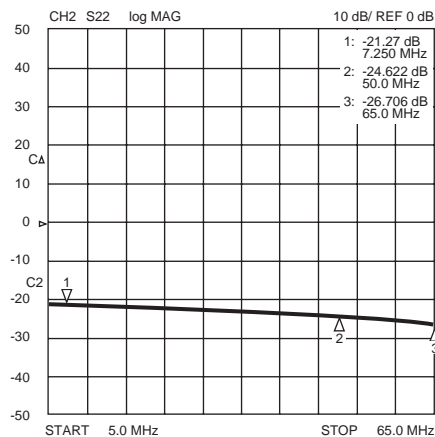
NOISE FIGURE



INPUT RETURN LOSS



OUTPUT RETURN LOSS



DATA SUBJECT TO CHANGE WITHOUT NOTICE