

IT7612

2.0A 380kHz 23V High Efficiency Step-Down DC/DC
Converter

Preliminary Specification V0.2.2

ITE TECH. INC.

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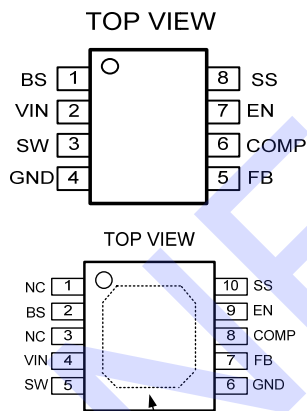
General Description

The IT7612 is a high efficiency step-down DC/DC converter series with the output current up to 2A and incorporates low on-resistance NMOS at the high side. Included on the substrate with the features listed is a high performance trans-conductance error amplifier that provides tight voltage regulation and accuracy under transient conditions. Embedded is Under Voltage Lockout (UVLO) circuit to prevent the device start-up under 4.5V input voltage threshold. In addition, it features an over-current protection and thermal shutdown. To improve the light load efficiency, it is designed as the power saving mode (PSM) to minimize the switching loss by reducing the switching frequency. The IT7612 is available in MSOP10EP & SOP8 package.

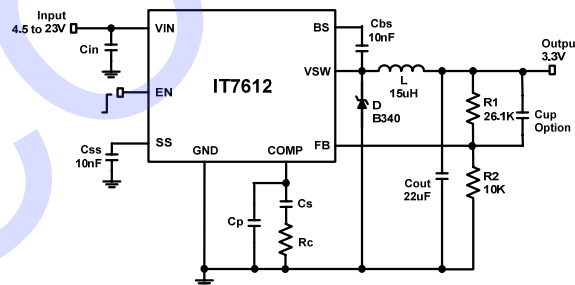
Application

- ◆ Set-Top-Box, DVD, LCD Display
- ◆ High Power LED Power Supply
- ◆ Distributed Power System
- ◆ Data comm. xDSL CPE

Pin Configuration



Typical Application



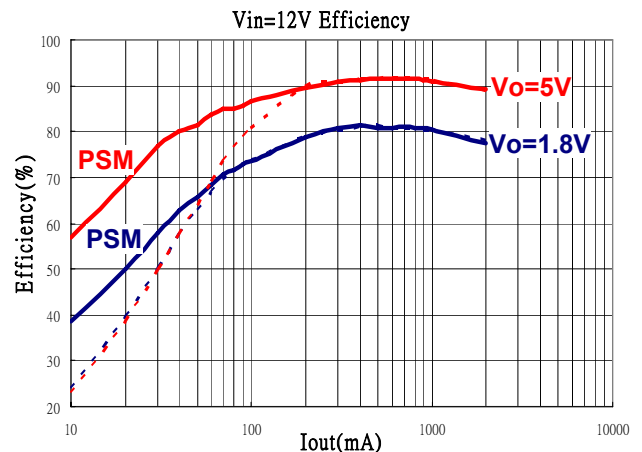
Features

- ◆ High Efficiency up to 93%
- ◆ Power Saving Mode (PSM) during the light Load Operation
- ◆ Wide Input Voltage Range from 4.5V to 23V
- ◆ Adjustable Output Voltage Range from 0.92V to 12V
- ◆ Output Current up to 2.0A
- ◆ 380kHz Constant Frequency Operation
- ◆ Current Mode Control
- ◆ Over-temperature Protection
- ◆ Over-current Protection
- ◆ Under Voltage Lockout (UVLO)
- ◆ 10μA Shutdown Current
- ◆ Internal Soft-Start
- ◆ MSOP10EP & SOP8 Package
- ◆ RoHS Compliant (100% Green available)

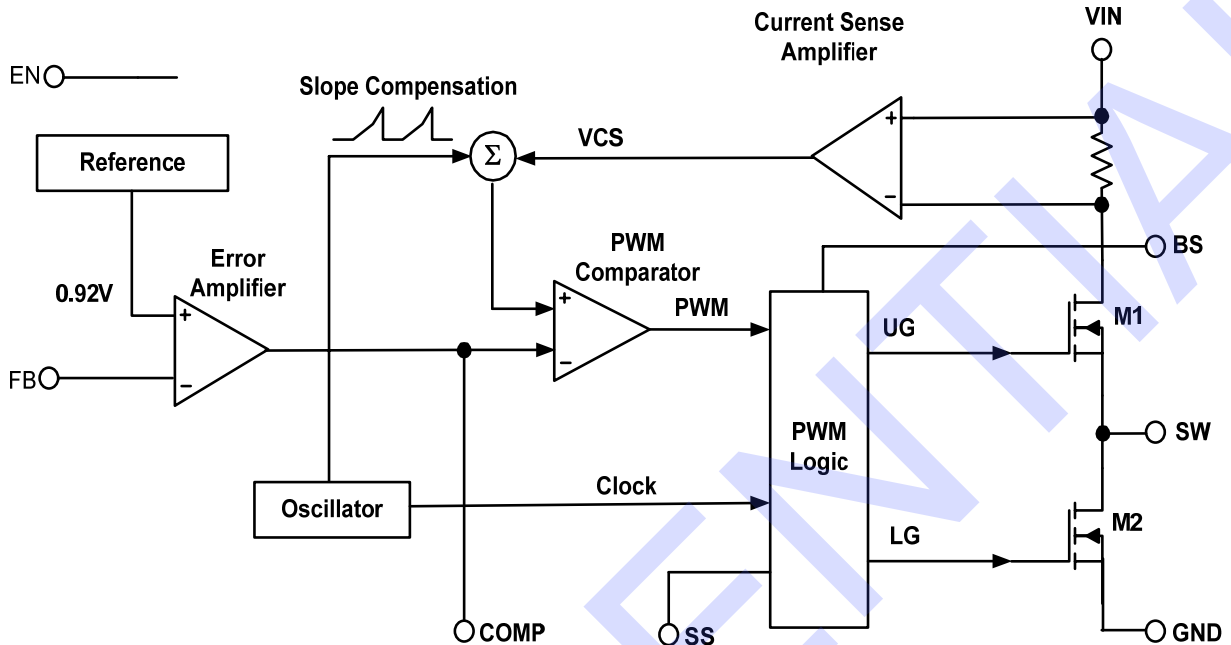
Ordering Information

Part No.	Package	Shipment
IT7612MS	MSOP10EP	3000/Tape & Reel
IT7612M	SOP8	2500/Tape & Reel

Efficiency Table



Block Diagram



Pin Description

Pin(s) No.	Symbol	Description
1	N/C	No Connection
2	BS	High Side Gate Drive Boost Input It is required to connect SW and BS by a capacitor, which is able to boost the gate drive to the internal NMOS above VIN to fully turn it ON.
3	N/C	No Connection
4	VIN	Power Supply This pin is connected to the input voltage for the power supply.
5	SW	Power Switch Output This is the output of a power MOSFET switch connected directly to the input voltage.
6	GND	Ground This is the reference of the ground connection for all components in the power supply.
7	FB	Voltage Feedback This is the input to an error amplifier, which drives the PWM controller. It is necessary to connect this pin to the actual output of power supply to set the DC output voltage.
8	COMP	Compensation This pin is to compensate the regulation control loop by connecting a series of RC network from COMP pin to GND pin.
9	EN	Enable This input provides an electrical ON/OFF control of the power supply. If the EN pin is open, it will be pulled high by the internal circuit.
10	SS	Soft-Start This pin is connected to an external capacitor, which is between the SS pin and GND to control soft-start time. This externally connected 0.1 μF capacitor is able to set the soft-start period to 9.2ms.

Absolute Maximum Ratings

Input Supply Voltage.....+26V
 SW Voltage..... - 1V to 27V
 <10ns..... (-5V TO 30V)
 EN Voltage.....- 1 to VIN+0.3V
 Other Pins.....- 0.3V to +6V
 Boost VoltageVsw+6V
 ESD Classification.....Class 2
 Junction Temperature Range..... 150°C
 Storage Temperature Range.....-65°C to 150°C
 Lead Temperature (Soldering 10s).....260°C

Package Thermal Characteristics

Power Dissipation at TA = 25°C

SOP8.....1.098W
 MSOP10EP.....1.25W

Thermal Resistance

	θJA	θJC
SOP8	91	43 °C/W
MSOP10EP	80	18 °C/W

Recommended Operating Conditions

Supply Voltage (VIN).....4.5V to 23V
 Operating Temperature.....-40°C to +85°C

Note 1: Stresses beyond above listed under absolute maximum ratings may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications are not implied. Exposure to absolute maximum rating conditions for extended periods may remain possibility to affect device reliability.

Note 2: Guaranteed by design, not production tested.

Note 3: For the measuring condition of θJA, it is under the natural convection at TA = 25°C and on a four-layer test board with highly effective thermal conductivity following the JEDEC 51-7 thermal measurement standard. As for the case position of θJC, it is on the exposed pad of the package.

DC Electrical Characteristics

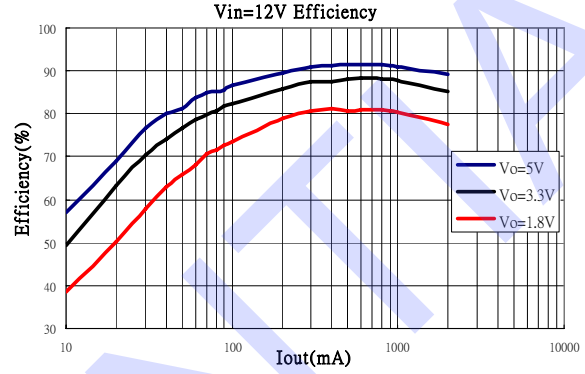
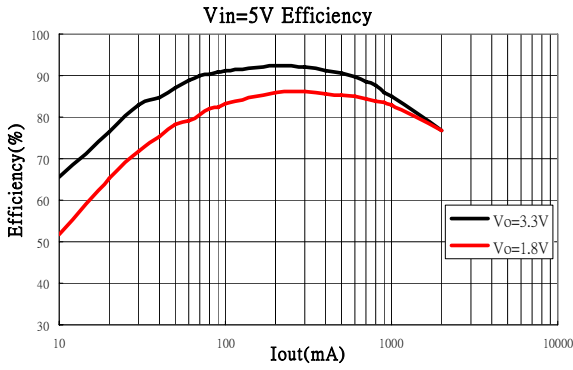
(Recommended Operating Conditions; unless otherwise noted; VIN = 12V; TA = 25 °C)

Parameter	Conditions	Min.	Typ.	Max.	Unit
Supply Voltage		4.5		23	V
Shutdown Supply Current	VEN = 0V		10		µA
Regulated Feedback Voltage	4.5V ≤ VIN ≤ 23V	0.892	0.92	0.948	V
Error Amplifier Transconductance	ΔICOMP = ±10µA		830		µA/V
Current Sense to COMP Transconductance			2.2		A/V
Current Limit			3.6		A
SW Leakage Current	VEN = 0V, VSW = 0V			10	µA
High Side On Resistance			0.25		Ω
Low Side On Resistance	I		30		Ω
Oscillation Frequency		320	380	460	kHz
Short Circuit Oscillation Frequency	VFB=0V		100		kHz
Maximum Duty Cycle	VFB=0.7V		90		%
Minimum Duty Cycle	VFB=1.1V			0	%
Minimum On Time			100		ns
Under Voltage Lockout Threshold	VIN Rising	3.9	4.2	4.3	V
Under Voltage Lockout Threshold Hysteresis			400		mV
Thermal Shutdown Threshold			160		°C
EN High Level			2.4		V
EN UVLO Threshold Hysteresis			200		mV
EN Low Level	Icc<100uA		1.5		V
EN Input Current	VEN = 0V	1.1	1.8	2.5	µA
Soft-Start Current	VSS=0V		10		µA
Soft-Start Period	Css=0.1uF		9.2		mS

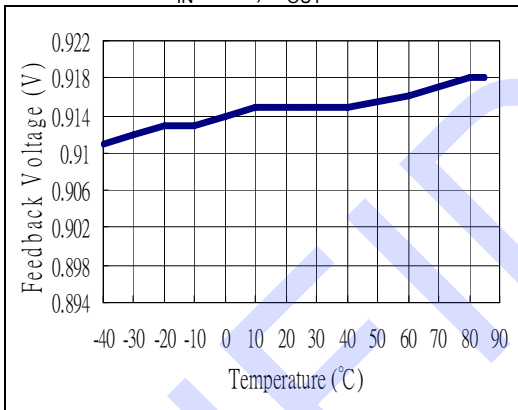
Note: Fully production test at +25°C. Specifications over the temperature range are guaranteed by design and characterization.

Typical Operation Characteristics

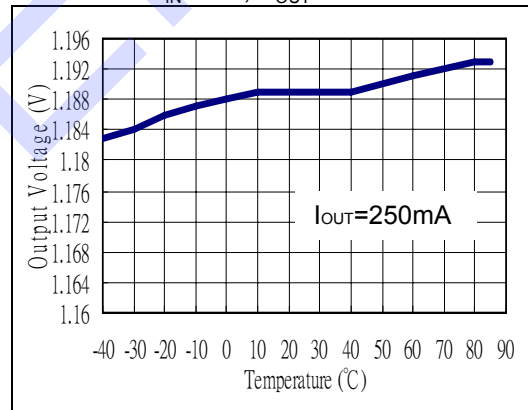
$V_{IN}=12V$, $C_{IN}=22\mu F$, $C_{OUT}=20\mu F$, $T_A=+25^\circ C$



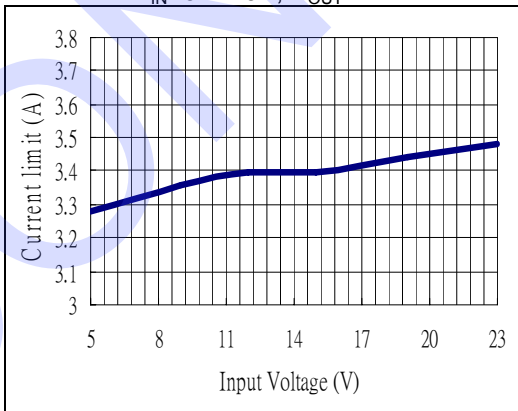
Feedback Voltage vs. Temperature
 $V_{IN}=12V$, $V_{OUT}=0.92V$



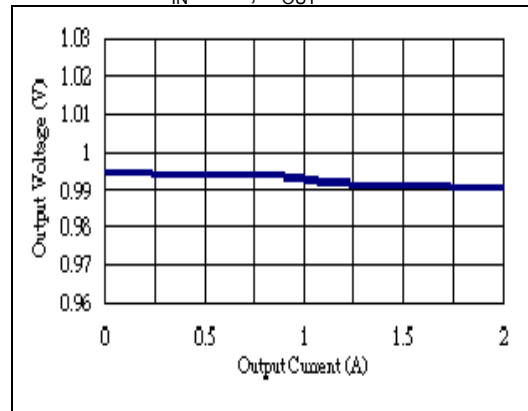
Output Voltage vs. Temperature
 $V_{IN}=12V$, $V_{OUT}=1.2V$



Current Limit vs. Input Voltage
 $V_{IN}=5V\sim 23V$, $V_{OUT}=1V$

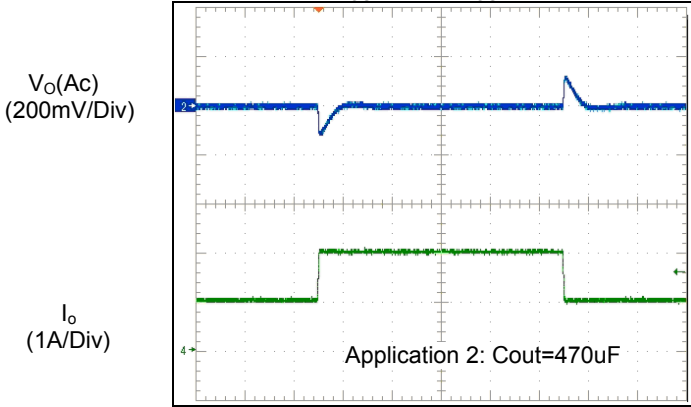


Load Regulation
 $V_{IN}=12V$, $V_{OUT}=1V$



Load Transient Response

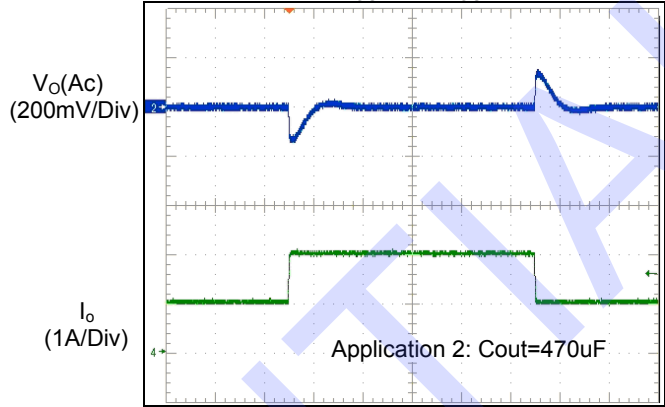
$V_{IN}=12V, V_{OUT}=3.3V, I_{OUT}=1A\sim 2A$



Time: (400uS/Div)

Load Transient Response

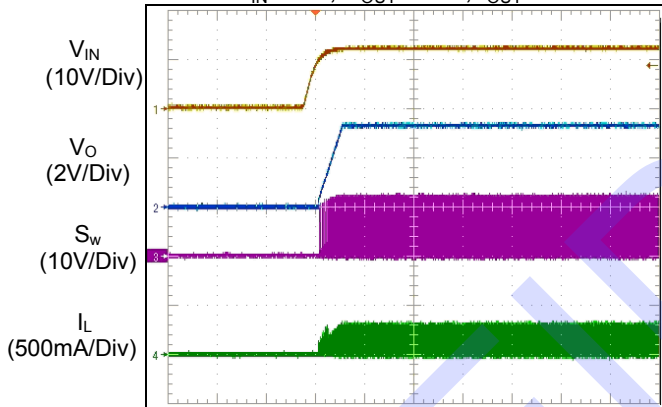
$V_{IN}=12V, V_{OUT}=5V, I_{OUT}=1A\sim 2A$



Time: (400uS/Div)

Power On

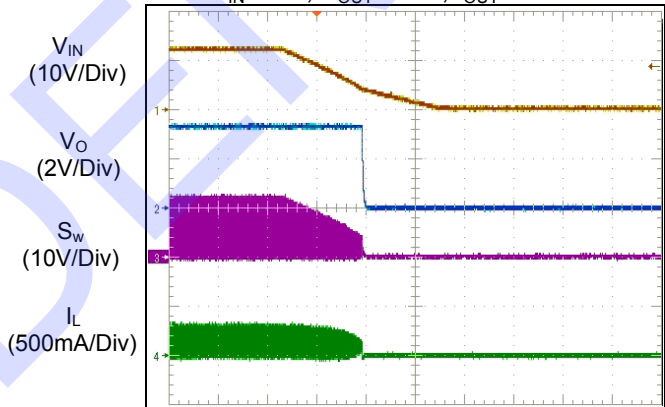
$V_{IN}=12V, V_{OUT}=3.3V, I_{OUT}=100mA$



Time: (20mS/Div)

Power Off

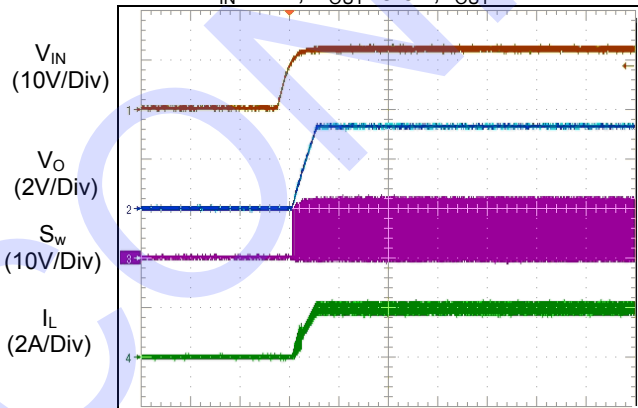
$V_{IN}=12V, V_{OUT}=3.3V, I_{OUT}=100mA$



Time: (20mS/Div)

Power On

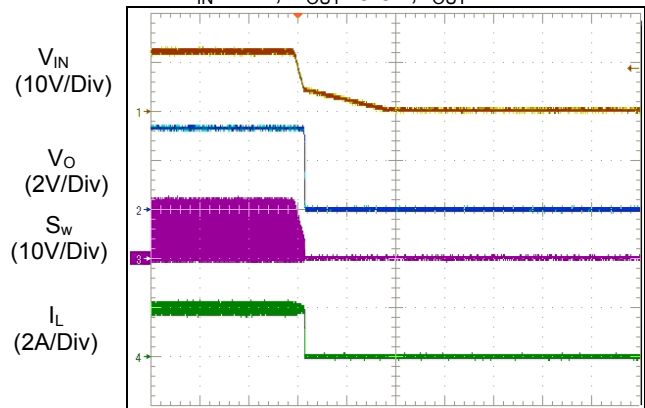
$V_{IN}=12V, V_{OUT}=3.3V, I_{OUT}=2A$



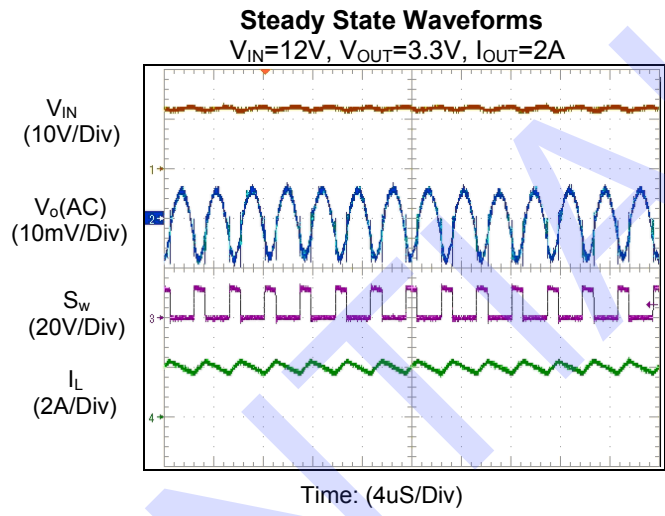
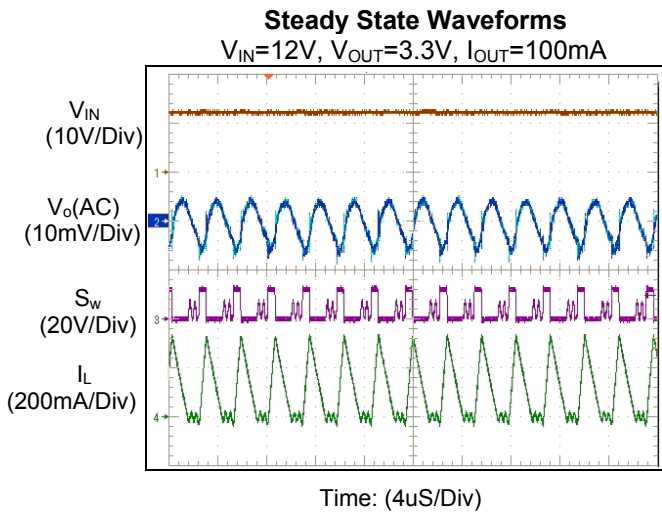
Time: (20mS/Div)

Power Off

$V_{IN}=12V, V_{OUT}=3.3V, I_{OUT}=2A$



Time: (20mS/Div)



Functional Description

The IT7612 is a current mode PWM step-down converter with a constant switching frequency. It regulates the input voltage from 4.5V to 23V and a low output voltage of 0.92V. The supplied load current is up to 2A.

Power Saving Mode

The switching losses resulted from the Miller capacitance of the MOSFET are the dominant power dissipation parameters at light load. The power saving mode at light load can minimize the switching loss by reducing the switching frequency. Therefore, the IT7612 is designed as the power saving mode for high efficiency at light load.

Oscillator Frequency

Slope compensated current mode PWM control provides not only stable switching and cycle-by-cycle current limit for superior load and line response but also protection of the internal main switch and synchronous rectifier. The IT7612 switches at a constant frequency (380 kHz) and regulates the output voltage. The PWM comparator modulates the power transferred to the load by changing the inductor's peak current based on the feedback error voltage during each cycle. The main switch is turned on for a certain period to ramp the inductor's current at each rising edge of the internal oscillator under normal operation whereas off when the inductor's peak current is above the error voltage. After the main switch is turned off, the low side MOS will be turned on immediately and stay on until the next cycle starts.

Short Circuit Protection

The IT7612 provides short circuit protection. When the output is shorted to ground, the oscillator's frequency is reduced to prevent the inductor's current from increasing beyond the NMOS current limit. The NMOS current limit is also reduced to lower the short circuit current. The frequency and current limit will return to the normal values once the short circuit condition is removed and the feedback voltage reaches 0.92V.

Maximum Load current

The IT7612 can operate down to 4.5V input voltage;

however the maximum load current decreases at lower input due to large IR voltage drop on the main switch and low side switch. The slope compensation signal reduces the inductor's peak current as a function of the duty cycle to prevent sub-harmonic oscillations at duty cycles greater than 50%.

Enable

The EN pin provides electrical on/off control of the regulator. Once the voltage of the EN pin exceeds the threshold voltage, the regulator starts operation and the internal slow start begins to ramp. If the voltage of the EN pin is pulled below the threshold, the regulator will stop switching and the internal slow start reset. Connecting this pin to ground or to any voltage less than 0.9V will disable the regulator and activate the shutdown mode.

Under Voltage Lockout (UVLO)

The IT7612 incorporates an Under Voltage Lockout circuit to keep the device disabled when VIN is below the UVLO start threshold. During power-up, the internal circuit is held inactive until VIN exceeds the UVLO start threshold voltage. Once this threshold voltage is reached, device start-up begins. The device operates until VIN falls below the UVLO stop threshold voltage. The typical hysteresis in the UVLO comparator is 400mV.

Soft-Start

The built-in soft-start function is to gradually raise the output voltage after power-on. The soft-start period can be set by the external capacitor, which is between the SS pin and GND. The IT7612 provides a 10uA charge current to this external capacitor. If 0.1uF capacitor is used to set the soft-start period, it will be 9.2ms (typ.).

$$T_{SS} = \frac{C_{SS} \times V_{ref}}{I_{SS}}$$

Boost Capacitor

The BS pin and SW pin can be connected by a 10nF low ESR ceramic capacitor, providing the gate drive voltage for the high side MOSFET.

Thermal Shutdown

The IT7612 protects itself from overheating with an internal thermal shutdown circuit. If the junction temperature exceeds the thermal shutdown threshold, the voltage reference will be grounded and high side MOSFET turned off.

Compensation

The system stability is controlled through COMP pin. It will present a general design procedure to ensure a stable and operational circuit. The design in this data sheet is optimized for particular requirements. Some components may need to be changed to ensure stability if there are different requirements. First of all, the power components and their corresponding effects need to be determined. Following are the compensation components, which are to produce stability.

The compensation steps for the converter are listed below:

- (1). Choose an appropriate inductor and output capacitance based on the allowed output voltage ripple and load transient.
- (2). Placing F_C as high as possible can respond quickly to the load transient. Considering the output capacitor's tolerances and temperature effects, typically place F_C approximately 1/10 of F_S for the multi-layer ceramic output capacitor (X5R, X7R). However, if the type of the output capacitor is the aluminum electrolytic or that largely variable with the temperature, place F_C approximately 1/20 of F_S .
- (3). Set the compensation R_C to zero to cancel the $R_{LOAD} C_{OUT}$ pole.

$$R_C = \frac{2\pi \times F_C \times C_{OUT} \times V_{OUT}}{G_M \times G_{CS} \times V_{REF}}$$

$$C_C = \frac{C_{OUT} \times R_{LOAD}}{R_C}$$

G_M : error amp transconductance

G_{CS} : current sense transconductance

- (4). Determine C_p if required.

If Z_{ESR} (zero occurs by output capacitor ESR) is less than F_C , it should be cancelled with a pole set by capacitor C_p connected between C_C and GND.

$$C_P = C_{OUT} \times \frac{R_{ESR}}{R_C}$$

Input Capacitor Selection

It is necessary for the input capacitor to sustain the ripple current produced during the period of "on" state of the upper MOSFET, so a low ESR is required to minimize the loss. The RMS value of this ripple can be obtained by the following:

$$I_{IN\text{RMS}} = I_{OUT} \sqrt{D \times (1 - D)}$$

where D is the duty cycle, $I_{in\text{RMS}}$ is the input RMS current, and I_{OUT} is the load current. The equation reaches its maximum value with $D = 0.5$. The loss of the input capacitor can be calculated by the following equation:

$$P_{CIN} = ESR_{CIN} \times I_{IN\text{RMS}}^2$$

where P_{CIN} is the power loss of the input capacitor and ESR_{CIN} is the effective series resistance of the input capacitance. Due to large dI/dt through the input capacitor, electrolytic or ceramics should be used. If a tantalum must be used, it must be surge-protected. Otherwise, capacitor failure could occur.

Application Information

Output Inductor Selection

The output inductor selection is to meet the requirements of the output voltage ripple and affects the load transient response. The higher inductance can reduce the inductor's ripple current and induce the lower output ripple voltage. The ripple voltage and current can be approximately calculated by the following equations:

$$\Delta I = \frac{V_{in} - V_{out}}{F_S \times L} \bullet \frac{V_{out}}{V_{in}}$$

$$\Delta V_{out} = \Delta I \times ESR$$

Although the increase of the inductance reduces the ripple current and voltage, it contributes to the decrease of the response time for the regulator to

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load transient as well. Increasing the switching frequency (F_s) for a given inductor also can reduce the ripple current and voltage but it will increase the switching loss of the power MOS.

The way to set a proper inductor value is to have the ripple current (ΔI) be approximately 10%~50% of the maximum output current. Once the value has been determined, select an inductor capable of carrying the required peak current without going into saturation. It is also important to have the inductance tolerance specified to keep the accuracy of the system controlled. Using 20% for the inductance (at room temperature) is reasonable tolerance able to be met by most manufacturers. For some types of inductors, especially those with core made of ferrite, the ripple current will increase abruptly when it saturates, resulting in a larger output ripple voltage.

Output Capacitors Selection

An output capacitor is required to filter the output and supply the load transient current. The high capacitor value and low ESR will reduce the output ripple and the load transient drop. These requirements can be met by a mix of capacitors and careful layout.

In typical switching regulator design, the ESR of the output capacitor bank dominates the transient response. The number of output capacitors can be determined by the following equations:

$$ESR_{MAX} = \frac{\Delta V_{ESR}}{\Delta I_{OUT}}$$

$$\text{Number Of Capacitors} = \frac{ESR_{CAP}}{ESR_{MAX}}$$

ΔV_{ESR} = change in output voltage due to ESR (assigned by the designer)

ΔI_{OUT} = load transient.

ESR_{CAP} = maximum ESR per capacitor (specified in manufacturer's data sheet).

ESR_{MAX} = maximum allowable ESR.

High frequency decoupling capacitors should be placed as close to the power pins of the load as physically possible. For the decoupling requirements, please consult the capacitor manufacturers for confirmation.

Output Voltage

The output voltage is set using the FB pin and a resistor divider connected to the output as shown in the following AP Circuit. The output voltage (V_{out}) can be calculated according to the voltage of the FB pin (V_{FB}) and ratio of the feedback resistors by the following equation, where (V_{FB}) is 1.23:

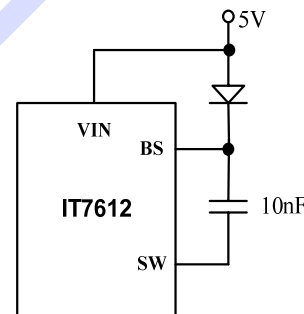
$$V_{FB} = V_{out} \times \frac{R_2}{(R_1 + R_2)}$$

Thus the output voltage is:

$$V_{out} = 0.92 \times \frac{(R_1 + R_2)}{R_2}$$

External Bootstrap Diode

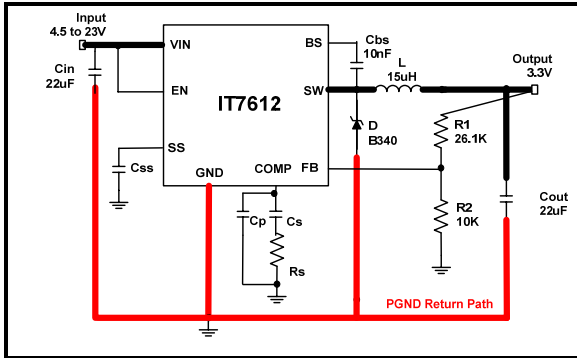
It is strongly recommended that an external bootstrap diode be added when there is a 5V fixed input for the system or the power supply generates a 5V output in order to improve the efficiency of the IT7612 regulator. The boost diode can be the one with lost cost such as IN4148 or BAT54.



This diode is also recommended for high duty cycle operation when Duty Cycle > 65% (Example: $V_{IN}=5V$ & $V_{out}=3.3V$; Duty Cycle=66%) and high output voltage ($V_{OUT}>12V$) applications.

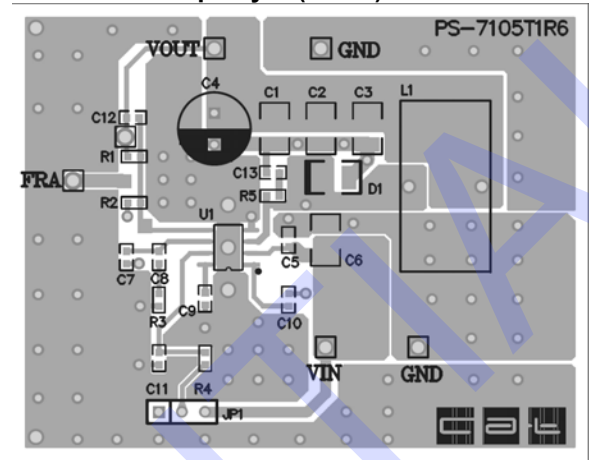
Layout Consideration

For proper operation of the converter, some layout rules should be followed. It is necessary to understand which pin of IT7612 is sensitive and which is insensitive. Please refer the following for the location where noise comes from on the circuit and where the clear ground is for the small signal ground.

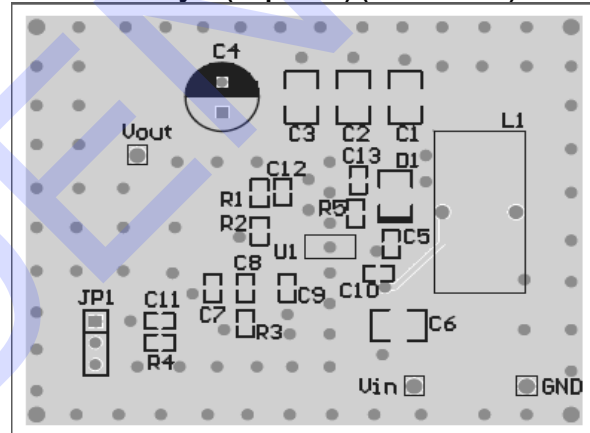


- 1.) First, put the input capacitor (C_{IN}) as close as possible to the VIN pin.
- 2.) Secondly, place the C_s , R_s , C_p , C_{ss} and R_2 as close as IT7612 and connect these analog grounds (Clear AGND) to IT7612's GND pin. It is recommended to use a dot short for these AGND pins or connect the GND pin via contact.
- 3.) The large current loop shown in bold lines in the above figure circuit should be routed as short and wide as possible and the switch node is a high dv/dt . It easily couples noise to other traces by the capacitive path. Therefore the sensitive signals like FB, COMP and AGND should be routed away with this noise source.
- 4.) The feedback network resistors (R_1 & R_2) should be routed away from the inductor and switch node to minimize noise and EMI issue. And the R_1 resistor should be sensed the output capacitor or device loading, not the inductor's output node.

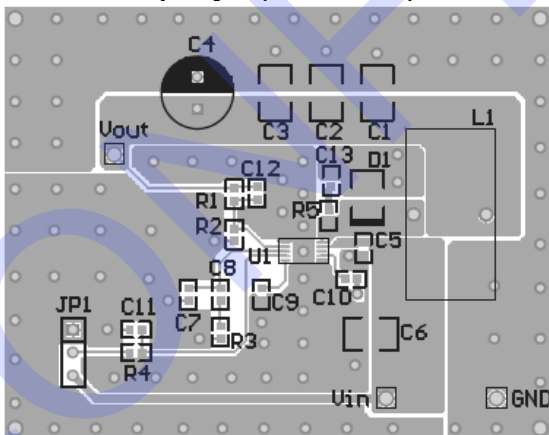
Top Layer (SOP8)



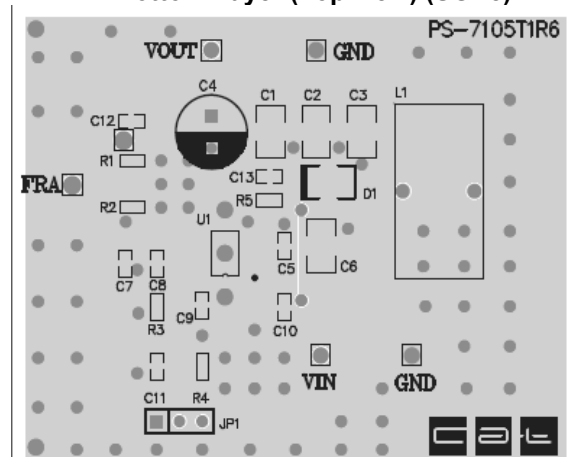
Bottom Layer (Top view) (MSOP10EP)



Top Layer (MSOP10EP)

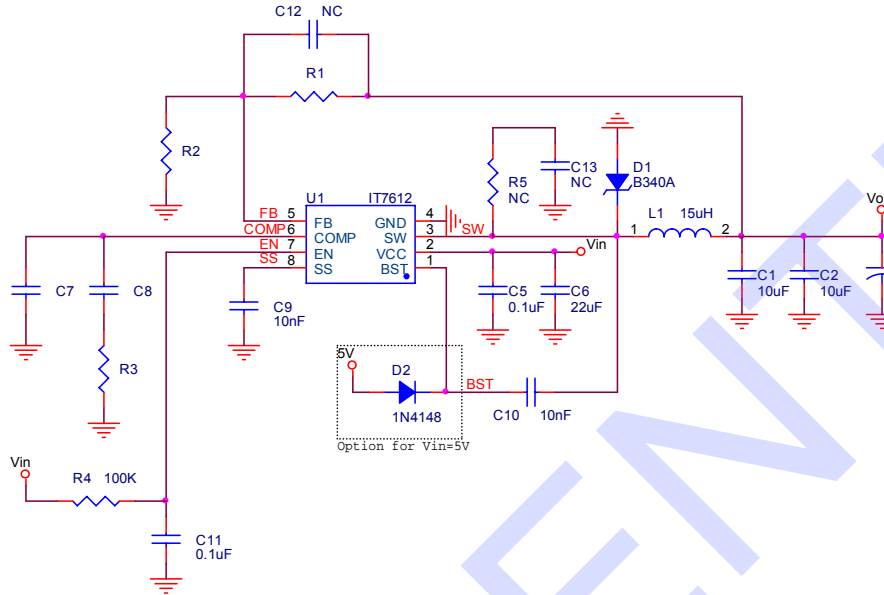


Bottom Layer (Top view) (SOP8)



IT7612 EVB Schematic

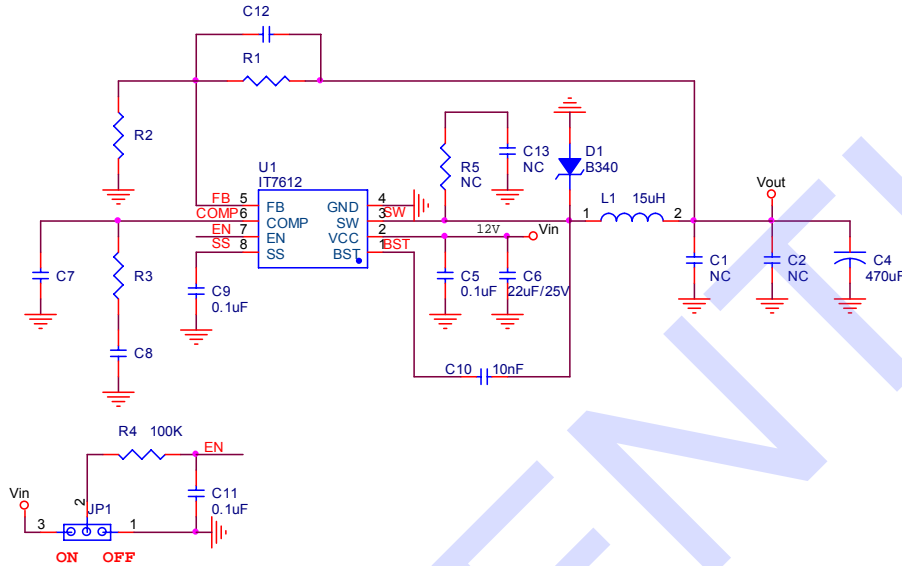
Application 1:



EVB BOM List

Qty	Ref	Value	Description	Package	
2	C1, C2,	10µF	Ceramic Capacitor X5R 6.3V (TDK part number: C3216X5R0J226MT000N)	1206	
1	C6	22µF	Ceramic Capacitor X5R 25V	1206	
1	C4	NC			
2	C5,C11	0.1µF	Ceramic Capacitor	0603	
1	L1	15µH	Inductor, Rated Current 3.5A (WE-part number: 744 771 115)	Type L	
1	R4	100KΩ	Resistor, ±1%	0603	
1	R1	Vout=5V	44.2kΩ	Resistor, ±1%	0603
		Vout=3.3V	26.1kΩ		
		Vout=1.8V	9.53kΩ		
		Vout=1.2V	3kΩ		
		Vout=1V	820Ω		
1	R2	10kΩ	Resistor, ±1%	0603	
1	R3	Vout=5V	10kΩ	Resistor, ±1%	0603
		Vout=3.3V	6.49kΩ		
		Vout=1.8V	3.9kΩ		
		Vout=1.2V	2.49kΩ		
		Vout=1V	2.49kΩ		
1	C9, C10	10nF	Ceramic Capacitor	0603	
1	C7	NC	Ceramic Capacitor	0603	
1	C8	Vout=5V	10nF	Ceramic Capacitor	0603
		Vout=3.3V	10nF		
		Vout=1.8V	10nF		
		Vout=1.2V	10nF		
		Vout=1V	10nF		
1	C12,C13,R5	NC			
1	D1	B340A	Diode	1206	
1	U1	IT7612	Step-Down DC/DC Converter	SOP8	

Application 2:



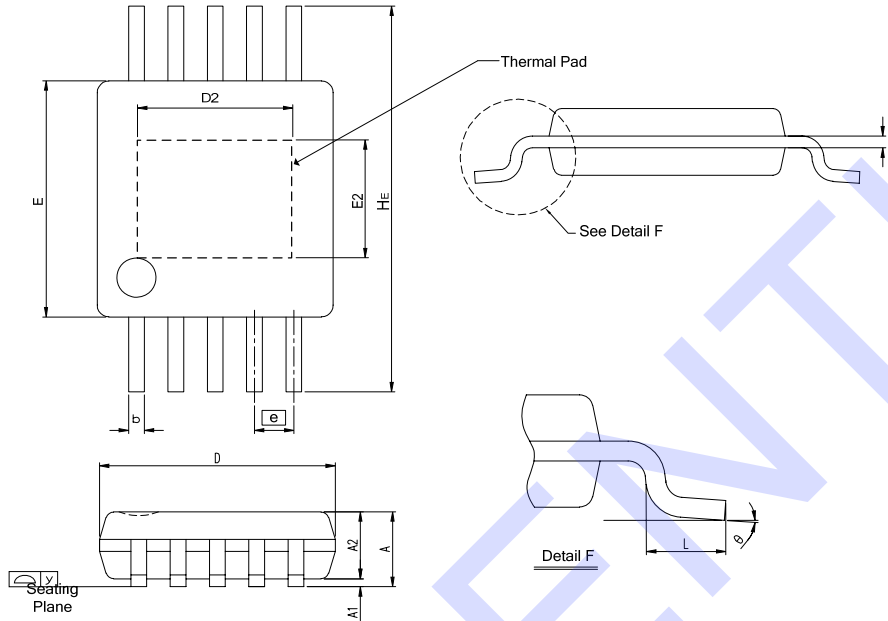
EVB BOM List

Qty	Ref	Value	Description	Package	
2	C1, C2,	NC	Ceramic Capacitor X5R 6.3V	1206	
1	C6	22uF	Ceramic Capacitor X5R 25V	1206	
1	C4	470uF EC	Low ESR		
3	C5,C11,C9	0.1uF	Ceramic Capacitor	0603	
1	L1	15uH	Inductor, Rated Current 3.5A (WE-part number: 744 771 115)	SMD	
1	R4	100KΩ	Resistor, ±1%	0603	
1	R1	Vout=5V	44.2kΩ	Resistor, ±1%	0603
		Vout=3.3V	26.1kΩ		
		Vout=2.5V	17.4kΩ		
		Vout=1.8V	9.53kΩ		
		Vout=1.5V	6.34kΩ		
		Vout=1.2V	3kΩ		
1	R2	10kΩ	Resistor, ±1%	0603	
1	R3	Vout=5V	150kΩ	Resistor, ±1%	0603
		Vout=3.3V	100kΩ		
		Vout=2.5V	68kΩ		
		Vout=1.8V	47kΩ		
		Vout=1.5V	39kΩ		
		Vout=1.2V	33kΩ		
1	C10	10nF	Ceramic Capacitor	0603	
1	C7	1nF	C7 value is adjustable for the ESR of Cout. Recommend value ≤ 1nF (C7 << C8)	0603	
1	C8	Vout=5V	10nF	Ceramic Capacitor	0603
		Vout=3.3V	10nF		
		Vout=2.5V	10nF		
		Vout=1.8V	10nF		
		Vout=1.5V	10nF		
		Vout=1.2V	10nF		
1	C12,C13,R5	NC			
1	D1	B340A	Diode	1206	
1	U1	IT7612	Step-Down DC/DC Converter	SOP8	

Package Information

MSOP10EP Outline Dimensions

unit: inches/mm



Symbol	Dimension in inches			Dimension in mm		
	Min	Nom	Max	Min	Nom	Max
A	--	--	0.043	--	--	1.10
A1	0.000	--	0.006	0.00	--	0.15
A2	0.030	0.033	0.037	0.75	0.85	0.95
b	0.007	--	0.011	0.17	--	0.27
c	0.003	--	0.009	0.08	--	0.23
D	0.118 BSC			3.00 BSC		
D2	0.055	--	0.087	1.39	--	2.21
HE	0.193 BSC			4.90 BSC		
E2	0.055	--	0.077	1.39	--	1.95
E	0.118 BSC			3.00 BSC		
e	0.020 BSC			0.50 BSC		
L	0.016	0.024	0.031	0.40	0.60	0.80
y	--	--	0.004	--	--	0.10
θ	0°	--	8°	0°	--	8°

Note :

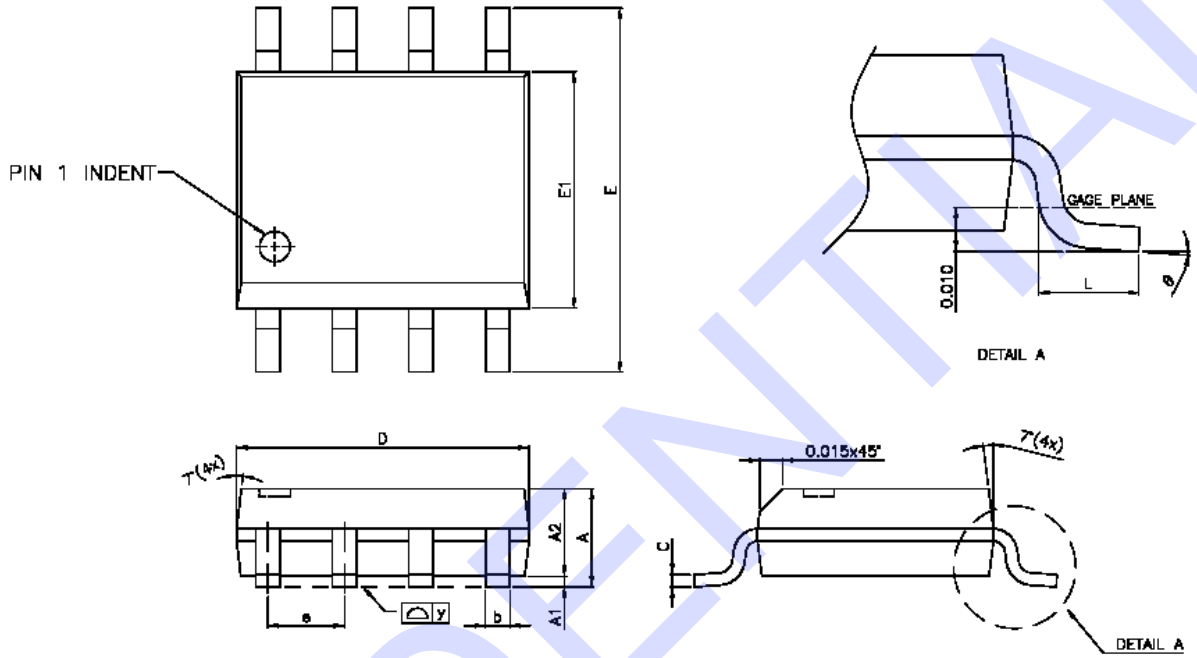
1. Controlling dimension : mm
2. Dimension "D" and "E" do not include mold protrusion.
"D" and "E" are maximum plastic body size dimension including mold mismatch.
3. Dimension "b" dose not include dambar protrusion.
Dambar cannot be located on the lower radius of the foot.
4. Reference Document : JEDEC SPEC MO-187.

DI-E-MSOP10(118mil Body)v0

Package Information

SOP8 Outline Dimensions

unit: inches/mm



Symbol	Dimensions in inches			Dimensions in mm		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	0.053	—	0.069	1.35	—	1.75
A1	0.004	—	0.010	0.10	—	0.25
A2	—	0.057	—	—	1.45	—
b	0.013	0.016	0.020	0.33	0.41	0.51
C	0.007	0.008	0.0098	0.17	0.20	0.25
D	0.185	0.191	0.201	4.70	4.85	5.10
E	0.228	0.236	0.244	5.80	6.00	6.20
E1	0.150	0.154	0.157	3.80	3.90	4.00
e	—	0.050	—	—	1.27	—
L	0.015	0.028	0.050	0.38	0.71	1.27
y	—	—	0.004	—	—	0.1
θ	0°	—	8°	0°	—	8°

Notes:

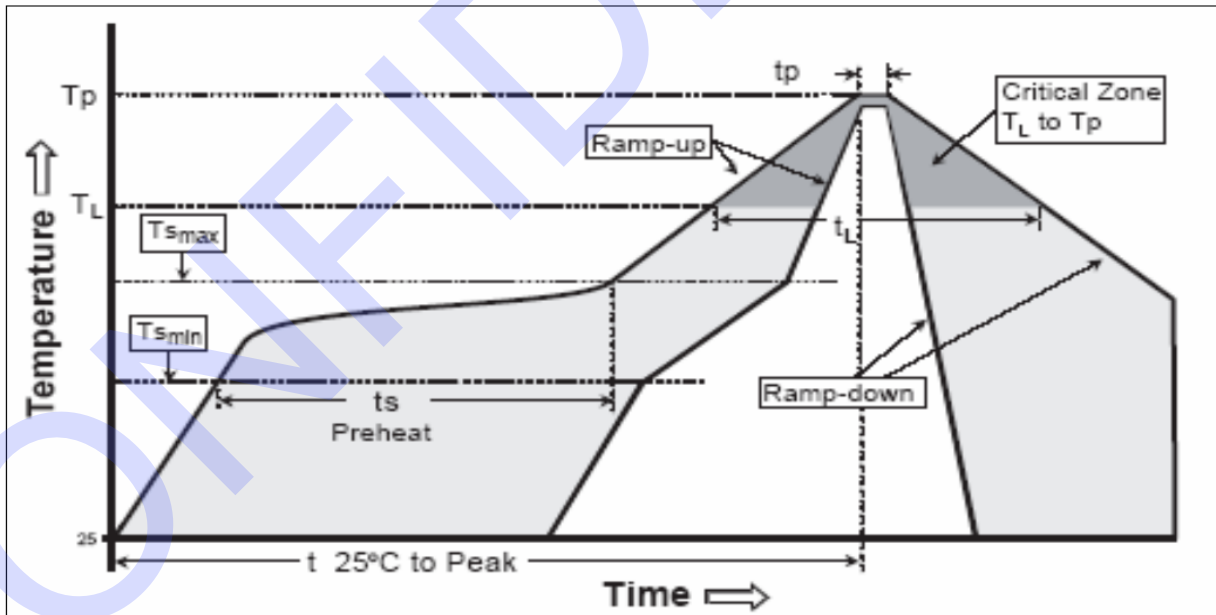
1. Controlling dimension : Millimeter
2. Dimension "D" does not include mold flash, Tie bar burrs and gate burrs.
3. Dimension "b" does not include dambar protrusion.
4. Reference document : JEDEC SPEC MS-012

DI-SOP8(150mil Body)v2

Classification Reflow Profiles

Reflow Profile	Green Assembly
Average Ramp-Up Rate ($T_{s_{max}}$ to T_p)	3°C/second max.
Preheat -Temperature -Temperature -Time($t_{s_{min}}$ to $t_{s_{max}}$)	Min($T_{s_{min}}$) Max($T_{s_{max}}$) 150°C 200°C 60-120 seconds
Time maintained above: -Temperature(T_L) -Time(t_L)	217°C 60-150 seconds
Peak Temperature(T_p)	260 +0/-5 °C
Time within 5 °C of actual Peak Temperature(t_p)	10-30 seconds
Ramp-Down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

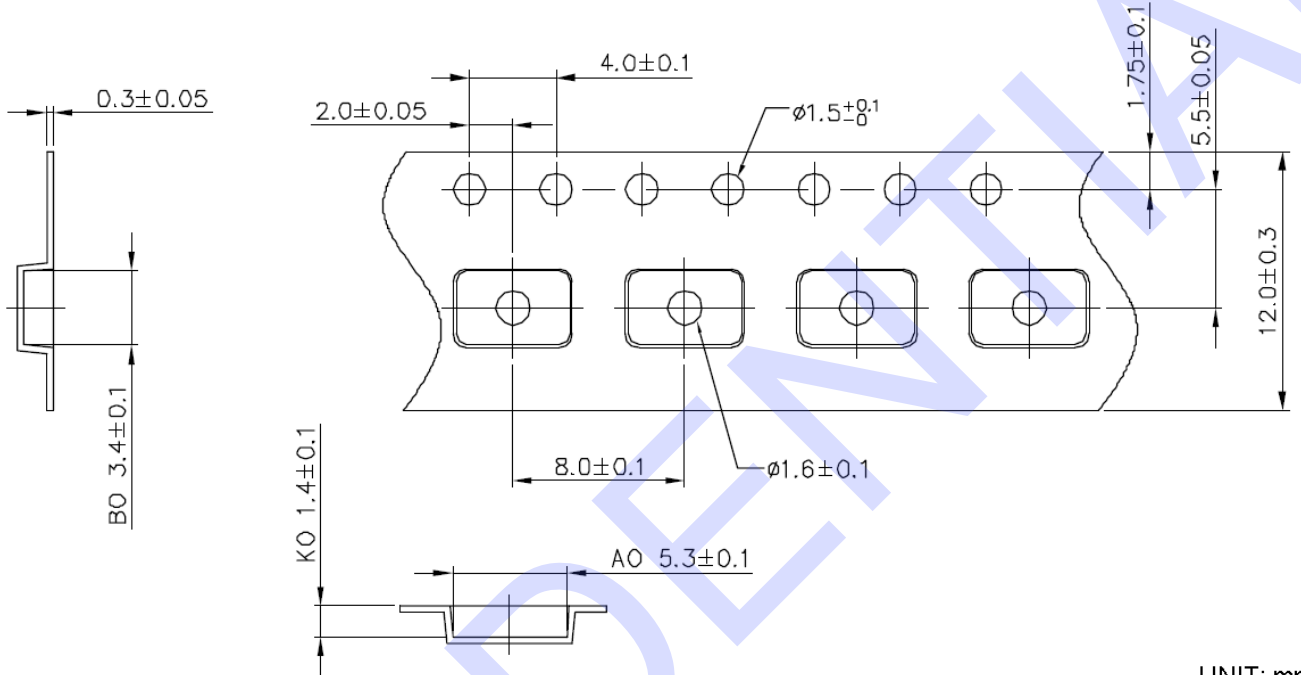
Note: For all temperature information, please refer to topside of the package, measured on the package body surface.



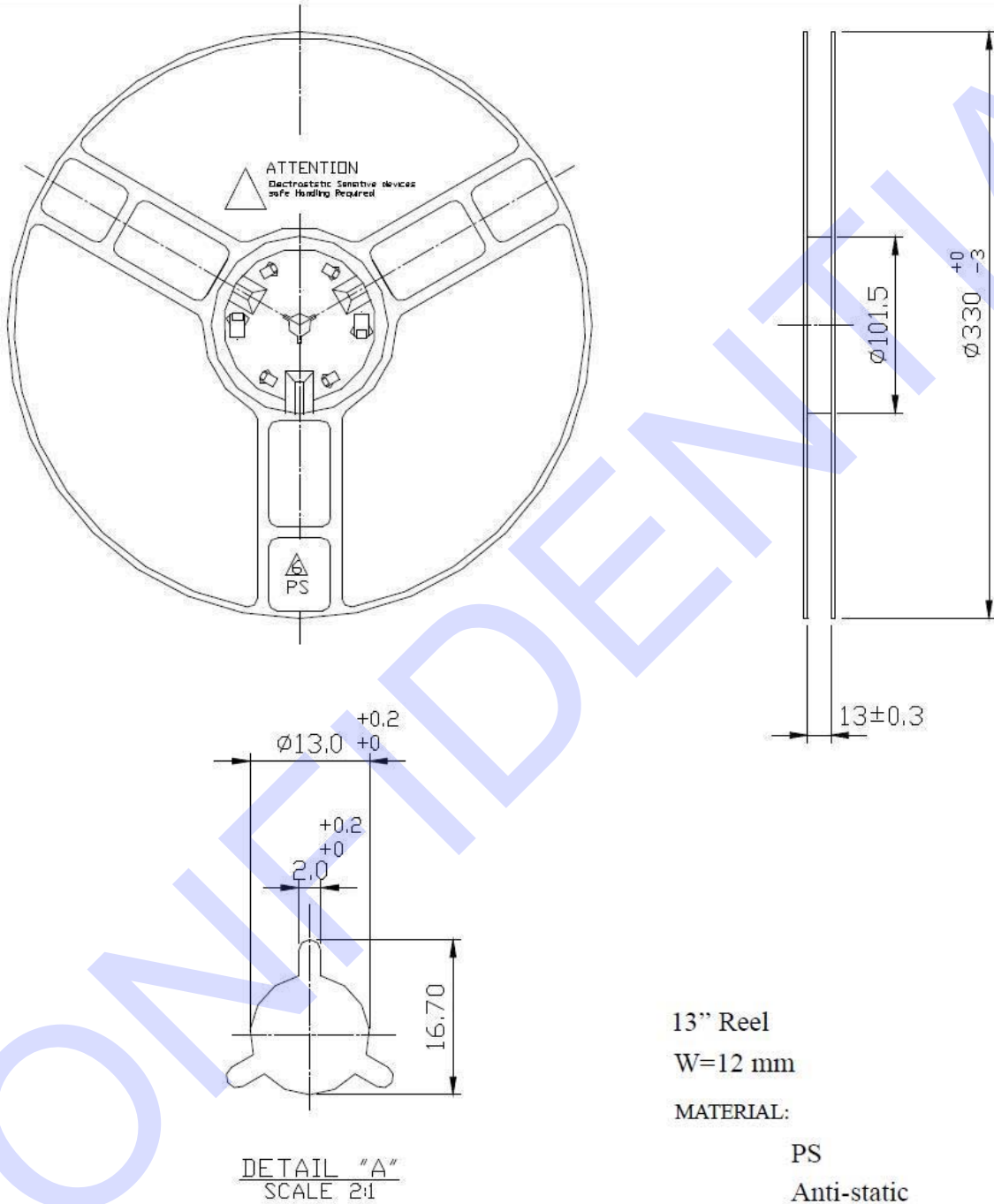
Carrier Tape & Reel Dimensions

MSOP10EP Carrier Tape Dimensions

Carrier Tape

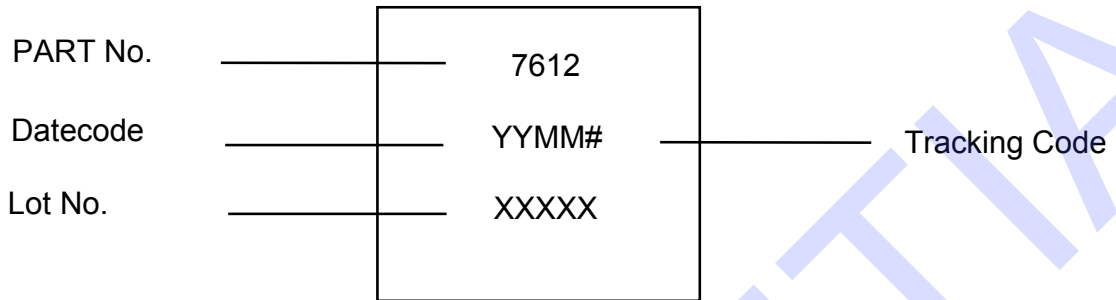


Reel Outline Dimensions



Top Marking Information

MSOP10EP Package



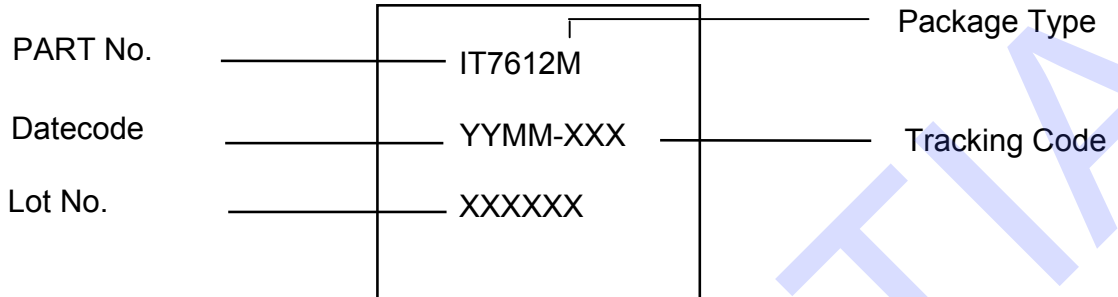
YYMM: Datecode

YY: Year

MM: Week

Year	Code	Week	Code
2009	09	Week01	01
2010	10	Week02	02
...

SOP8 Package



YYMM: Datecode

YY: Year

MM: Week

Year	Code	Week	Code
2009	09	Week01	01
2010	10	Week02	02
...

ITE TECH. INC. TERMS AND CONDITIONS OF SALE (Rev. 2005)

0. PARTIES

ITE Tech. Inc. ("Seller") is a company headquartered in Taiwan, Republic of China, and incorporated under laws of Republic of China, Buyer is a company or an entity, purchasing product from ITE Tech. Inc..

1. ACCEPTANCE OF TERMS

BUYER ACCEPTS THESE TERMS (i) BY WRITTEN ACCEPTANCE (BY PURCHASE ORDER OR OTHERWISE), OR (ii) BY FAILURE TO RETURN GOODS DESCRIBED ON THE FACE OF THE PACKING LIST WITHIN FIVE DAYS OF THEIR DELIVERY.

2. DELIVERY

- (a) Delivery will be made Free Carrier (Incoterms), Seller's warehouse, Science-Based Industrial Park, Hsinchu, Taiwan.
- (b) Title to the goods and the entire risk will pass to Buyer upon delivery to carrier.
- (c) Shipments are subject to availability. Seller shall make every reasonable effort to meet the date(s) quoted or acknowledged; and if Seller makes such effort, Seller will not be liable for any delays.

3. TERMS OF PAYMENT

- (a) Terms are as stated on Seller's quotation, or if none are stated, net thirty (30) days. Accounts past due will incur a monthly charge at the rate of one percent (1%) per month (or, if less, the maximum allowed by applicable law) to cover servicing costs.
- (b) Seller reserves the right to change credit terms at any time in its sole discretion.

4. LIMITED WARRANTY

- (a) Seller warrants that the goods sold will be free from defects in material and workmanship and comply with Seller's applicable published specifications for a period of ninety (90) days from the date of Seller's delivery. Within the warranty period and by obtaining a return number from Seller, Buyer may request replacement or repair for defective goods.
- (b) Goods or parts which have been subject to abuse (including without limitation repeated or extended exposure to conditions at or near the limits of applicable absolute ratings) misuse, accident, alteration, neglect, or unauthorized repair or improper application are not covered by any warranty. No warranty is made with respect to custom products or goods produced to Buyer's specifications (unless specifically stated in a writing signed by Seller).
- (c) No warranty is made with respect to goods used in devices intended for use in applications where failure to perform when properly used can reasonably be expected to result in significant injury (including, without limitation, navigation, aviation or nuclear equipment, or for surgical implant or to support or sustain life) and Buyer agrees to indemnify, defend, and hold harmless Seller from all claims, damages and liabilities arising out of any such uses.
- (d) This Paragraph 4 is the only warranty by Seller with respect to goods and may not be modified or amended except in writing signed by an authorized officer of Seller.
- (e) Buyer acknowledges and agrees that it is not relying on any applications, diagrams or circuits contained in any literature, and Buyer will test all parts and applications under extended field and laboratory conditions. Notwithstanding any cross-reference or any statements of compatibility, functionality, interchangeability, and the like, the goods may differ from similar goods from other vendors in performance, function or operation, and in areas not contained in the written specifications, or as to ranges and conditions outside such specifications; and Buyer agrees that there are no warranties and that Seller is not responsible for such things.
- (f) EXCEPT AS PROVIDED ABOVE, SELLER MAKES NO WARRANTIES OR CONDITIONS, EXPRESS, IMPLIED, OR STATUTORY; AND SELLER EXPRESSLY EXCLUDES AND DISCLAIMS ANY WARRANTY OR CONDITION OF MERCHANTABILITY OR FITNESS FOR PARTICULAR PURPOSE OR APPLICATION.

5. LIMITATION OF LIABILITY

- (a) Seller will not be liable for any loss, damage or penalty resulting from causes beyond its reasonable control, including but not limited to delay by others, force majeure, acts of God, or labor conditions. In any such event, the date(s) for Seller's performance will be deemed extended for a period equal to any delay resulting.
- (b) THE LIABILITY OF SELLER ARISING OUT OF THE CONTRACT OR ANY GOODS SOLD WILL BE LIMITED TO REFUND OF THE PURCHASE PRICE OR REPLACEMENT OF PURCHASED GOODS (RETURNED TO SELLER FREIGHT PRE-PAID) OR, WITH SELLER'S PRIOR WRITTEN CONSENT, REPAIR OF PURCHASED GOODS.
- (c) Buyer will not return any goods without first obtaining a customer return order number.
- (d) AS A SEPARATE LIMITATION, IN NO EVENT WILL SELLER BE LIABLE FOR COSTS OF SUBSTITUTE GOODS; FOR ANY SPECIAL, CONSEQUENTIAL, INCIDENTAL OR INDIRECT DAMAGES; OR LOSS OF USE, OPPORTUNITY, MARKET POTENTIAL, AND/OR PROFIT ON ANY THEORY (CONTRACT, TORT, FROM THIRD PARTY CLAIMS OR OTHERWISE). THESE LIMITATIONS SHALL APPLY NOT WITHSTANDING ANY FAILURE OF ESSENTIAL PURPOSE OF ANY REMEDY.
- (e) No action against Seller, whether for breach, indemnification, contribution or otherwise, shall be commenced more than one year after the cause of action has accrued, or more than one year after either the Buyer, user or other person knew or with reasonable diligence should have known of the matter or of any claim of dissatisfaction or defect involved; and no such claim may be brought unless Seller has first been given commercially reasonable notice, a full written explanation of all pertinent details, and a good faith opportunity to resolve the matter.
- (f) BUYER EXPRESSLY AGREES TO THE LIMITATIONS OF THIS PARAGRAPH 5 AND TO THEIR REASONABLENESS.

6. SUBSTITUTIONS AND MODIFICATIONS

Seller may at any time make substitutions for product ordered which do not materially and adversely affect overall performance with the then current specifications in the typical and intended use. Seller reserves the right to halt deliveries and shipments and alter specifications and prices without notice. Buyer shall verify that the literature and information is current before purchasing.

7. CANCELLATION

The purchase contract may not be canceled by Buyer except with written consent by Seller and Buyer's payment of reasonable cancellation charges (including but not be limited to expenses already incurred for labor and material, overhead, commitments made by Seller, and a reasonable profit).

8. INDEMNIFICATION

Seller will, at its own expense, assist Buyer with technical support and information in connection with any claim that any parts as shipped by Seller under the purchase order infringe any valid and enforceable copyright, or trademark, provided however, that Buyer (i) gives immediate written notice to Seller, (ii) permits Seller to participate and to defend if Seller requests to do so, and (iii) gives Seller all needed information, assistance and authority. However, Seller will not be responsible for infringements resulting from anything not entirely manufactured by Seller, or from any combination with products, equipment, or materials not furnished by Seller. Seller will have no liability with respect to intellectual property matters arising out of products made to Buyer's specifications, code, or designs.

Except as expressly stated in this Paragraph 8 or in another writing signed by an authorized officer, Seller makes no representations and/or warranties with respect to intellectual and/or industrial property and/or with respect to claims of infringement. Except as to claims Seller agrees in writing to defend, BUYER WILL INDEMNIFY, DEFEND AND HOLD HARMLESS SELLER FROM ALL CLAIMS, COSTS, LOSSES AND DAMAGES (INCLUDING ATTORNEYS FEES) AGAINST AND/OR ARISING OUT OF GOODS SOLD AND/OR SHIPPED HEREUNDER.

9. NO CONFIDENTIAL INFORMATION

Seller shall have no obligation to hold any information in confidence except as provided in a separate non-disclosure agreement signed by both parties.

10. ENTIRE AGREEMENT

- (a) These terms and conditions are the entire agreement and the only representations and understandings between Seller and Buyer, and no addition, deletion or modification shall be binding on Seller unless expressly agreed to in written and signed by an officer of Seller.
- (b) Buyer is not relying upon any warranty or representation except for those specifically stated here.

11. APPLICABLE LAW

The contract and all performance and disputes arising out of or relating to goods involved will be governed by the laws of R.O.C. (Taiwan, Republic of China), without reference to the U.N. Convention on Contracts for the International Sale of Goods or to conflict of laws principles. Buyer agrees at its sole expense to comply with all applicable laws in connection with the purchase, use or sale of the goods provided hereunder and to indemnify Seller from any failure by Buyer to so comply. Without limiting the foregoing, Buyer certifies that no technical data or direct products thereof will be made available or re-exported, directly or indirectly, to any country to which such export or access is prohibited or restricted under R.O.C. laws or U.S. laws or regulations, unless prior authorization is obtained from the appropriate officials and agencies of the government as required under R.O.C. or U.S. laws or regulations.

12. JURISDICTION AND VENUE

The courts located in Hsinchu, Taiwan, Republic of China, will have the sole and exclusive jurisdiction and venue over any dispute arising out of or relating to the contract or any sale of goods hereunder. Buyer hereby consents to the jurisdiction of such courts.

13. ATTORNEYS' FEES

Reasonable attorneys' fees and costs will be awarded to the prevailing party in the event of litigation involving and/or relating to the enforcement or interpretation of the contract and/or any goods sold under it.