

### **INCHANGE SEMICONDUCTOR**

## isc N-Channel MOSFET Transistor

### IXFH130N15X3

D(2)

### FEATURES

- Static drain-source on-resistance:
  - $R_{DS}(on) \le 9.0 m_{\Omega} @V_{GS} = 10V$
- Fully characterized avalanche voltage and current
- 100% Avalanche Tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### APPLICATION

SYMBOL

Rth(j-c)

- Switched mode power supplies
- DC-DC converters



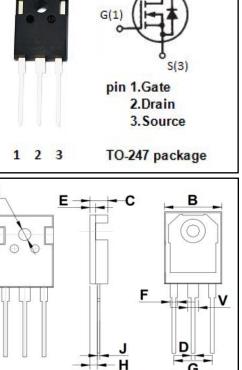
MAX

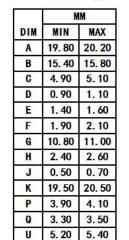
0.32

1

UNIT

°C/W





2.90

3.10

PARAMETER

Junction-to-case thermal resistance

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# isc N-Channel MOSFET Transistor

# IXFH130N15X3

### **ELECTRICAL CHARACTERISTICS**

#### $T_c=25^{\circ}C$ unless otherwise specified

| SYMBOL              | PARAMETER                      | CONDITIONS   | MIN | МАХ  | UNIT         |
|---------------------|--------------------------------|--|-----|------|--------------|
| BV <sub>DSS</sub>   | Drain-Source Breakdown Voltage | V <sub>GS</sub> = 0V; ID = 250 μ A   | 150 |      | V            |
| $V_{GS(th)}$        | Gate Threshold Voltage         | V <sub>DS</sub> = V <sub>GS</sub> ; ID = 1.5mA                                   | 2.5 | 4.5  | V            |
| R <sub>DS(on)</sub> | Drain-Source On-Resistance     | V <sub>GS</sub> =10V; I <sub>D</sub> = 65A                                       |     | 9.0  | mΩ           |
| I <sub>GSS</sub>    | Gate-Source Leakage Current    | $V_{GS}$ = ±20V; $V_{DS}$ =0V  |     | ±100 | nA           |
| I <sub>DSS</sub>    | Drain-Source Leakage Current   | V <sub>DS</sub> = V <sub>DSS</sub> ; V <sub>GS</sub> = 0V                        |     | 5    | - μ <b>Α</b> |
|                     |                                | V <sub>DS</sub> = V <sub>DSS</sub> ; V <sub>GS</sub> = 0V;T <sub>J</sub> = 125°C |     | 300  |              |
| Vsd                 | Diode forward voltage          | I <sub>F</sub> = 130A; V <sub>GS</sub> = 0V                                      |     | 1.4  | V            |

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