

HiPerFET™ Power MOSFETs IXFR 14N80

IsoPlus247™

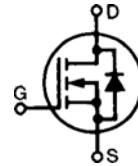
(Electrically Isolated Tab)

$$V_{DSS} = 800 \text{ V}$$

$$I_{D25} = 14 \text{ A}$$

$$R_{DS(on)} = 0.7 \text{ } \Omega$$

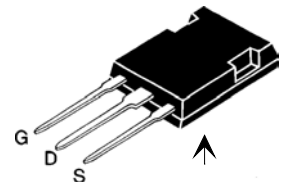
N-Channel Enhancement Mode
High dv/dt, Low t_{rr} , HDMOS™ Family



$$t_{rr} \leq 250 \text{ ns}$$

Symbol	Test Conditions	Maximum Ratings	
V_{DSS}	$T_J = 25^\circ\text{C}$ to 150°C	800	V
V_{DGR}	$T_J = 25^\circ\text{C}$ to 150°C ; $R_{GS} = 1 \text{ M}\Omega$	800	V
V_{GS}	Continuous	± 20	V
V_{GSM}	Transient	± 30	V
I_{D25}	$T_C = 25^\circ\text{C}$	14	A
I_{DM}	$T_C = 25^\circ\text{C}$, pulse width limited by T_{JM}	56	A
I_{AR}	$T_C = 25^\circ\text{C}$	14	A
E_{AR}	$T_C = 25^\circ\text{C}$	45	mJ
dv/dt	$I_S \leq I_{DM}$, $di/dt \leq 100 \text{ A}/\mu\text{s}$, $V_{DD} \leq V_{DSS}$, $T_J \leq 150^\circ\text{C}$, $R_G = 2 \text{ } \Omega$	5	V/ns
P_D	$T_C = 25^\circ\text{C}$	250	W
T_J		-55 ... +150	$^\circ\text{C}$
T_{JM}		150	$^\circ\text{C}$
T_{stg}		-55 ... +150	$^\circ\text{C}$
T_L	1.6 mm (0.062 in.) from case for 10 s	300	$^\circ\text{C}$
V_{ISOL}	50/60 Hz, RMS, $t = 1$ minute leads-to-tab	2500	V
Weight		6	g

ISOPLUS 247



Isolated mounting tab*

G = Gate D = Drain
S = Source

* Patent pending

Features

- Isolated mounting tab
- Low $R_{DS(on)}$ HDMOS™ process
- Rugged polysilicon gate cell structure
- Unclamped Inductive Switching (UIS) rated
- Low drain to tab capacitance (<25pF)
- Fast intrinsic Rectifier

Applications

- DC-DC converters
- Battery chargers
- Switched-mode and resonant-mode power supplies
- DC choppers
- AC motor control

Advantages

- Easy assembly
- Space savings
- High power density

Symbol	Test Conditions	Characteristic Values ($T_J = 25^\circ\text{C}$, unless otherwise specified)		
		min.	typ.	max.
V_{DSS}	$V_{GS} = 0 \text{ V}$, $I_D = 3 \text{ mA}$	800		V
$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 4 \text{ mA}$	2		4.5 V
I_{GSS}	$V_{GS} = \pm 20 \text{ V}_{DC}$, $V_{DS} = 0$			$\pm 100 \text{ nA}$
I_{DSS}	$V_{DS} = 0.8 \cdot V_{DSS}$, $T_J = 25^\circ\text{C}$ $V_{GS} = 0 \text{ V}$, $T_J = 125^\circ\text{C}$			250 μA 1 mA

Symbol	Test Conditions ($T_J = 25^\circ\text{C}$, unless otherwise specified)	Characteristic Values		
		Min.	Typ.	Max.
$R_{DS(on)}$	$V_{GS} = 10\text{ V}$, $I_D = 0.5 I_{D25}$ Pulse test, $t \leq 300\ \mu\text{s}$, duty cycle $d \leq 2\%$			0.7 Ω
g_{fs}	$V_{DS} = 10\text{ V}$; $I_D = 0.5 I_{D25}$, pulse test	8	14	S
C_{iss} C_{oss} C_{rss}	$V_{GS} = 0\text{ V}$, $V_{DS} = 25\text{ V}$, $f = 1\text{ MHz}$		4200	pF
			360	pF
			100	pF
$t_{d(on)}$ t_r $t_{d(off)}$ t_f	$V_{GS} = 10\text{ V}$, $V_{DS} = 0.5 V_{DSS}$, $I_D = 0.5 I_{D25}$ $R_G = 2\ \Omega$ (External)		20	ns
			33	ns
			63	ns
			32	ns
$Q_{g(on)}$ Q_{gs} Q_{gd}	$V_{GS} = 10\text{ V}$, $V_{DS} = 0.5 V_{DSS}$, $I_D = 0.5 I_{D25}$		128	nC
			30	nC
			55	nC
R_{thJC} R_{thCK}		0.15		0.50 K/W K/W

Symbol	Test Conditions	Characteristic Values ($T_J = 25^\circ\text{C}$, unless otherwise specified)			
		Min.	Typ.	Max.	
I_S	$V_{GS} = 0\text{ V}$			14 A	
I_{SM}	Repetitive; pulse width limited by T_{JM}			56 A	
V_{SD}	$I_F = I_S$, $V_{GS} = 0\text{ V}$, Pulse test, $t \leq 300\ \mu\text{s}$, duty cycle $d \leq 2\%$			1.5 V	
t_{rr} Q_{RM} I_{RM}	$I_F = I_S$ $-di/dt = 100\text{ A}/\mu\text{s}$, $V_R = 100\text{ V}$			250 ns	
				400 ns	
			1		μC
			8.5		A

ISOPLUS 247 OUTLINE

SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.190	.205	4.83	5.21
A1	.090	.100	2.29	2.54
A2	.075	.085	1.91	2.16
b	.045	.055	1.14	1.40
b1	.075	.084	1.91	2.13
b2	.115	.123	2.92	3.12
C	.024	.031	0.61	0.80
D	.819	.840	20.80	21.34
E	.620	.635	15.75	16.13
e	.215 BSC		5.45 BSC	
L	.780	.800	19.81	20.32
L1	.150	.170	3.81	4.32
Q	.220	.244	5.59	6.20
R	.170	.190	4.32	4.83
S	.520	.540	13.21	13.72
T	.620	.640	15.75	16.26
U	.065	.080	1.65	2.03

1 - GATE
 2 - DRAIN (COLLECTOR)
 3 - SOURCE (EMITTER)
 4 - NO CONNECTION

NOTE: This drawing will meet all dimensions requirement of JEDEC outline TO-247AD except screw hole.

IXYS reserves the right to change limits, test conditions, and dimensions.