

# **INCHANGE SEMICONDUCTOR**

# isc N-Channel MOSFET Transistor

# IXTA14N60P

## • FEATURES

- Static drain-source on-resistance: RDs(on) ≤ 550mΩ@V<sub>GS</sub>=10V
- Fully characterized avalanche voltage and current
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

## APPLICATION

SYMBOL

VDSS

V<sub>GS</sub>

ΙD

Ірм

- DC/DC Converter
- Switch-Mode and Resonant-Mode Power Supplies

PARAMETER

VALUE

600

 $\pm 30$ 

14

42

1

UNIT

V

V

А

А

Uninterrupted Power Supplies

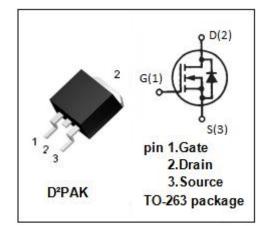
## • ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

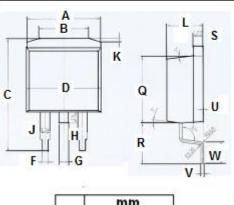
Drain-Source Voltage

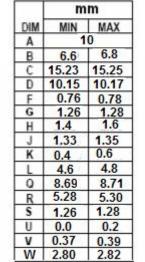
Gate-Source Voltage

Drain Current-Continuous

**Drain Current-Single Pulsed** 







# PDTotal Dissipation @Tc=25°C300WTjOperating Junction Temperature-55~150°CTstgStorage Temperature-55~150°C• THERWAL CHARACTERISTICSSYMBOLPARAMETERMAXUNIT

SYMBOL	PARAMETER	MAX	UNIT
Rth(j-c)	Junction-to-case thermal resistance	0.42	°C/W



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## **ELECTRICAL CHARACTERISTICS**

### $T_c=25^{\circ}C$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	МАХ	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; ID = 250 μ A	600		V
$V_{GS(th)}$	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> ; ID = 250 μ A	3.0	5.5	V
$R_{\text{DS(on)}}$	Drain-Source On-Resistance	V <sub>GS</sub> =10V; I <sub>D</sub> = 7A		550	mΩ
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = ±30V; $V_{DS}$ =0V		±100	nA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> = V <sub>DSS</sub> ; V <sub>GS</sub> = 0V		5	μ <b>Α</b>
		V <sub>DS</sub> = V <sub>DSS</sub> ; V <sub>GS</sub> = 0V;T <sub>J</sub> = 125°C		100	
V <sub>SD</sub>	Diode forward voltage	I <sub>F</sub> = 14A; V <sub>GS</sub> = 0V		1.5	V

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