

## **INCHANGE SEMICONDUCTOR**

# isc N-Channel MOSFET Transistor

# IXTA2N80

### • FEATURES

Static drain-source on-resistance:

RDS(on) ≤ 6.2Ω@V<sub>GS</sub>=10V

- Fully characterized avalanche voltage and current
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### APPLICATION

SYMBOL

VDSS

V<sub>GS</sub>

ΙD

Ірм

 $\mathbf{P}_{\mathsf{D}}$ 

Τį

Tstg

- DC/DC Converter
- Switch-Mode and Resonant-Mode Power Supplies

PARAMETER

VALUE

800

 $\pm 20$ 

2

8

54

-55~150

-55~150

1

UNIT

V

V

А

А

W

°C

°C

Uninterrupted Power Supplies

### • ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

Drain-Source Voltage

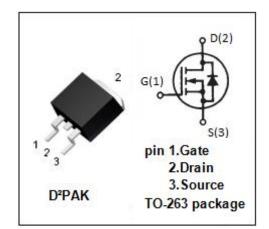
Gate-Source Voltage

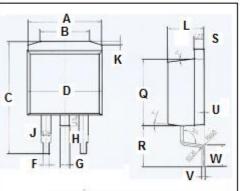
Drain Current-Continuous

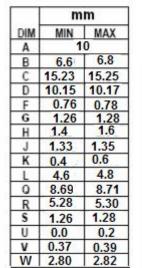
Drain Current-Single Pulsed

Total Dissipation @Tc=25°C

**Operating Junction Temperature** 







### THERMAL CHARACTERISTICS

Storage Temperature

SYMBOL	PARAMETER	MAX	UNIT
Rth(j-c)	Junction-to-case thermal resistance	2.31	°C/W



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## **ELECTRICAL CHARACTERISTICS**

#### $T_c=25^{\circ}C$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	МАХ	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; ID = 250 μ A	800		V
$V_{GS(th)}$	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> ; ID = 250 μ A	2.5	5.5	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =10V; I <sub>D</sub> = 1A		6.2	Ω
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±20V;V <sub>DS</sub> =0V		±100	nA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> = V <sub>DSS</sub> ; V <sub>GS</sub> = 0V		25	μΑ
		V <sub>DS</sub> = V <sub>DSS</sub> ; V <sub>GS</sub> = 0V;T <sub>J</sub> = 125°C		500	
$V_{\text{SD}}$	Diode forward voltage	I <sub>F</sub> = 2A; V <sub>GS</sub> = 0V		1.8	V

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