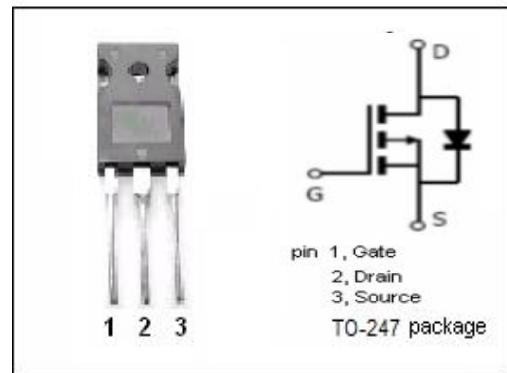


Isc P-Channel MOSFET Transistor

IXTH52P10P

• FEATURES

- With To-252(DPAK) package
- Low input capacitance and gate charge
- Low gate input resistance
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



• APPLICATIONS

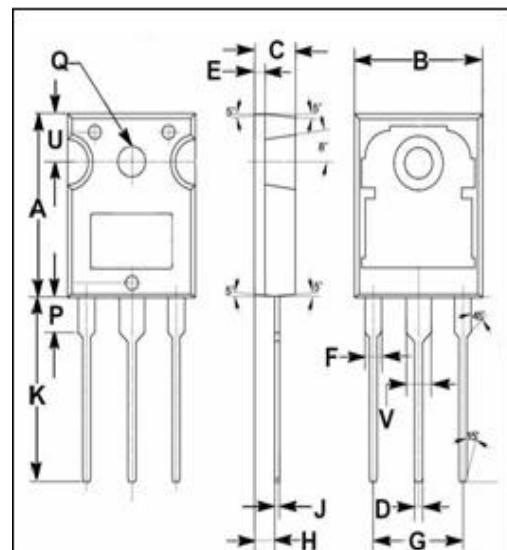
- Switching applications
- Motor control
- DC-DC converters

• ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DSS}	Drain-Source Voltage	-100	V
V_{GSS}	Gate-Source Voltage	± 30	V
I_D	Drain Current-Continuous <small>T_c=25°C T_c=70°C</small>	-52	A
I_{DM}	Drain Current-Single Pulsed	-130	A
P_D	Total Dissipation @ $T_c=25^\circ\text{C}$	300	W
T_j	Max. Operating Junction Temperature	150	°C
T_{stg}	Storage Temperature	-55~150	°C

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	0.42	°C/W
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	50	°C/W



DIM	mm	
	MIN	MAX
A	19.80	20.20
B	15.40	15.80
C	4.90	5.10
D	0.90	1.10
E	1.40	1.60
F	1.90	2.10
G	10.80	11.00
H	2.40	2.60
J	0.50	0.70
K	19.50	20.50
P	3.90	4.10
Q	3.30	3.50
U	5.20	5.40
V	2.90	3.10

Isc P-Channel MOSFET Transistor**IXTH52P10P****ELECTRICAL CHARACTERISTICS****T_c=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V; I _D = -0.25mA	-100			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} ; I _D =-0.25mA	-2.5		-4.5	V
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} = -10V; I _D =-26A		6.7	8.1	mΩ
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ±20V; V _{DS} = 0V			±0.1	μ A
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-100V; V _{GS} = 0V; T _c =25°C T _c =125°C			-10 -150	μ A
V _{SDF}	Diode forward voltage	I _{SD} =-26A, V _{GS} = 0 V			-3.5	V

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