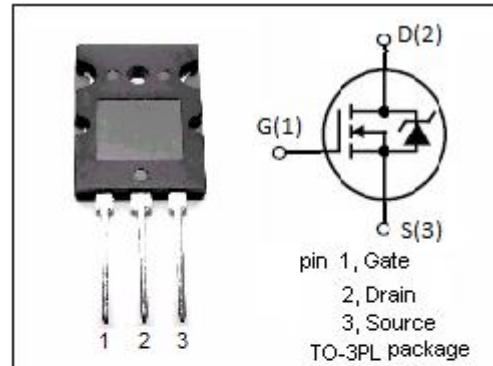


Isc N-Channel MOSFET Transistor

IXTK200N10L2

• FEATURES

- With To-3PL package
- Low input capacitance and gate charge
- Low gate input resistance
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



• APPLICATIONS

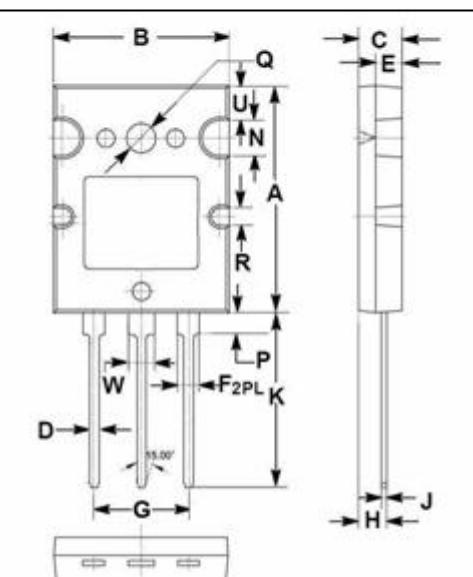
- Switching applications

• ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DSS}	Drain-Source Voltage	100	V
V_{GSS}	Gate-Source Voltage	± 30	V
I_D	Drain Current-Continuous	200	A
I_{DM}	Drain Current-Single Pulsed	500	A
P_D	Total Dissipation @ $T_c=25^\circ\text{C}$	1040	W
T_{ch}	Max. Operating Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature	-55~150	$^\circ\text{C}$

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	0.12	$^\circ\text{C}/\text{W}$
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	62	$^\circ\text{C}/\text{W}$



DIM	mm	
	MIN	MAX
A	25.50	26.50
B	19.80	20.20
C	4.50	5.50
D	0.90	1.10
E	2.80	3.20
F	2.40	2.60
G	10.80	11.00
H	3.10	3.30
J	0.50	0.70
K	20.00	21.00
N	3.90	4.50
P	2.40	2.60
Q	3.10	3.50
R	1.90	2.60
U	3.90	4.10
W	2.90	3.25

Isc N-Channel MOSFET Transistor**IXTK102N30P****ELECTRICAL CHARACTERISTICS****T_c=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V; I _D =1mA	100			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} ; I _D =3mA	2.0		4.5	V
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} = 10V; I _D =100A			11	mΩ
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ±20V; V _{DS} =0V			±0.2	μA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =100V; V _{GS} = 0V; T _j =25°C V _{DS} =100V; V _{GS} = 0V; T _j =150°C			10 250	μA
V _{SDF}	Diode forward voltage	I _{SD} =100A, V _{GS} = 0 V			1.4	V

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