

## **INCHANGE SEMICONDUCTOR**

# isc N-Channel MOSFET Transistor

# IXTP2R4N50P

### FEATURES

- Static drain-source on-resistance:  $R_{DS}(on) \le 3.75 \Omega @V_{GS}=10V$
- Fully characterized avalanche voltage and current
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### APPLICATION

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

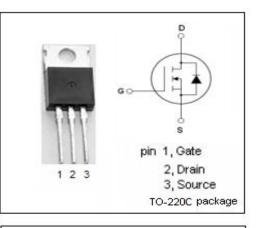
SYMBOL	PARAMETER	VALUE	UNIT
V <sub>DSS</sub>	Drain-Source Voltage	500	V
V <sub>GS</sub>	Gate-Source Voltage	±30	V
ID	Drain Current-Continuous	2.4	А
I <sub>DM</sub>	Drain Current-Single Pulsed	4.5	А
PD	Total Dissipation @T <sub>C</sub> =25℃	55	W
Tj	Operating Junction Temperature	-55~150	°C
T <sub>stg</sub>	Storage Temperature	-55~150	°C

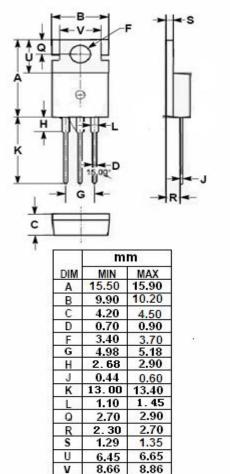
### • ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	МАХ	UNIT
R <sub>th(j-c)</sub>	Junction-to-case thermal resistance	2.27	°C/W

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### **ELECTRICAL CHARACTERISTICS**

#### $T_c=25^{\circ}C$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	МАХ	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; ID =250 μ A	500		V
$V_{GS(th)}$	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> ; ID =250 μ A	3.0	5.5	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =10V; I <sub>D</sub> = 1.2A		3.75	Ω
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = ±30V; $V_{DS}$ =0V		±50	nA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> = V <sub>DSS</sub> ; V <sub>GS</sub> = 0V		1	μ <b>Α</b>
		V <sub>DS</sub> = V <sub>DSS</sub> ; V <sub>GS</sub> = 0V;T <sub>J</sub> = 125°C		50	
$V_{\text{SD}}$	Diode forward voltage	I <sub>F</sub> = 2.4A; V <sub>GS</sub> = 0V		1.5	V

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