Unit: mm

TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (L^2 - π -MOSV)

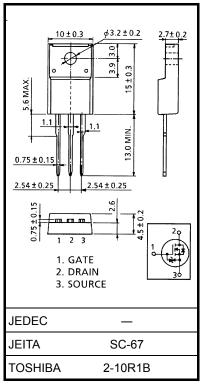
2SJ380

Relay Drive, DC-DC Converter and Motor Drive Applications

- 4-V gate drive
- Low drain-source ON resistance: R_{DS} (ON) = 0.15 Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 7.7 \text{ S}$ (typ.)
- Low leakage current: $I_{DSS} = -100 \ \mu A \ (max) \ (V_{DS} = -100 \ V)$
- Enhancement mode: $V_{th} = -0.8 \sim -2.0 \text{ V} (V_{DS} = -10 \text{ V}, \text{ I}_D = -1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	-100	V
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	-100	V
Gate-source voltage		V _{GSS}	±20	V
Drain current	DC (Note 1)	ID	-12	А
	Pulse (Note 1)	I _{DP}	-48	А
Drain power dissipation	n (Tc = 25°C)	PD	35	W
Single pulse avalanche energy (Note 2)		E _{AS}	312	mJ
Avalanche current		I _{AR}	-12	А
Repetitive avalanche energy (Note 3)		E _{AR}	3.5	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature range		T _{stg}	-55~150	°C



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	3.57	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Note1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = -25 \text{ V}, \text{ T}_{ch} = 25^{\circ}\text{C}$ (initial), L = 2.94 mH, R_G = 25 Ω , I_{AR} = -12 A

Note 3: Repetitive rating: pulse width limited by maximum junction temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

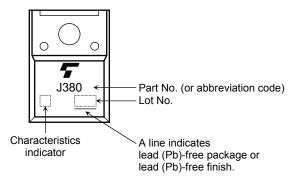
Electrical Characteristics (Tc = 25°C)

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS}=\pm 16~V,~V_{DS}=0~V$	_	_	±10	μA
Drain cut-off current		I _{DSS}	$V_{DS} = -100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	_		-100	μA
Drain-source brea	akdown voltage	V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-100	_		V
Gate threshold vo	oltage	V _{th}	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$	-0.8	_	-2.0	V
Drain-source ON resistance		Decession	$V_{GS} = -4 \text{ V}, \text{ I}_D = -6 \text{ A}$	_	0.25	0.32	Ω
		R _{DS (ON)}	$V_{GS} = -10 \text{ V}, \text{ I}_{D} = -6 \text{ A}$	_	0.15	0.21	
Forward transfer	nsfer admittance $ Y_{fs} $ $V_{DS} = -10 V$, $I_D = -6 A$		$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -6 \text{ A}$	4.5	7.7		S
Input capacitance		C _{iss}		_	1100	_	pF
Reverse transfer capacitance		C _{rss}	$V_{DS} = -10 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$	_	200		pF
Output capacitance		C _{oss}		_	440		pF
Switching time	Rise time	tr	$V_{GS} \xrightarrow{I_D = -6 \text{ A}} V_{OUT}$ $V_{GS} \xrightarrow{I_D = -6 \text{ A}} V_{OUT}$ $C \xrightarrow{G} \xrightarrow{G} \xrightarrow{G} \xrightarrow{G} \xrightarrow{G} \xrightarrow{G} \xrightarrow{G} \xrightarrow{G}$		18	_	ns
	Turn-on time	t _{on}		_	30		
	Fall time	t _f			18		
	Turn-off time	t _{off}		—	65		
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \simeq -80 \text{ V}, \text{ V}_{GS} = -10 \text{ V},$	_	48		nC
Gate-source charge		Q _{gs}	$I_{\rm D} = -12 {\rm A}$	_	29		nC
Gate-drain ("miller") charge		Q _{gd}]		19		nC

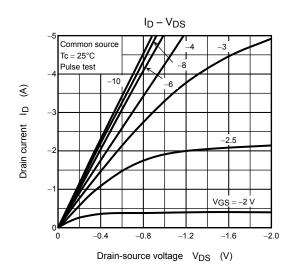
Source-Drain Ratings and Characteristics (Tc = 25°C)

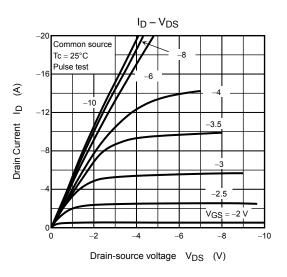
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_		_	-12	A
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	-48	A
Forward voltage (diode)	V _{DSF}	$I_{DR} = -12$ A, $V_{GS} = 0$ V		_	1.7	V
Reverse recovery time	t _{rr}	$I_{DR} = -12 \text{ A}, V_{GS} = 0 \text{ V}$	_	160	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 50 A/µs	_	0.5	_	μC

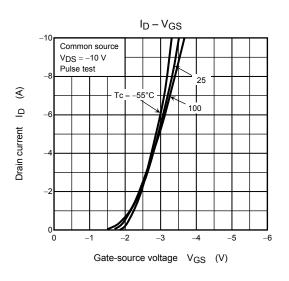
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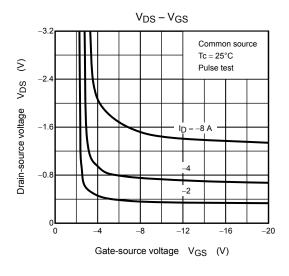


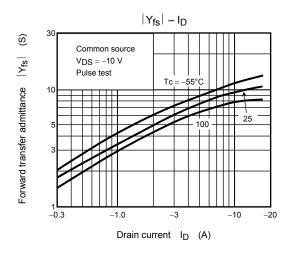
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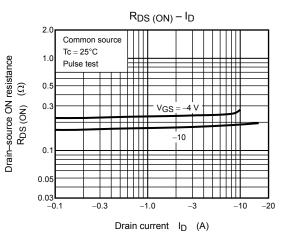




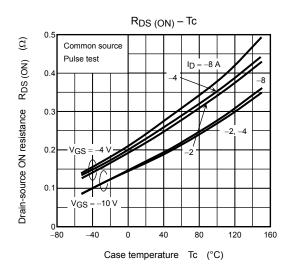


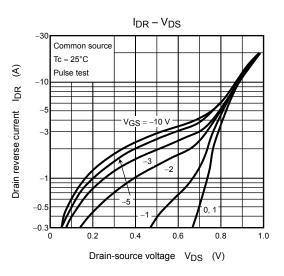


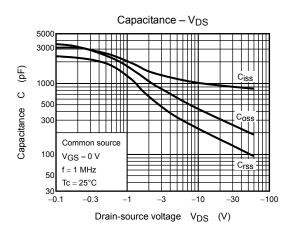


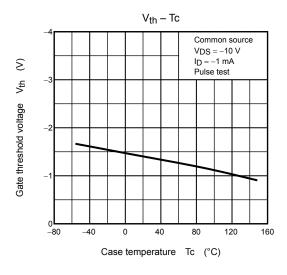


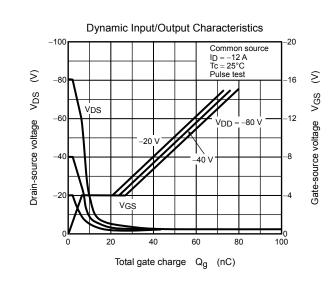
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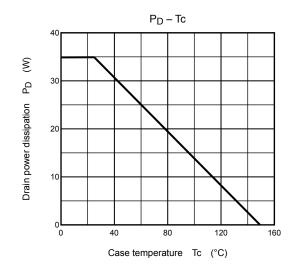


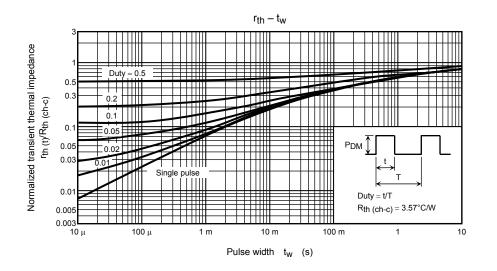


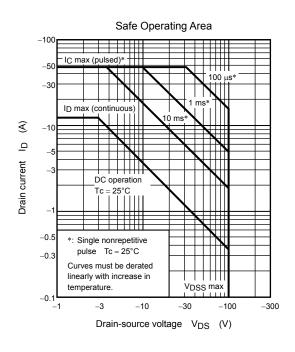


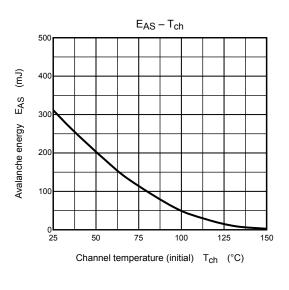


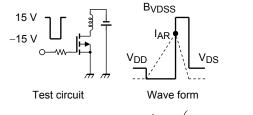














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