

J507 **Current Regulator Diode**



Linear Systems replaces discontinued Siliconix J507

The Linear Systems J507 is a ± 20% range current regulator

The J507 is a ±20% range current regulator designed for			FE	FEATURES					
demanding applications in test equipment and instrumentation.				REPLACEMENT SOURCE FOR SILICONIX J507					
The J507 utilizes JFET techniques to produce a single two-leaded device which is extremely simple to operate.			WI	WIDE CURRENT RANGE			1.80mA ± 20%		
				BIASING NOT REQUIRED			$V_{GS} = 0V$		
 Two-Lead Plastic Package Guaranteed ±20% Tolerance 			AB	ABSOLUTE MAXIMUM RATINGS ¹					
Operation up to 50VExcellent Temperature Stability				@ 25 °C (unless otherwise stated)					
				Maximum Temperatures					
 Simple Series Circuitry, No Separate Voltage Source Tight Guaranteed Circuit Performance Excellent Performance in Low-Voltage/Battery Circuits 			Sto	orage Te	-55 to 150°C				
			Ju	nction O	-55 to 135°C				
and High-Voltage Spike ProtectionHigh Circuit Stability vs. Temperature				Maximum Power Dissipation					
				ntinuou	360mW				
J507 Applications: Constant-Current Supply Current-Limiting Timing Circuits			Ma	Maximum Currents					
			Fo	rward C	20mA				
			Re	verse C	50mA				
			Ma	Maximum Voltages					
				ak Oper	P _{ov} = 50V				
ELECTRICAL CHARACTERISTICS @ 25 °C (unless otherwise stated)									
SYMBOL	CHARACTERISTIC	MIN	TYP	MAX	UNITS	CONDITIONS			
Pov	Peak Operating Voltage ²	50			V	$I_F = 1.1I_{F(max)}$			
		1							

SPECIFIC ELECTRICAL CHARACTERISTICS @ 25 °C (unless otherwise stated)

PART	Forward Current ³ I _F			Dynamic Ir Z	, -	Knee Impedance Z _k	Limiting Voltage ⁵ V∟	
	V _F = 25V			V _F =	25V	V _F = 6V	$I_F = 0.8I_{F(min)}$	
	MIN	NOM	MAX	MIN	TYP	TYP	TYP	MAX
J507	1.440	1.80	2.160	0.20	1	0.19	2.8	1.3

0.8

2.2

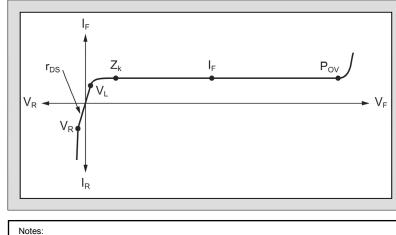
V-I CHARACTERISTICS CURRENT REGULATING DIODE

Reverse Voltage

Forward Capacitance

 V_R

CF



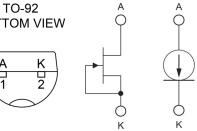
Available Packages: **BOTTOM VIEW** TO-92 Bare Die.

V

pF

 $I_R = 1 m A$

 $V_{\rm F} = 25V, f = 1$ MHz



Please contact Micross for full package and die dimensions

Micross Components Europe



Tel: +44 1603 788967 Email: chipcomponents@micross.com Web: http://www.micross.com/distribution

1. Absolute maximum ratings are limiting values above which serviceability may be impaired. 2. Pulsed, t = 2ms. Maximum V_F where IF < $1.1_{\rm IF}$ (max).

- $\begin{array}{l} \text{2-Puised, } t=2\text{ms. Maximum V}_{\text{F}} \text{ where } t=1 \quad \text{3-Puised, } t=2\text{ms. Continuous currents may vary.}\\ \text{4. Puised, } t=2\text{ms. Continuous impedances may vary.}\\ \text{5. Min } V_{\text{F}} \text{ required to ensure } I_{\text{F}}=0.8_{\text{IF}}(\text{min}). \end{array}$

Information furnished by Linear Integrated Systems and Micross Components is believed to be accurate and reliable. However, no responsibility is assumed for its use; nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Linear Integrated Systems.

Micross Components Ltd, United Kingdom, Tel: +44 1603 788967, Fax: +44 1603788920, Email: chipcomponents@micross.com Web: www.micross.com/distribution.aspx