2SJ634



DC / DC Converter Applications

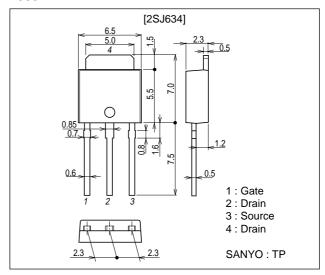
Preliminary

Features

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- · 4V drive.

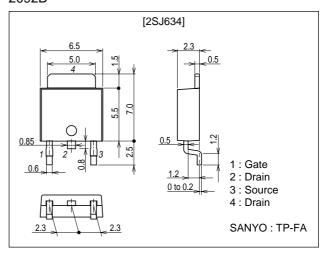
Package Dimensions

unit : mm 2083B



Package Dimensions

unit : mm 2092B



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Specifications

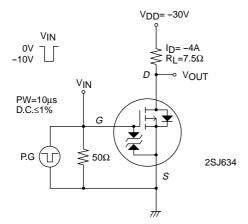
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		-60	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		-8	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-32	Α
Allowable Power Dissipation	D-		1	W
	PD	Tc=25°C	20	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=-1mA, VGS=0	-60			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =-60V, V _{GS} =0			-1	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0			±10	μΑ
Gate-to-Source Cutoff Voltage	VGS(off)	V _{DS} =-10V, I _D =-1mA	-1.2		-2.6	V
Forward Transfer Admittance	yfs	V _{DS} =-10V, I _D =-4A	4	7		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =-4A, V _G S=-10V		105	138	mΩ
	R _{DS} (on)2	I _D =-4A, V _G S=-4V		145	205	mΩ
Input Capacitance	Ciss	V _{DS} =-20V, f=1MHz		990		pF
Output Capacitance	Coss	V _{DS} =-20V, f=1MHz		110		pF
Reverse Transfer Capacitance	Crss	V _{DS} =-20V, f=1MHz		76		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		12		ns
Rise Time	t _r	See specified Test Circuit.		85		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		95		ns
Fall Time	tf	See specified Test Circuit.		80		ns
Total Gate Charge	Qg	V _{DS} =-30V, V _{GS} =-10V, I _D =-8A		22		nC
Gate-to-Source Charge	Qgs	V _{DS} =-30V, V _{GS} =-10V, I _D =-8A		4		nC
Gate-to-Drain "Miller" Charge	Qgd	VDS=-30V, VGS=-10V, ID=-8A		4		nC
Diode Forward Voltage	V _{SD}	IS=-8A, VGS=0		-0.95	-1.2	V

Switching Time Test Circuit



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