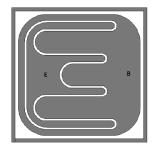


NPN Low Power Silicon Transistor Die

Features

- Qualified to MIL-PRF-19500/391
- Lightweight & Low Power
- Ideal for Space, Military, & Other High Reliability Applications



Electrical Characteristics ($T_A = +25^{\circ}C$ unless otherwise specified)

Parameter	Test Conditions	Symbol	Units	Min.	Max.
Collector - Emitter Breakdown Voltage	I _C = 30 mA	V _{(BR)CEO}	V dc	80	—
Collector - Base Cutoff Current	V _{CB} = 140 V	I _{CBO1}	µA dc	—	10
Emitter - Base Cutoff Current	V _{EB} = 7 V	I _{EBO1}	µA dc	—	10
Collector - Emitter Cutoff Current	V _{CE} = 90 V	I _{CES1}	nA dc	_	10
Emitter - Base Cutoff Current	V _{EB} = 5 Vdc I _{EBC}		nA dc	—	10
Forward Current Transfer Ratio		h _{FE1} h _{FE2} h _{FE3} h _{FE4} h _{FE5}	-	100 50 90 50 15	300 300 300
Collector - Emitter Saturation Voltage	I_{C} = 150 mA; I_{B} = 15 mA I_{C} = 500 mA; I_{B} = 50 mA	V _{CE(SAT)1} V _{CE(SAT)2}	V dc		0.2 0.5
Base - Emitter Saturation Voltage	I _C = 150 mA; I _B = 15 mA	$V_{\text{BE(SAT)}}$	V dc	_	1.1
Collector - Emitter Cutoff Current	T _A = +150°C V _{CE} = 90 V	I _{CES2}	µA dc	_	5
Forward Current Transfer Ratio	$T_A = -55^{\circ}C$ V _{CE} = 10 V dc; I _C = 150 mA dc	h _{FE6}		40	

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NPN Low Power Silicon Transistor Die

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Parameter	Test Conditions	Symbol	Units	Min.	Max.		
Dynamic Characteristics							
Small-Signal Short-Circuit Forward -Current Transfer Ratio	V_{CE} = 5 V dc; I _C = 1 mA dc; f = 1 kHz	h _{FE}		80	400		
Magnitude of Small-Signal Short-Circuit Forward Current Transfer Ratio	V_{CE} = 10 V dc; I _C = 50 mA dc; f = 20 MHz	h _{FE}		5	20		
Input Capacitance (Output Open Circuited)	V_{EB} = 0.5 V dc; I _C = 0; 100 kHz ≤ f ≤ 1 MHz	C _{ibo}	pF	_	60		
Open Circuit Output Capacitance	V _{CB} = 10 V dc; I _E = 0; 100 kHz ≤ f ≤ 1 MHz	C_{obo}	pF	—	12		
Noise Figure	V_{CE} = 10 V dc; I _C = 100 µA dc; Rg = 1 kΩ; power bandwidth = 200 H _Z f = 1 kHz	NF	dB	_	4		
Pulse Response	See Figure 21 of MIL-PRF-19500/391	t _{on} +t _{off}	ns		30		

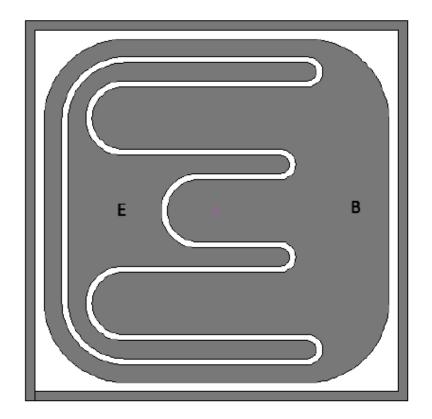
Absolute Maximum Ratings ($T_A = +25^{\circ}C$ unless otherwise specified)

Ratings	Symbol	Value	
Collector - Emitter Voltage	V _{CEO}	80 V dc	
Collector - Base Voltage	V _{CBO}	140 V dc	
Emitter - Base Voltage	V_{EBO}	7 V dc	
Collector Current	Ι _C	1 A dc	
Operating & Storage Temperature Range	T_J, T_{STG}	-65°C to +200°C	



NPN Low Power Silicon Transistor Die

Outline Drawing (Die)



Physical characteristics (C-version):

- Die size:
- Die thickness:
- .023 x .023 inch ±.002 inch (0.584 X 0.584 ±0.051 millimeter). .010 inch ±.002 inch (0.254 ±0.508 millimeter).
- Base pad:
- B = .004 inch x .010 inch (0.102 X 0.254 millimeter).
- Emitter pad:
- E = .0039 inch x .0039 inch (0. 992 X 0.099 millimeter). Backside.
- Collector pad:
 Top metal:
- Aluminum 16,000Å minimum, 20,000Å nominal. Gold: 5,000Å ±500Å.
- Backside metal:

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NPN Low Power Silicon Transistor Die

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