

Features

- Advanced Trench MOS Technology
- Low Gate Charge
- 100% EAS Guaranteed
- Green Device Available

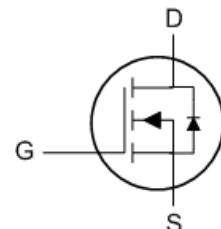
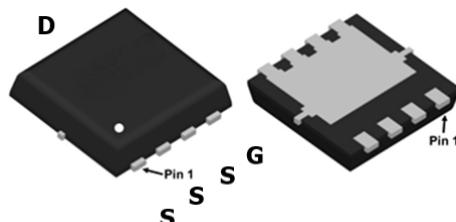
Product Summary

BVDSS	RDS(ON)	ID
60V	15mΩ	29A

DFN 3x3 Pin Configuration

Applications

- Motor Control.
- DC/DC Converter.
- Synchronous Rectifier Applications.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	60	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	29	A
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	18	A
I _{DM}	Pulsed Drain Current ²	58	A
EAS	Single Pulse Avalanche Energy ³	31	mJ
I _{AS}	Avalanche Current	25	A
P _D @T _C =25°C	Total Power Dissipation ⁴	20.8	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-Ambient ¹	---	62	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	6	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	60	---	---	V
$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance ²	$V_{GS}=10\text{V}$, $I_D=8\text{A}$	---	12	15	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$, $I_D=8\text{A}$	---	20	25	
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu\text{A}$	1.2	---	2.3	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=48\text{V}$, $V_{GS}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	uA
		$V_{DS}=48\text{V}$, $V_{GS}=0\text{V}$, $T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$, $V_{DS}=0\text{V}$	---	---	± 100	nA
R_g	Gate Resistance	$V_{DS}=0\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$	---	0.8	---	Ω
Q_g	Total Gate Charge	$V_{DS}=30\text{V}$, $V_{GS}=10\text{V}$, $I_D=8\text{A}$	---	11.7	---	nC
Q_{gs}	Gate-Source Charge		---	2	---	
Q_{gd}	Gate-Drain Charge		---	3.5	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=30\text{V}$, $V_{GS}=10\text{V}$, $R_G=3\Omega$, $I_D=8\text{A}$	---	6	---	ns
T_r	Rise Time		---	4	---	
$T_{d(off)}$	Turn-Off Delay Time		---	25	---	
T_f	Fall Time		---	3	---	
C_{iss}	Input Capacitance	$V_{DS}=30\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$	---	512	---	pF
C_{oss}	Output Capacitance		---	171	---	
C_{rss}	Reverse Transfer Capacitance		---	20	---	
Diode Characteristics						
I_s	Continuous Source Current ^{1,5}	$V_G=V_D=0\text{V}$, Force Current	---	---	29	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{DD}=25\text{V}$, $V_{GS}=10\text{V}$, $L=0.1\text{mH}$, $I_{AS}=25\text{A}$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_S , in real applications , should be limited by total power dissipation.

Typical Characteristics

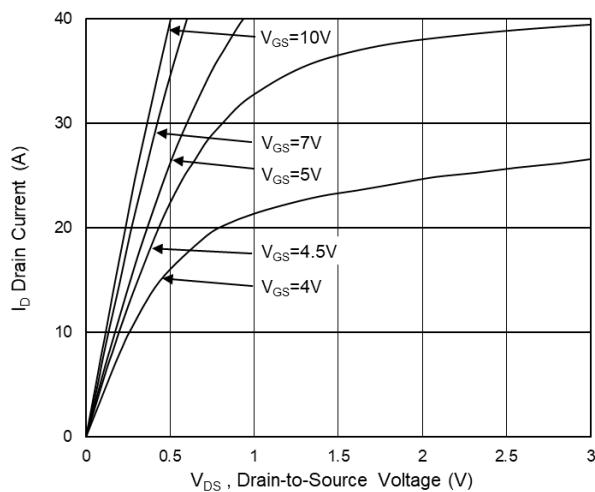


Fig.1 Typical Output Characteristics

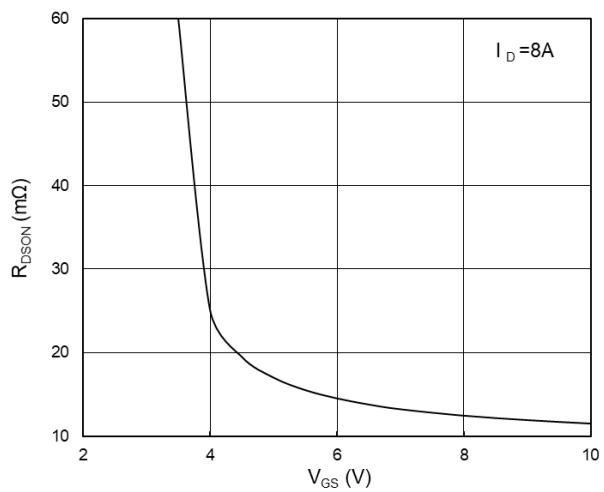


Fig.2 On-Resistance vs G-S Voltage

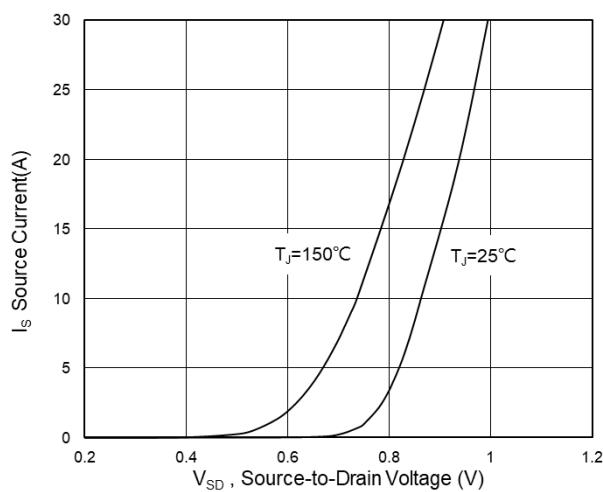


Fig.3 Source Drain Forward Characteristics

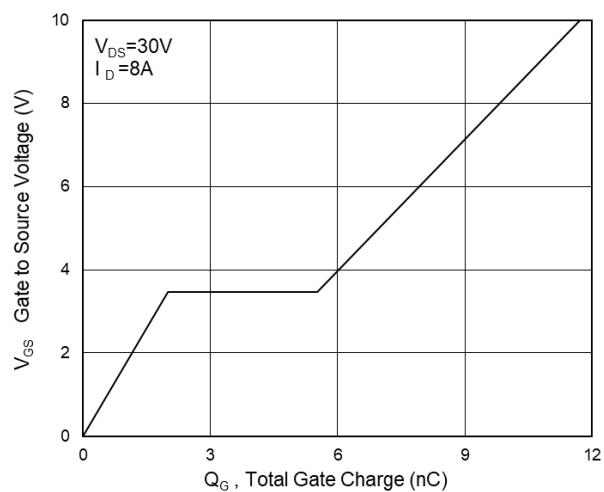


Fig.4 Gate-Charge Characteristics

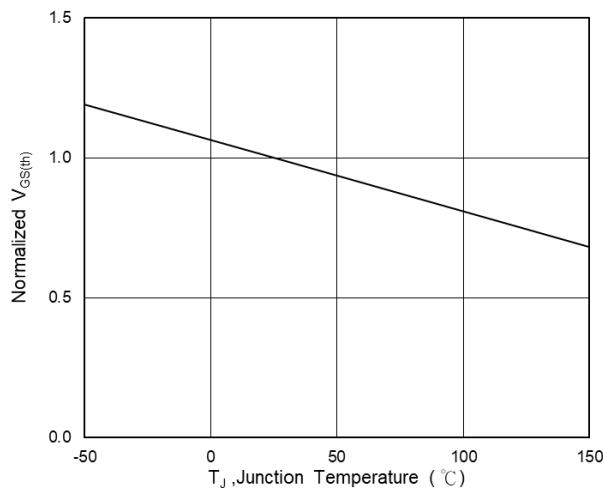


Fig.5 Normalized $V_{GS(th)}$ vs T_J

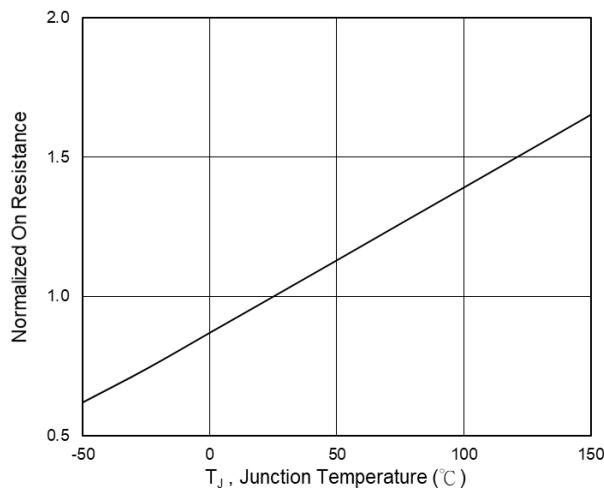
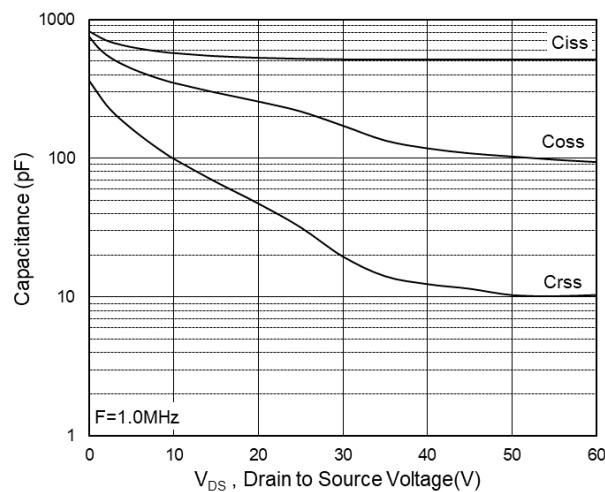
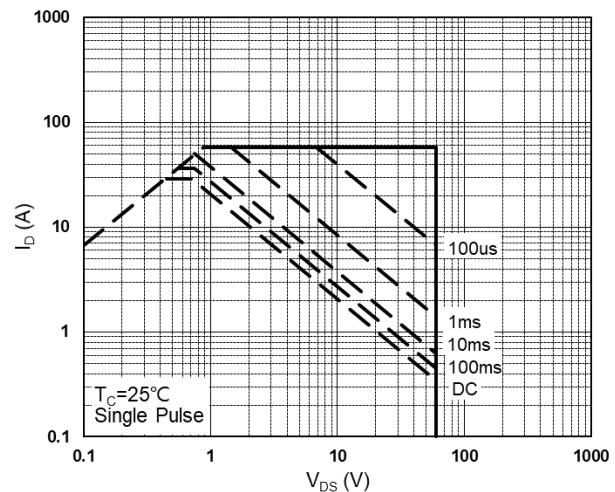
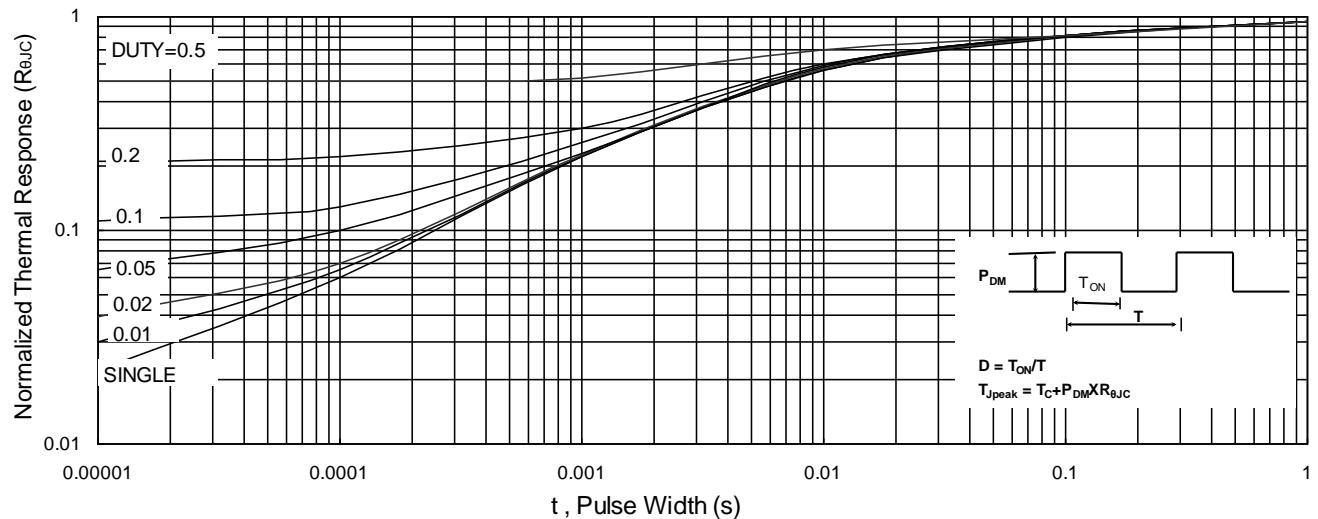
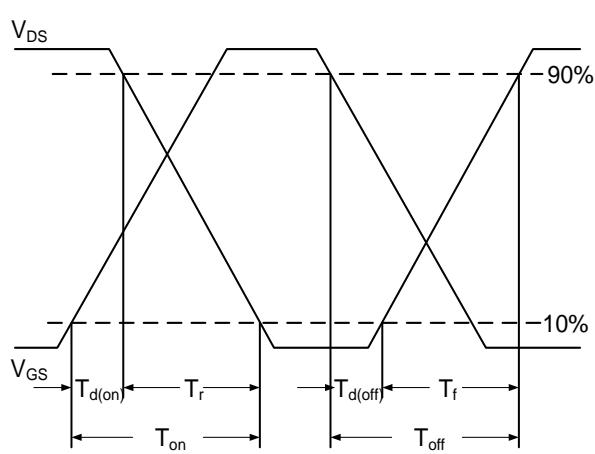
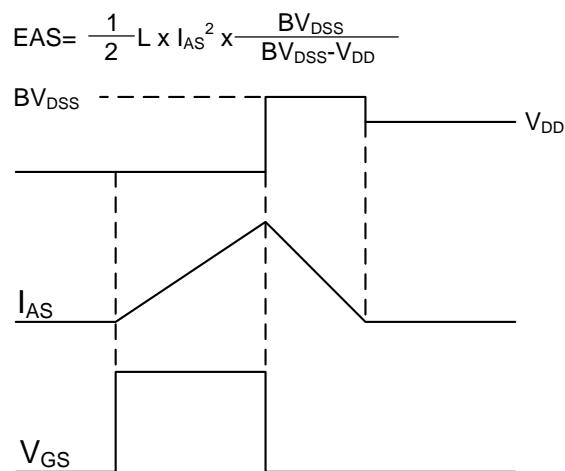
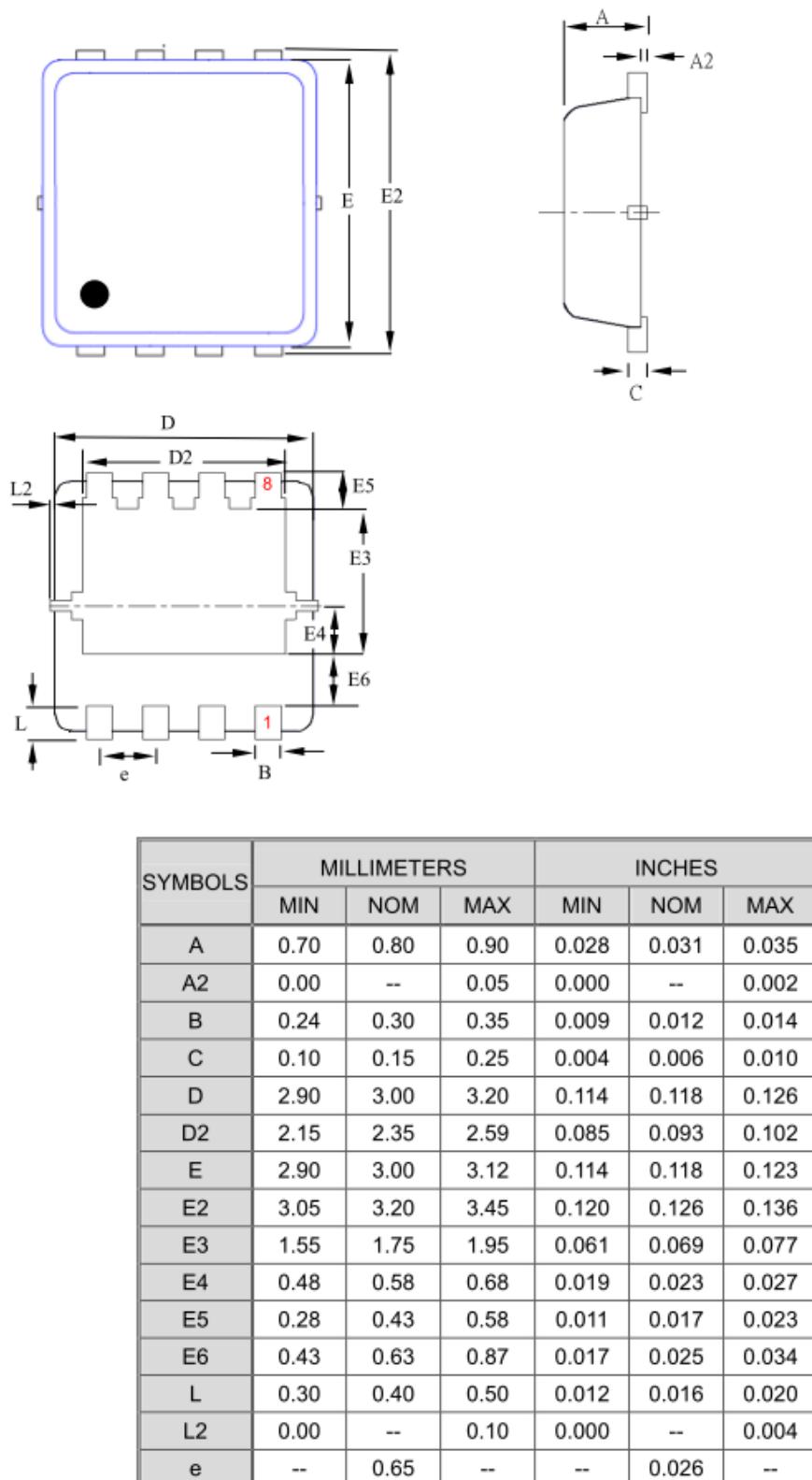


Fig.6 Normalized $R_{DS(on)}$ vs T_J


Fig.7 Capacitance

Fig.8 Safe Operating Area

Fig.9 Normalized Maximum Transient Thermal Impedance

Fig.10 Switching Time Waveform

Fig.11 Unclamped Inductive Waveform

DFN3*3 Package Outline Dimensions



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