Description

JMT Dual N-channel Enhancement Mode Power MOSFET

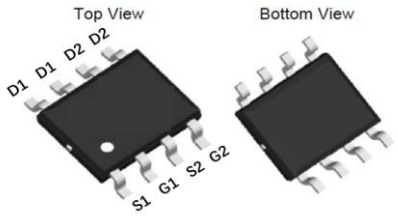
Features

- 20V, 6A
  $R_{DS(ON)}$ < 27mΩ @ V_{GS} = 4.5V
  $R_{DS(ON)}$ < 33mΩ @ V_{GS} = 2.5V
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead Free

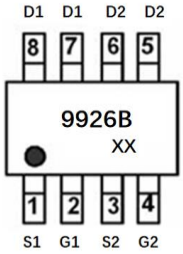
Applications

- Load Switch
- PWM Application
- Power Management

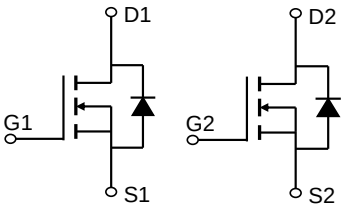
100% UIS TESTED!
100% ΔVds TESTED!



SOP-8 (Dual)



Marking and Pin Assignment



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Outline	Package	Reel Size	Reel(pcs)	Per Carton (pcs)
9926B	JMTP9926B	TAPING	SOP-8	13"	4000	48000

Absolute Maximum Ratings (@ T_A = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		20	V
V_{GS}	Gate-to-Source Voltage		±12	V
I_D	Continuous Drain Current	T_A = 25°C	6	A
		T_A = 100°C	4	
I_{DM}	Pulsed Drain Current (1)		24	A
E_{AS}	Single Pulsed Avalanche Energy (2)		7.5	mJ
P_D	Power Dissipation	T_A = 25°C	1.7	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient(3)		75	°C/W
T_J, T_{STG}	Junction & Storage Temperature Range		-55 to 150	°C

Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	I_D = 250μA, V_{GS} = 0V	20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 20V, V_{GS} = 0V	-	-	1.0	μA
I_{GSS}	Gate-Body Leakage Current	V_{DS} = 0V, V_{GS} = ±12V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250μA	0.5	0.75	1.0	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance(4)	V_{GS} = 4.5V, I_D = 6A	-	21	27	mΩ
		V_{GS} = 2.5V, I_D = 5A	-	25	33	mΩ
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{GS} = 0V, V_{DS} = 10V, f = 1MHz	-	455	-	pF
C_{oss}	Output Capacitance		-	64	-	pF
C_{rss}	Reverse Transfer Capacitance		-	55	-	pF
Q_g	Total Gate Charge	V_{GS} = 0 to 4.5V V_{DD} = 10V, I_D = 2A	-	6	-	nC
Q_{gs}	Gate Source Charge		-	1	-	nC
Q_{gd}	Gate Drain("Miller") Charge		-	1.5	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On DelayTime	V_{GS} = 4.5V, V_{DD} = 10V I_D= 2A, R_{GEN} = 3Ω	-	4	-	ns
t_r	Turn-On Rise Time		-	13	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	65	-	ns
t_f	Turn-Off Fall Time		-	33	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	6	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	24	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS} = 0V, I_S = 6A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	I_F = 2A, di/dt = 60A/us	-	6	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	0.8	-	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. EAS condition: Starting T_J=25C, V_{DD}=10V, V_G=10V, R_G=25ohm, L=0.5mH, I_{AS}=5.5A
3. $R_{\theta JA}$ is measured with the device mounted on a 1inch^2 pad of 2oz copper FR4 PCB
4. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.

Typical Performance Characteristics

Figure 1: Output Characteristics

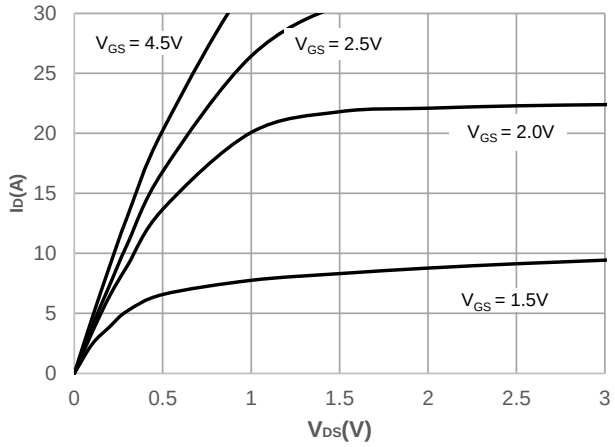


Figure 2: Typical Transfer Characteristics

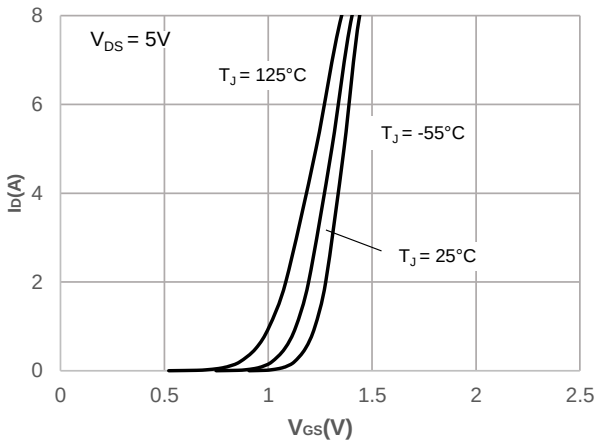


Figure 3: On-resistance vs. Drain Current

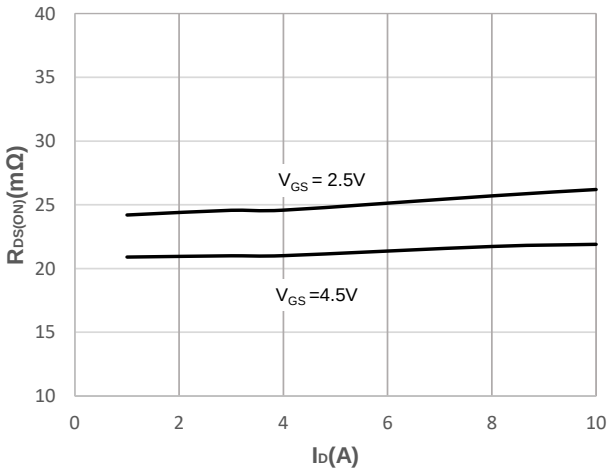


Figure 4: Body Diode Characteristics

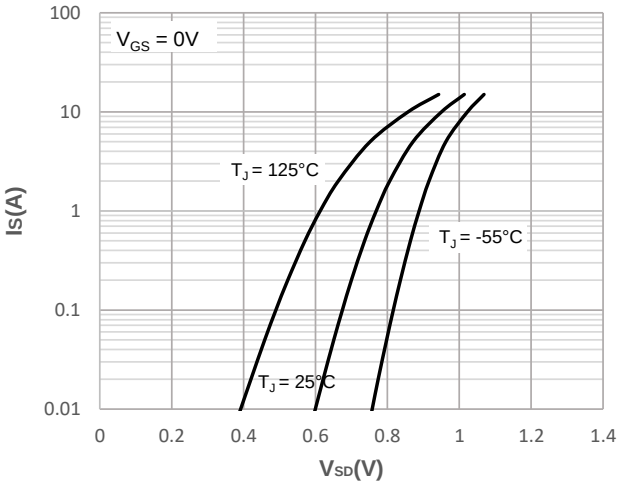


Figure 5: Gate Charge Characteristics

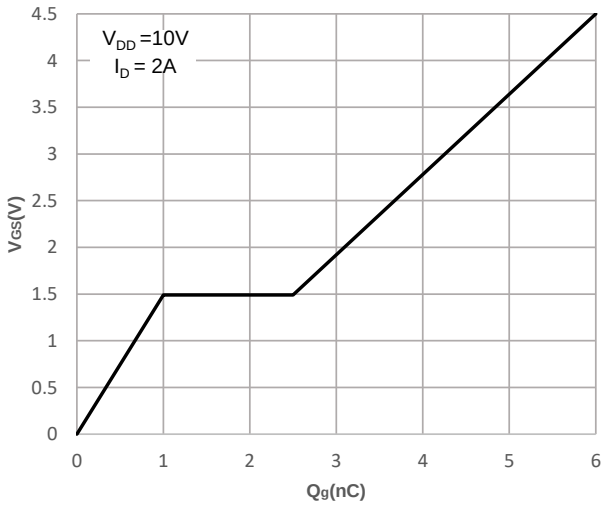
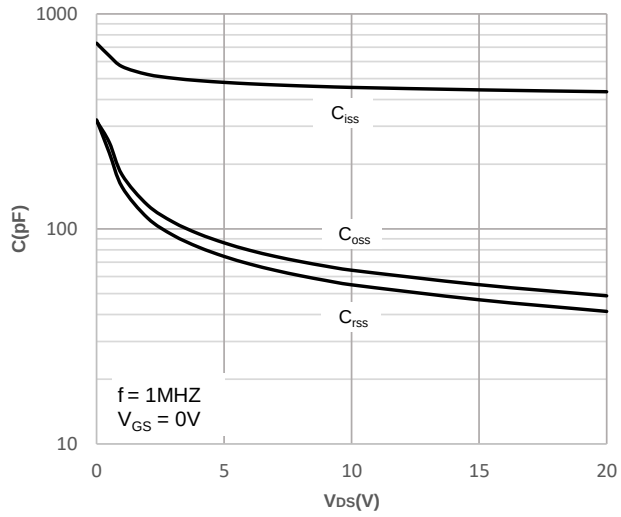


Figure 6: Capacitance Characteristics



Typical Performance Characteristics

Figure 7: Normalized Breakdown voltage vs. Junction Temperature

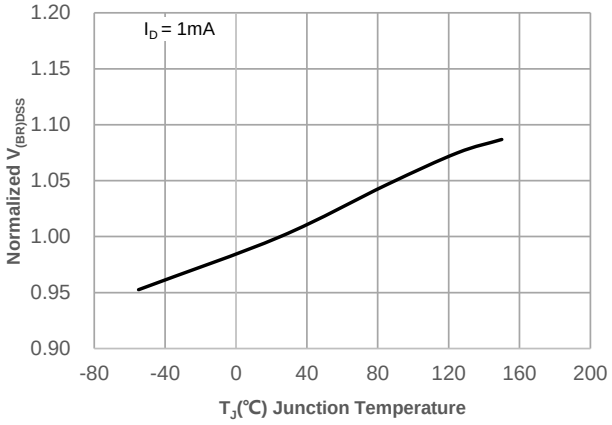


Figure 8: Normalized on Resistance vs. Junction Temperature

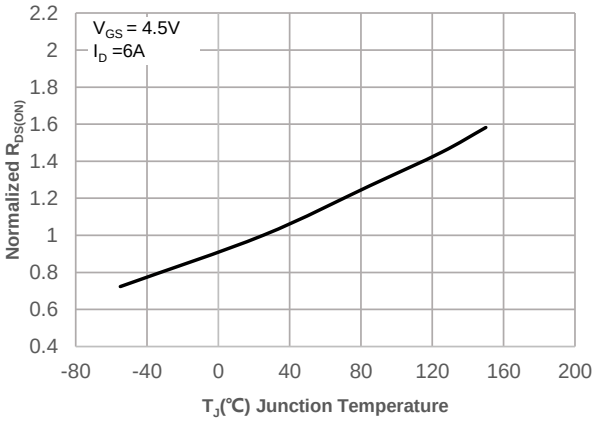


Figure 9: Maximum Safe Operating Area

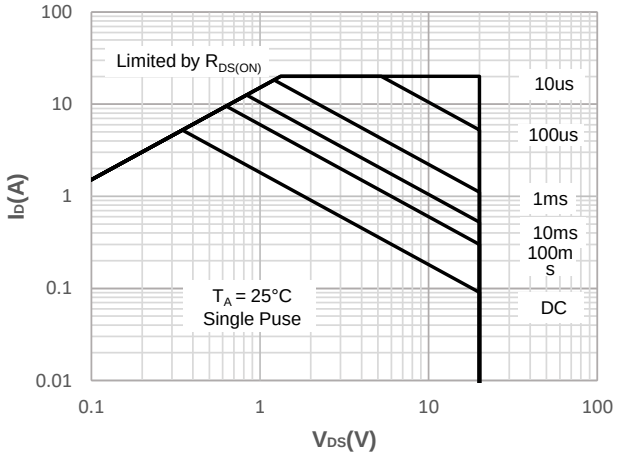


Figure 10: Maximum Continuous Drian Current vs. Ambient Temperature

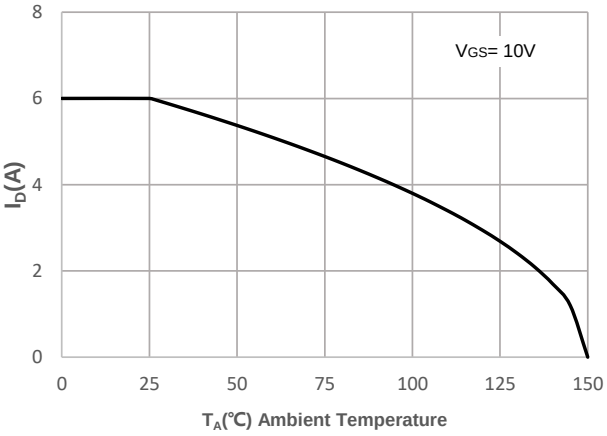


Figure 11: Normalized Maximum Transient Thermal Impedance

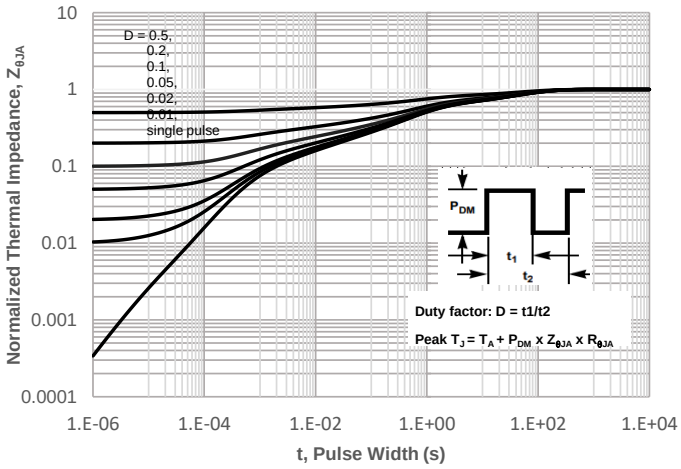
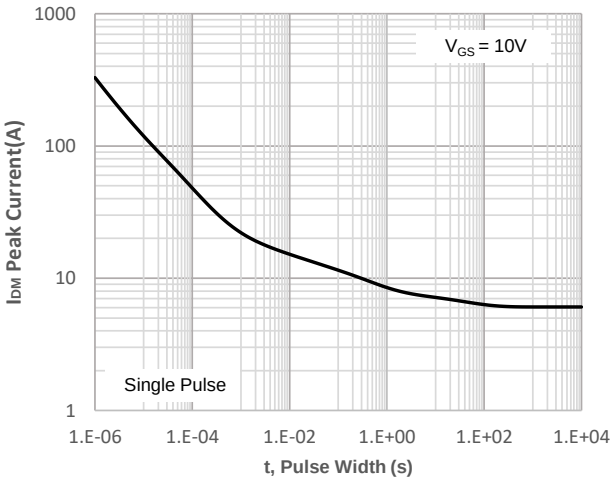


Figure 12: Peak Current Capacity



Test Circuit

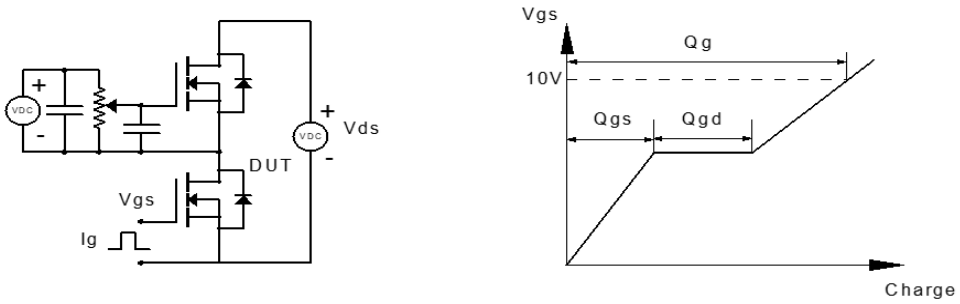


Figure 1: Gate Charge Test Circuit & Waveform

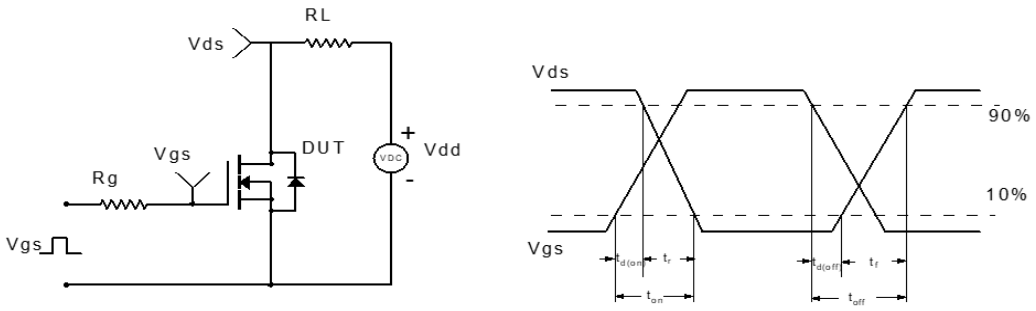


Figure 2: Resistive Switching Test Circuit & Waveform

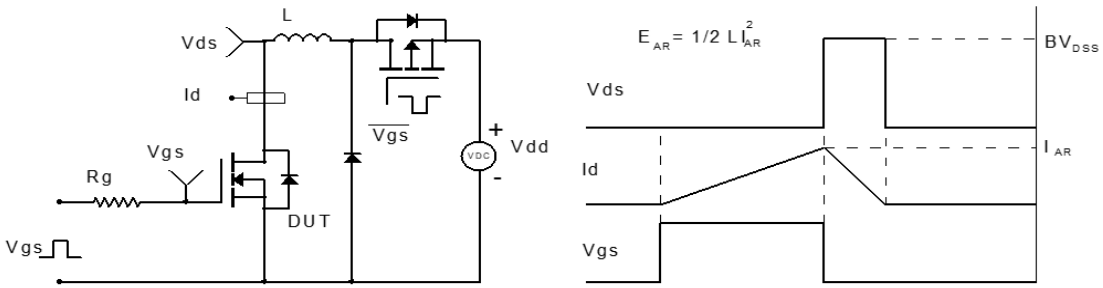


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

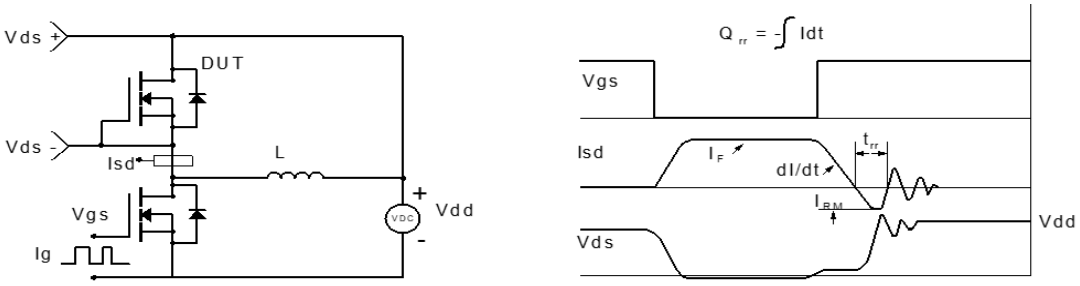
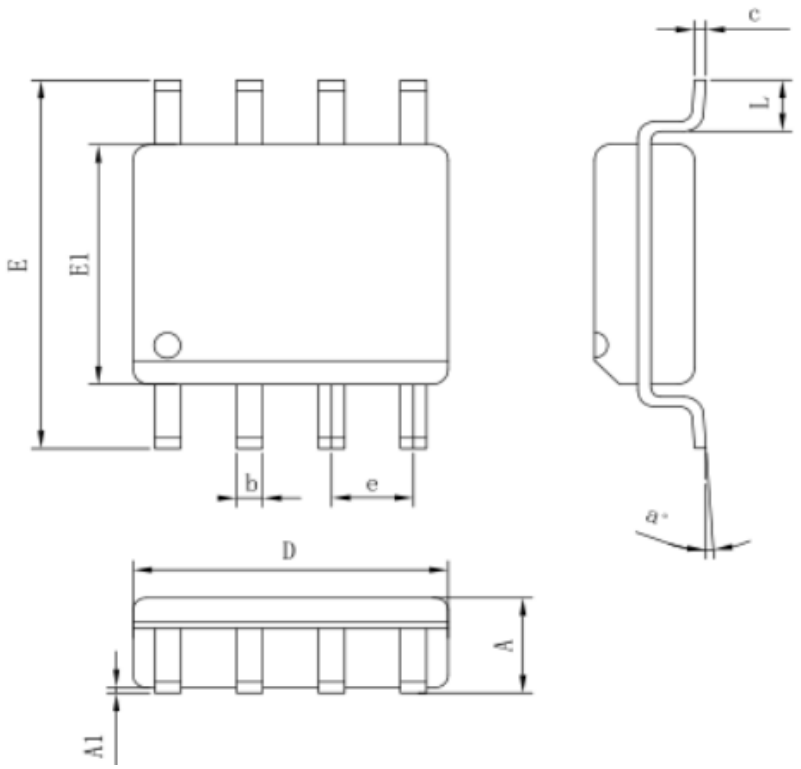


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(SOP-8)



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	--	--	1.75
A1	0.10	--	0.23
b	0.35	--	0.48
c	0.19	--	0.25
D	4.70	4.90	5.00
E	5.80	6.00	6.20
E1	3.70	3.90	4.10
e	1.27BSC		
L	0.50	--	0.80
a°	0°	--	8°