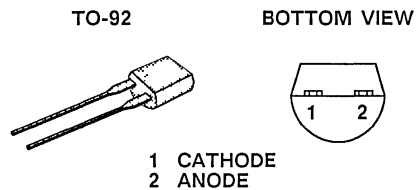


The JPAD5 Series of low-leakage diodes provides a superior alternative to conventional diode technology when reverse current (leakage) must be minimized. These devices feature leakage currents ranging from -5 pA (JPAD5) to -500 pA (JPAD500) to support varying system requirements. Its TO-92 package allows designers to maximize circuit performance while maintaining the objectives of low cost and compact packaging. Tape and reel is available for use with automated assembly techniques. (See Section 8.)

PART NO.	$I_R$ (pA)
JPAD5	-5
JPAD10	-10
JPAD20	-20
JPAD50	-50
JPAD100	-100
JPAD200	-200
JPAD500	-500

## SIMILAR PRODUCTS

- SOT-23, See SSTPAD5 Series
- TO-18, See PAD1 Series
- Duals, See DPAD1 Series
- Chips, Order JPADXXCHP



## ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMIT	UNITS
Forward Current	$I_F$	10	mA
Total Device Dissipation	$P_D$	360	mW
Storage Temperature	$T_{stg}$	-55 to 135	°C
Lead Temperature (1/16" from case for 10 seconds)	$T_L$	300	

# JPAD5 SERIES



ELECTRICAL CHARACTERISTICS <sup>1</sup>							
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT	
			TYP <sup>2</sup>	MIN	MAX		
<b>STATIC</b>							
Reverse Current	$I_R$	$V_R = -20\text{ V}$	JPAD5	-1		-5	pA
			JPAD10	-2		-10	
			JPAD20	-2		-20	
			JPAD50	-5		-50	
			JPAD100	-5		-100	
			JPAD200	-20		-200	
			JPAD500	-20		-500	
Reverse Breakdown Voltage	$BV_R$	$I_R = -1\ \mu\text{A}$	-60	-35		V	
Forward Voltage Drop	$V_F$	$I_F = 5\text{ mA}$	0.8		1.5		
<b>DYNAMIC</b>							
Reverse Capacitance	$C_R$	$V_R = -5\text{ V}, f = 1\text{ MHz}$	1.5		2	pF	

- NOTES: 1.  $T_A = 25\text{ }^\circ\text{C}$  unless otherwise noted.  
 2. For design aid only, not subject to production testing.