JW1532BL

High Efficiency Off-line CV Regulator

Preliminary Specifications Subject to Change without Notice

DESCRIPTION

The JW1532BL is a high efficiency low cost off-line constant voltage regulator for Buck and Buck- Boost topology with 500V MOSFET.

JW1532BL can output 18V/12V default voltage with few external components, which decreases the system cost. In light load condition, JW1532BL operates in green mode, in which the inductor peak current and the switching frequency is lower than that of full load to improve the system efficiency and the reference voltage is decreased to ensure good load regulation.

JW1532BL has multi-protection functions which largely enhance the safety and reliability of the system, including VDD under-voltage lockout (UVLO), short circuit protection (SCP), pulse-by-pulse current limit, over load protection (OLP) and over-temperature protection (OTP).

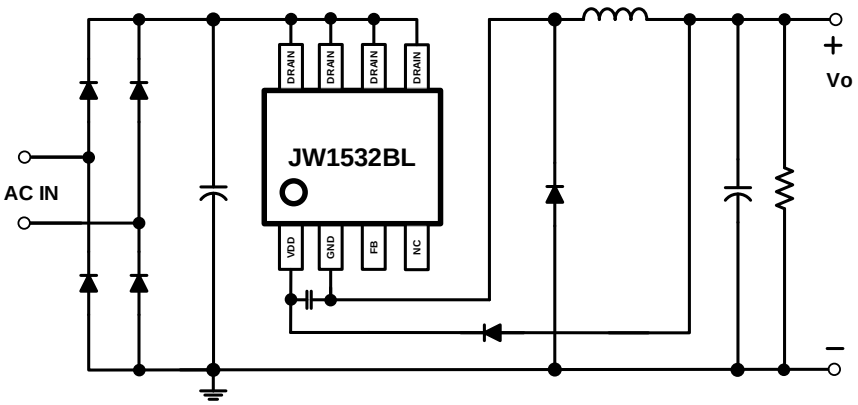
FEATURES

- Ultra Low System BOM Cost
- Integrated with 500V, Low R_{dson} MOSFET
- 18V/12V Default Output Voltage
- Support Buck and Buck-Boost Topology
- Peak Current Mode Control
- Frequency Jittering for Good EMC
- High Efficiency Over Wide Operating Range
- Output Voltage Load Regulation Compensation
- VDD UVLO
- Short Circuit Protection
- Pulse-by-pulse Current Limit
- Over Temperature Protection
- SOP8 Package

APPLICATIONS

- Home Appliance
- Standby Power
- Consumer Electronics

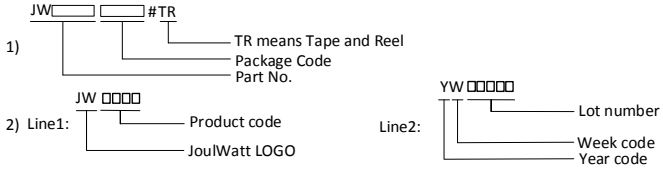
TYPICAL APPLICATION



ORDER INFORMATION

DEVICE[1)]	PACKAGE	TOP MARKING[2)]	ENVIRONMENTAL[3)]
JW1532BLSOPB#TR	SOP8	JW1532BL YW□□□□□	Green

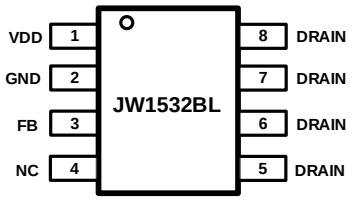
Notes:



3) All JoulWatt products are packaged with Pb-free and Halogen-free materials and compliant to RoHS standards.

PIN CONFIGURATION

TOP VIEW



ABSOLUTE MAXIMUM RATING[1)]

VDD Voltage to GND .....-0.3V to 22V,22V to 28V<1s
DRAIN Voltage to GND.....-0.3V to 500V
FB Voltage to GND……...……...…..…….-0.3V to 6.5V
Junction Temperature[2) 3)] 150ºC
Lead Temperature..... ..... 260ºC
Storage Temperature..... -65ºC to +150ºC
ESD Susceptibility (Human Body Model) 2.5kV

RECOMMENDED OPERATING CONDITIONS

DRAIN Voltage to GND.....400V

Operating Junction Temperature (T_J) .....-40ºC to 125ºC

Package	Recommended MAX Output Current
SOP8	450mA

THERMAL PERFORMANCE[5)]

	θ_{JA}	θ_{JC}
SOP8	96	45ºC/W

Note:

1) Exceeding these ratings may damage the device. These stress ratings do not imply function operation of the device at any other conditions beyond those indicated under RECOMMENDE OPERATING CONDITIONS.

2) The JW1532BL includes thermal protection that is intended to protect the device in overload conditions. Continuous operation over the specified absolute maximum operating junction temperature may damage the device.

3) The device is not guaranteed to function outside of its operating conditions.

4) Measured on JESD51-7, 4-layer PCB.

ELECTRICAL CHARACTERISTICS

T_A=25℃, unless otherwise stated.

Advance Information, not production data, subject to change without notice.

Item	Symbol	Condition	Min.	Typ.	Max.	Units
VDD Quiescent Current	I_Q	V_{DD_ST} -1V	75	100	125	μA
Operation Current	I_{OP}	V_{DD_ST} +1V	180	215	230	uA
VDD Charge Current	I_{CH}	VDD=5V	0.9	1	1.2	mA
VDD Startup Voltage	V_{DD_ST}		9.0	9.5	10.0	V
VDD Under Voltage Lockout	V_{DD_UVLO}		7.0	7.2	7.5	V
VDD Clamping Voltage	V_{CLP}	Sink current =5mA	23	24	25	V
VDD Feedback Reference	V_{DDREF}	FB floating	18.042	18.6	19.158	V
		FB short to GND	12.028	12.4	12.772	V
Peak Current Limit	I_{PK}			1.2		A
Oscillator Frequency	f_{osc}		60	70	78	kHz
Frequency Jittering Range[6)]	$\lvert\pm\Delta f/f_{OSC}\rvert$			8		%
Frequency Jittering Period [6)]	T_{Jit}			15		ms
Maximum On Time	T_{ONMAX}		7.5	8.5	9.5	μs
Leading Edge Blanking Time	T_{LEB}			400		ns
MOS Breakdown Voltage	BV		500			V
MOS Rdson	Rdson	Vgs=10V		5	6	Ω
Over Thermal Protection Threshold[6)]				150		℃
Over Thermal Protection Recovery Hysteresis[6)]				30		℃

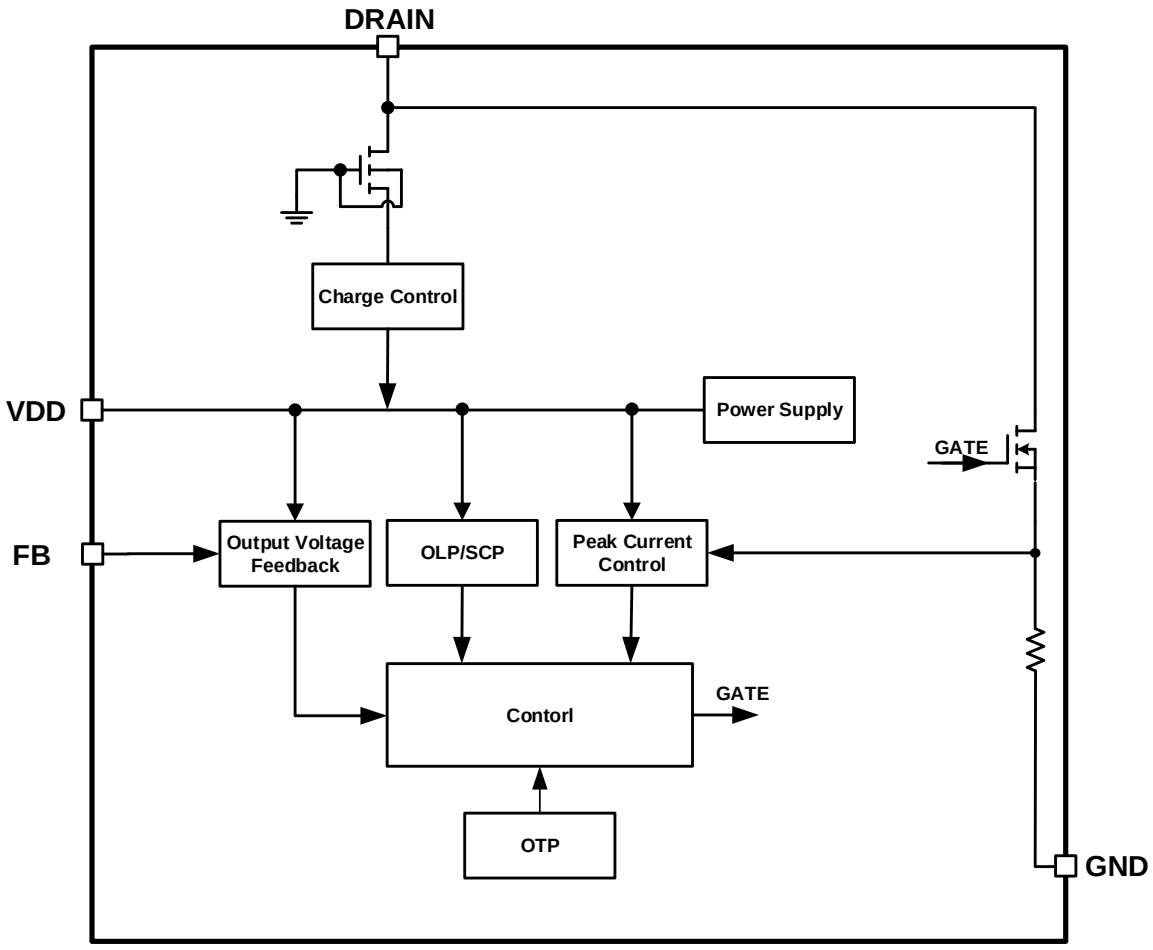
Note：

5) Guaranteed by design.

PIN DESCRIPTION

Pin SOP8	Name	Description
1	VDD	IC power supply and output voltage feedback
2	GND	IC ground
3	FB	Output voltage setup, FB floating-18V, FB short to GND-12V
4	NC	
5	DRAIN	Internal MOS drain and HV power supply
6	DRAIN	Internal MOS drain and HV power supply
7	DRAIN	Internal MOS drain and HV power supply
8	DRAIN	Internal MOS drain and HV power supply

BLOCK DIAGRAM



FUNCTIONAL DESCRIPTION

JW1532BL is a high efficiency low cost off-line constant voltage regulator for Buck and Buck-Boost topology.

Start Up

JW1532BL can be supplied from MOS DRAIN directly. When the internal high voltage(HV) power souse charges VDD up to the V_{DD_ST}, the gate driver starts to switch. VDD will be powered by output voltage in steady state. Once the voltage of VDD is lower than V_{DD_UVLO}, JW1532BL stops switching.

Peak Current Control

JW1532BL has the default peak current for output current. And it also has the SCP limit peak current for abnormal state such as inductance short.

Constant Voltage Control

The output voltage is sensed by VDD pin and adjusted by internal control compensation loop automatically.

The switching frequency of JW1532BL is fixed to f_{osc} with ±8% jittering to improve the EMI performance.

Output voltage can be selected by FB pin setup. If FB is floating, output voltage is 18V, if the FB pin is short to GND, output voltage is 12V.

Green Mode

In light or no load condition, JW1532BL operates in DCM which means the OFF time is very long. JW1532BL will reduce the peak current of the inductor to minimize the power loss. The longer Toff, the lower I_{PK}.

Short Circuit Protection (SCP)/ Over Load Protection (OLP)

In short circuit or over load condition, VDD can't be charged to V_{REF}. JW1532BL will operate in auto-restart mode which is represented in the following description if VDD<V_{REF} for some time.

Auto-restart Mode

JW1532BL will enter auto-restart mode if SCP/OLP/OTP is triggered. The chip stops switching and the HV power source is disconnected until VDD decreases to V_{DD_UVLO}. If VDD is charged to V_{DD_ST} for several cycles, the system restarts.

Over Temperature Protection

When internal temperature of the chip exceeds 150°C, JW1532BL operates in auto-restart mode to help the chip cooling.

PCB Design

1. The VDD pin must be locally bypassed with a capacitor.
2. Make the area of the power loop as small as possible in order to reduce the EMI radiation.

APPLICATION REFERENCE

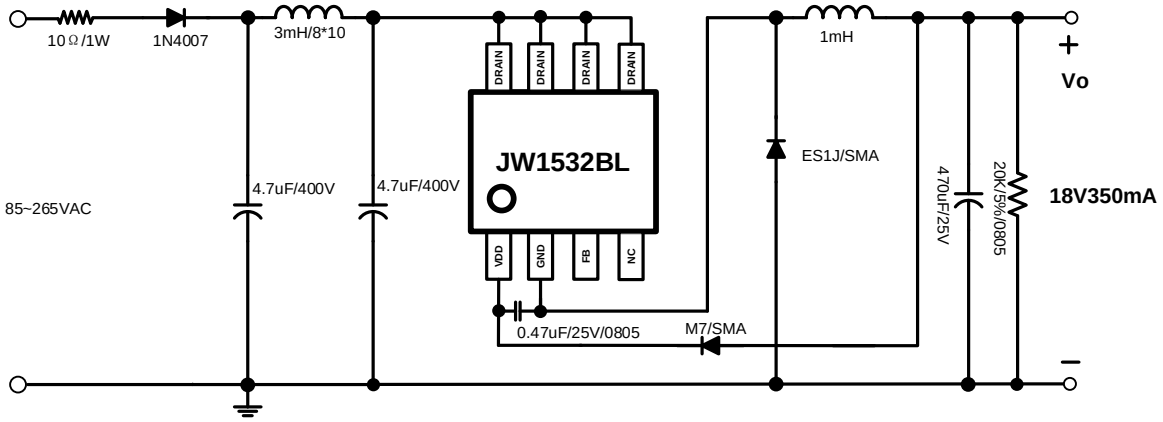
Note: Information in the following reference design sections is not part of JoulWatt component specification. Customers are responsible for determining suitability of components chosen for their purposes and should validate their design implementation to make sure the proper system functionality.

The reference design is suitable for non-isolated buck power supply default 18V output, using JW1532BL.

V_{IN}: 85~265VAC

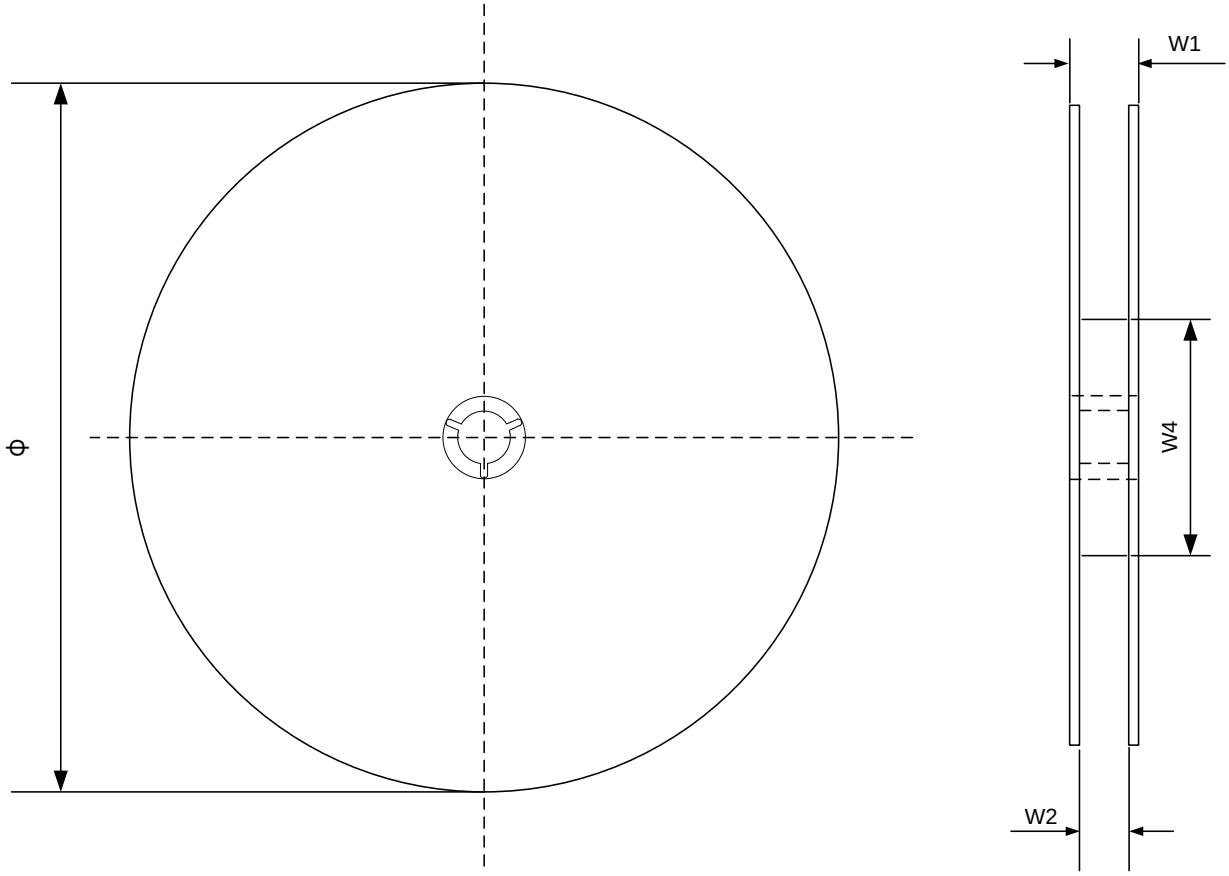
V_{OUT}: 18V

I_{OUT}: 350mA



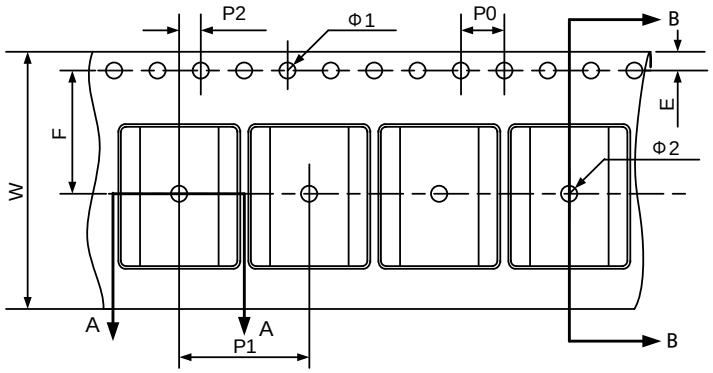
TAPE AND REEL INFORMATION

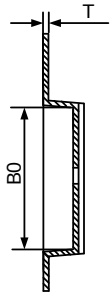
Reel UNIT: mm



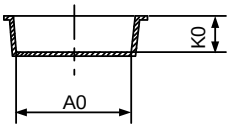
Package	Diameter Φ	Thickness W1	Width W2	W4
SOP8	330±2	17.6±2	12.4±2	100±2

Carrier Tape UNIT: mm





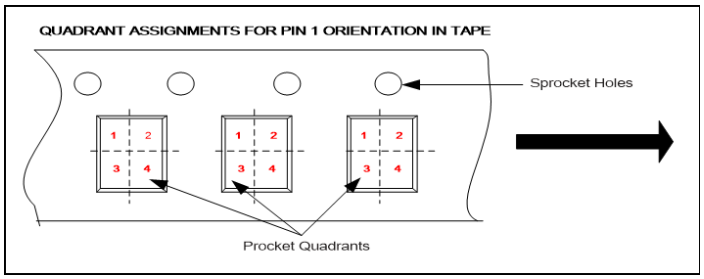
SECTION B-B



SECTION A-A

Note :

1) The carrier type is black, and colorless transparent.
2) Carrier camber is within 1mm in 100mm.
3) 10 pocket hole pitch cumulative tolerance:±0.20.
4) All dimensions are in mm.

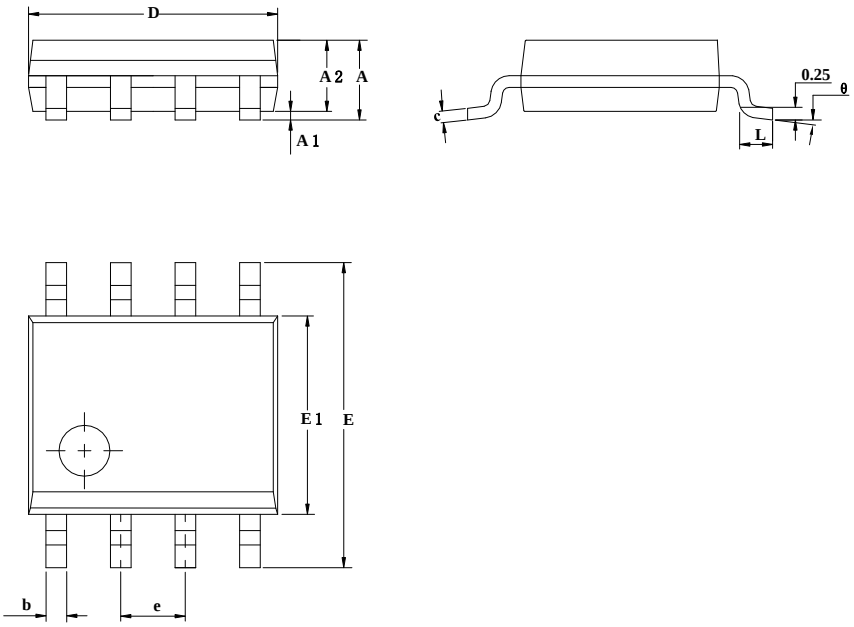


Package Type	Pin1 Quadrant
SOP8	1

Package	Tape dimension (mm)											
	P0	P2	P1	A0	B0	W	T	K0	Φ1	Φ2	E	F
SOP8	4.0±0.1	2.0±0.1	8.0±0.1	6.40±0.3	5.35±0.3	12.0±0.3	0.25±0.2	2.00±0.2	1.50min	1.50min	1.75±0.1	5.50±0.10

PACKAGE OUTLINE

SOP8 **UNIT :** mm



Symbol	MILLIMETER		
	MIN	NOM	MAX
A	—	—	1.75
A1	0.05	0.15	0.25
A2	1.25	1.40	1.65
b	0.32	0.42	0.52
c	0.10	0.20	0.30
D	4.50	5.00	5.50
E	5.50	6.00	6.50
E1	3.50	3.90	4.30
e	1.27TYP		
L	0.40	—	1.27
θ	0°	—	8°

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