

®JW19673C/JW19673D

BUCK LED Regulator With Power Factor Correction

DESCRIPTION

The JW19673C/ JW19673D (JW19673X series) is a constant current LED regulator which applies to single stage step-down power factor corrected LED drivers.

JW19673X series integrates high voltage power source, and can be supplied by line voltage directly, auxiliary winding and VCC capacitor are not needed.

Patented current sensing and digital compensation technics ensure a unit power factor and high accuracy output current. The critical conduction mode operation reduces the switching losses and increases the efficiency.

JW19673X series has multi-protection functions which largely enhance the safety and reliability of the system, including LED open protection, LED short protection, VIN over voltage protection and over temperature protection.

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FEATURES

- No Auxiliary Winding and VCC Capacitor
- Supplied from Line Voltage Directly
- High Current Accuracy of Line and Load Regulation
- Internal Compensation PFC Technics
- Critical Conduction Mode
- High Efficiency Over Wide Operating Range
- High Voltage Power MOSFET Integrated
- LED Open Protection
- LED Short Protection
- VIN Over Voltage Protection
- Internal Over Temperature Protection
- SOP7 Package

APPLICATIONS

Non-isolation Offline LED Driver

TYPICAL APPLICATION



JW19673C/JW19673D

ORDER INFORMATION

DEVICE ¹⁾	PACKAGE	TOP MARKING ²⁾
JW19673CSOPA#TRPBF	SOP7	JW19673C YW
JW19673DSOPA#TRPBF	SOP7	JW19673D YW

Notes:



3) All JoulWatt products are packaged with Pb-free and Halogen-free materials and compliant to RoHS standards.

DEVICE INFORMATION

DEVICE	MOS RDSON
JW19673CSOPA#TRPBF	4.7Ω
JW19673DSOPA#TRPBF	2.5Ω

PIN CONFIGURATION



ABSOLUTE MAXIMUM RATING¹⁾

VIN Voltage	700V
OVP,ISP Voltage	6V
Junction Temperature ^{2) 3)}	150ºC
Lead Temperature	260°C
Storage Temperature	65°C to +150°C

RECOMMENDED OPERATING CONDITIONS

SOP7	96	.45ºC/W
THERMAL PERFORMANCE ⁴⁾	$ heta_{J\!A}$	$ heta_{\scriptscriptstyle JC}$
Operating Junction Temperature (T _J)	-40°C	to 125°C
VIN Voltage	30V	/ to 500V

Note:

1) Exceeding these ratings may damage the device. These stress ratings do not imply function operation of the device at any other conditions beyond those indicated under RECOMMENDED OPERATING CONDITIONS.

2) The JW19673X series includes thermal protection that is intended to protect the device in overload conditions. Continuous operation over the specified absolute maximum operating junction temperature may damage the device.

3) The device is not guaranteed to function outside of its operating conditions.

4) Measured on JESD51-7, 4-layer PCB.

ELECTRICAL CHARACTERISTICS

$T_A=25^{\circ}C$, unless	otherwise stated.						
lte	em	Symbol	Condition	Min.	Тур.	Max.	Units
POWER SUPPLY							
VIN Start-up Voltage)	VIN_ST				10	V
VIN Quiescent Curre	ent	linq			180	225	μA
CURRENT REGUL	ATION						
ISP Sample Referer	nce	Vref		192	200	208	mV
Minimum On Time o	f MOSFET ⁵⁾	T _{ON_MIN}			1.2		μS
Maximum On Time	of MOSFET	Ton_max		3.75	5	7.25	μS
Minimum Off Time o	f MOSFET ⁵⁾	Toff_min			2		μS
Maximum Off Time	of MOSFET	T _{OFF_MAX}		255	340	425	μS
Maximum Switch Frequency ⁵⁾		f _{MAX}			100		KHz
Switching Period of	VINL ⁵⁾	TVINL			65		μS
PROTECTION				-			
ISP Over Voltage Pr	otection Threshold	VISP_MAX		1.08	1.2	1.32	V
		Vo_ovp1	R _{OVP} =510KΩ	84	93	102	
		Vo_ovp2	OVP Short	93	103	113	V
Vo Over Voltage Pro	tection Threshold	Vo_ovp3	OVP NC	113	125	137	
		Vo_ovp4	R _{OVP} =120KΩ	216	239	262	
OVP Pin Current		IOVP		3.45	4	5.75	μA
VIN Over Voltage Protection Threshold ⁵⁾		VIN_OVP			440		V
Thermal Protection Threshold ⁵⁾		T _{OTP}		140	150		°C
MOS							
MOS Rdson ⁵⁾	JW19673C	Rdson	Vgs=10V		4.7		
	JW19673D				2.5		Ω
Breakdown Voltage		BV		650			V

Note:

5) Guaranteed by design.

PIN DESCRIPTION

Pin No.	Name	Description
1	GND	Chip ground.
2	OVP	Set OVP threshold.
3	NC	No connection.
4	VIN	Bus voltage input.
5,6	DRAIN	DRAIN of the power MOSFET.
7	ISP	Output current sense.

BLOCK DIAGRAM



FUNCTIONAL DESCRIPTION

The JW19673X series is a constant current LED driver which applies to non-isolation step-down LED system with power factor correction. JW19673X series can achieve excellent line and load regulations, high efficiency and low system cost with few peripheral components.

Start Up

JW19673X series is supplied by line voltage directly. When VIN reaches VIN Start-up Voltage (V_{IN_ST}), the chip begins to switch. Once VIN is lower than VIN under voltage lockout, JW19673X series stops switching.

Constant Current Control

The JW19673X series controls the output current from the information of the sensing resistor voltage. The output LED mean current can be calculated as:

 $I_{LED} = V_{REF} / R_{SNS}$

Where

V_{REF}- ISP sample reference;

 $R_{\mbox{\scriptsize SNS}}$ – The sensing resistor connected between ISP and GND.

Critical Conduction Mode Operation

JW19673X series works in the Critical conduction mode of the inductor current. When the power MOSFET is turned on, the inductor current begins to increase from zero. The turn on time of the MOSFET can be calculated as:

 $T_{ON} = I_{PK} \times L / (V_{IN} - V_{OUT})$

Where,

L - Inductance;

I_{PK} – Peak current in one switch cycle;

 V_{IN} – Input voltage after rectification and filtering;

 V_{OUT} – Output LED voltage.

When the power MOSFET is turned off, the inductor current begins to decrease. The power MOSFET turns on again when the inductor

current is zero. The turn off time of the MOSFET can be calculated as:

 $T_{OFF} = I_{PK} \times L / V_{OUT}$

And the inductance of the system can be calculated as:

 $L = V_{OUT} \times (V_{IN} - V_{OUT}) / (f \times I_{PK} \times V_{IN})$ Where, f is the switching frequency of the step-down system.

LED Over Temperature Protection

When internal temperature of the chip exceeds the Thermal Protection Threshold(T_{OTP}), JW19673X series decreases LED current to help the chip cooling.

VIN Over Voltage Protection

When Vin voltage is higher than VIN Over Voltage Protection Threshold(V_{IN_OVP}), JW19673X series decreases LED current to improve the reliability of the system.



Figure. 1 VIN over voltage protection

LED Open Protection

The OVP threshold (V_{O_OVP}) is set by the OVP pin. When Vo is higher than V_{O_OVP} , LED open protection is triggered and the chip stops switching for 800ms in typical. The following table shows the V_{O_OVP} design guide:

	-	
OVP Pin	$V_{O_OVP}(V)$	
Connected with $510K\Omega$	Vo_ovp1	
resistor		
Short connected	V_{O_OVP2}	
Not connected	Vo_ovp3	
Connected with 120KΩ		
resistor	VO_OVP4	

APPLICATION NOTES

1: R_{VIN} and V_{O_OVP} design guide

To enhance the surge capability, VIN pin of JW19673X series should be connected to input capacitor by a resistor R_{VIN} (0805/1206 package, no less than 4.7K Ω is recommended). Larger R_{VIN} means better surge capability but should note that too large R_{VIN} may decrease the drive current, and the maximum R_{VIN} is limited by





Figure. 2 R_{VIN} design guide

 R_{VIN} and V_{O_OVP} design guide is shown in following Tab:

V _{O_PEAK} (V)	Recommended R _{VIN} (Ω)	OVP(V)
30~45	5.1K	
46~70	10K	VO_OVP1
71~80	10K~15K	Vo_ovp2
81~95	10K~20K	Vo_ovp3
96~180	20K~51K	V _{O_OVP4}

Where $V_{O_{PEAK}}$ is the peak value of the Vo, the ripple of the Vo and suitable margin should be taken into consideration when designing the OVP.

2: PCB Design

When designing the PCB of the JW19673X series system, please follow the directions:

- 1. Make the area of the power loop as small as possible in order to reduce the EMI radiation.
- 2. The chip should be far away from the heating element, such as the power inductor and the freewheel diode.

REFERENCE DESIGN

Note: Information in the following reference design sections is not part of JoulWatt component specification. Customers are responsible for determining suitability of components chosen for their purposes and should validate their design implementation to make sure the proper system functionality.

This reference design is suitable for 8 ~ 15W non-isolated Step-down LED driver, using JW19673C, with high efficiency, excellent line regulation.

Reference :

V_{IN}: 176VAC~264VAC V_{OUT}: 46~90V I_{OUT}: 160mA PF: >0.9



PACKAGE OUTLINE



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