

Parameters Subject to Change Without Notice

DESCRIPTION

The JW[®]5510 is a highly integrated Boost converter and can be widely used in portable applications.

The JW5510 implements a synchronous high-efficiency step-up boost converter with 10A switch current capability and is capable of providing an output voltage up to 20V.

The integrated low $R_{ds(on)}$ MOSFET minimizes physical footprint, maximizes the efficiency, which reduces the power dissipation. Constant current control is utilized to protect the device from overshooting in unwanted conditions. Built-in loop compensation simplifies the circuit and design. PFM is engaged to maintain high efficiency at light load.

JW5510 guarantees robustness with thermal protection and under voltage lockout.

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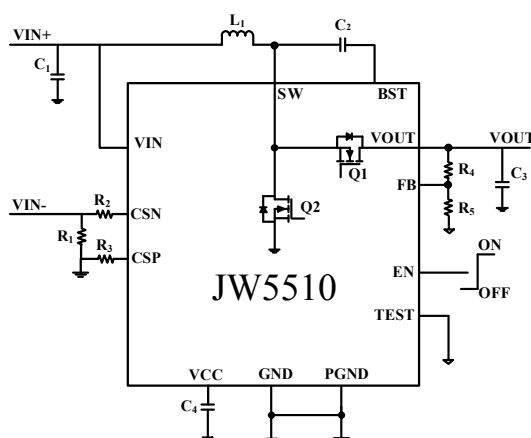
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FEATURES

- 3.0V to 20V input voltage range
- 4.0V to 20V output voltage range
- Integrate Low $R_{DS(on)}$ Switch Power MOS and Synchronous Rectifier MOS.
- 10A Switch Current Limit
- Switch Frequency: 500kHz.
- High Efficiency over Full Load Range
- Thermal Shutdown
- QFN3*4 package

APPLICATIONS

- Battery Powered system
- Electronic Cigarette
- Bluetooth Speaker



Typical application circuit

DEVICE ¹⁾	PACKAGE	TOP MARKING ²⁾
JW5510QFNE#TRPBF	QFN3×4-15	JW5510 XXXXXX

1) JW #TRPBF

Part No. Package Code PB Free
Tape and Reel (If "TR" is not shown, it means tube)

Pin configuration diagram for the ADXL345 in 16-pin TSSOP package. The pins are numbered 1 to 16. Pin 1 is CSP, Pin 2 is VOUT, Pin 3 is PGND, Pin 4 is VOUT, Pin 5 is FB, Pin 6 is TEST, Pin 7 is VIN, Pin 8 is BST, Pin 9 is SW, Pin 10 is SW, Pin 11 is EN, Pin 12 is NC, Pin 13 is VCC, Pin 14 is GND, Pin 15 is CS.

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Note:

- 1) Exceeding these ratings may damage the device.
- 2) The JW5510 guarantees robust performance from -40°C to 150°C junction temperature. The junction temperature range specification is assured by design, characterization and correlation with statistical process controls.
- 3) The JW5510 includes thermal protection that is intended to protect the device in overload conditions. Thermal protection is active when junction temperature exceeds the maximum operating junction temperature. Continuous operation over the specified absolute maximum operating junction temperature may damage the device.
- 4) Measured on JESD51-7, 4-layer PCB.

ELECTRICAL CHARACTERISTICS

VIN=5V, TA=25 °C, unless otherwise stated						
Item	Symbol	Condition	Min.	Typ.	Max.	Units
General parameters						
VIN Voltage Range	V _{IN}		3.0		20	V
VOUT voltage Range	V _{OUT}		4.0		20	V
VCC output voltage	V _{CC}	I _{VCC} =2mA		4.9		V
VCC output current limit	I _{VCC}	V _{CC} >2.7V		50		mA
Supply current in shut-down mode	I _Q	V _{IN} =4V, EN=0V		65		μA
Switch frequency	F _{sw}			500		kHz
Switch minimum off time	T _{off_min}		80	100	120	ns
EN Logic HIGH	V _{ENH}	V _{IN} =8V			2.0	V
EN Logic LOW	V _{ENL}	V _{IN} =8V	0.8			V
Top switch on-resistance	R _{dsbkTG}			12		mΩ
Bottom switch on-resistance	R _{dsbkBG}			12		mΩ
Feedback voltage	V _{FB}			0.9		V
Input average current limit	I _{IN_LIM}	R ₁ =5mΩ; R ₂ =R ₃ =3kΩ;		6		A
Protection						
VOUT OVP threshold	V _{O_OVP}	VOUT rising		23.2		V
		VOUT falling		21.5		V
VOUT OVP deglitch time	t _{O_OVP}			2		μs
VIN UVLO threshold	V _{IN_UVLO}	VIN rising		3.0		V
		VIN falling		2.7		V
Thermal shutdown threshold ⁵⁾	T _{SHUT}			150		°C
Thermal recovery threshold ⁵⁾	T _{REC}			130		°C

Notes:

5) Guaranteed by design.

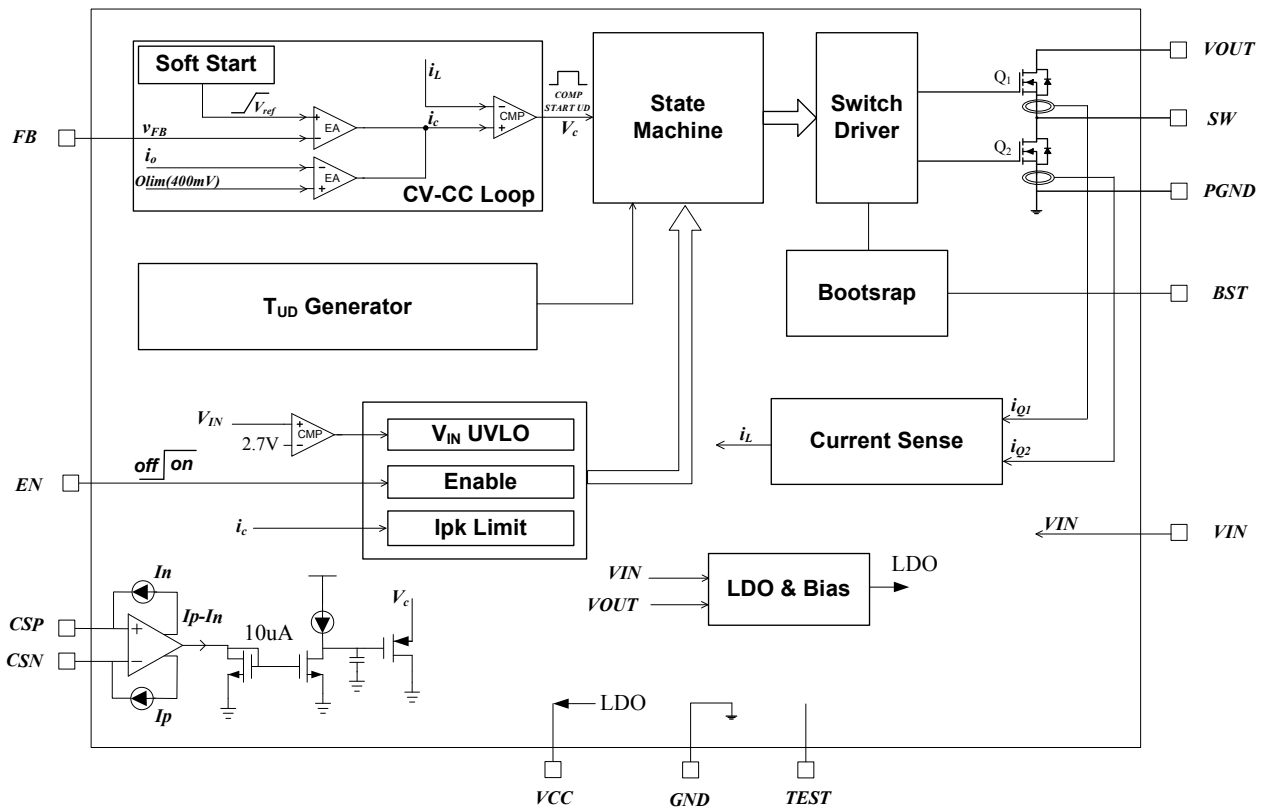
PIN DESCRIPTION

Pin No.	Name	Description
1	CSP	Positive terminal of current sense.
2, 4	VOUT	Output pin, connect this pin to GND with ceramic capacitor.
3	PGND	Power Ground.
5	FB	Output feedback pin.
6	TEST	Test pin. Tie this pin to GND for normal operation. Do not allow this pin to float.
7	VIN	Input voltage sense pin.
8	BST	Bootstrap supply pin for top switch. Connect this pin to SW with an 0.1 μ F capacitor.
9, 10	SW	Power switching node. Connect to input with inductor
11	EN	Enable control pin. Forcing the pin below 0.8V shuts down the converter, reducing quiescent current. Once the EN pin rises above 2.0V, the IC is turned on.
12	NG	No connection.
13	VCC	4.9V LDO for power driver and internal circuit. Must be bypassed to GND with a minimum of 10 μ F ceramic capacitor for stable operation.
14	GND	Signal GND.
15	CSN	Negative terminal of current sense.

Notes:

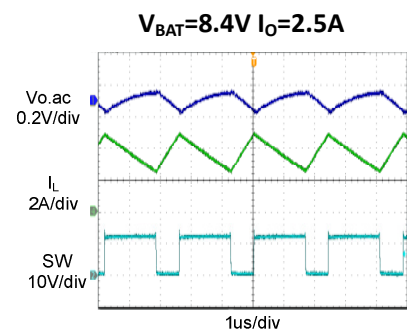
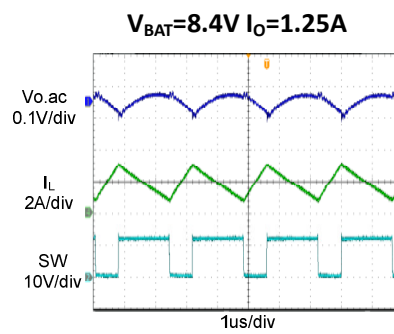
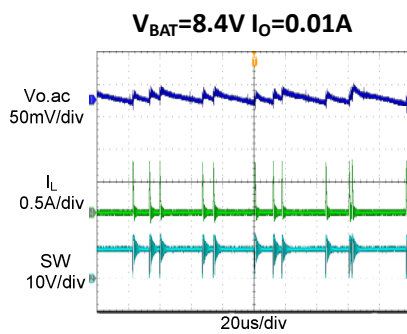
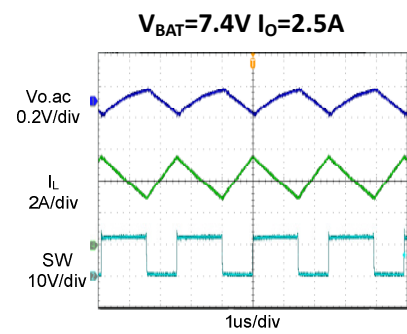
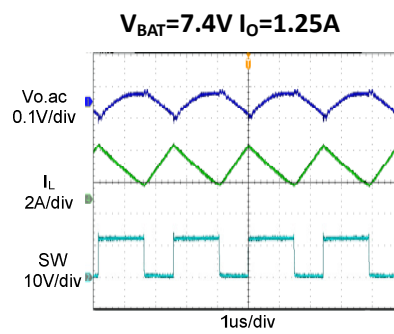
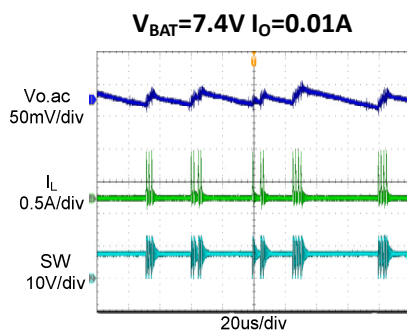
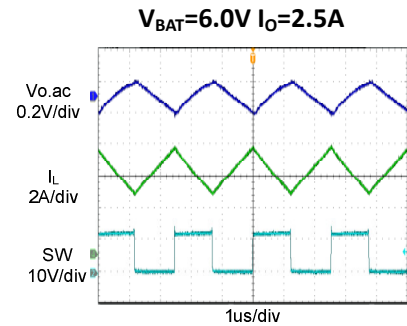
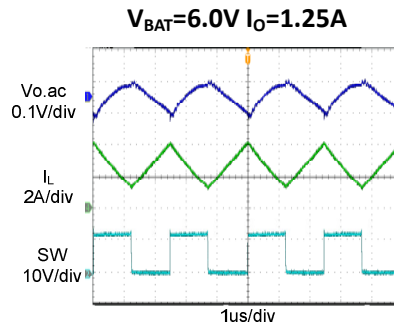
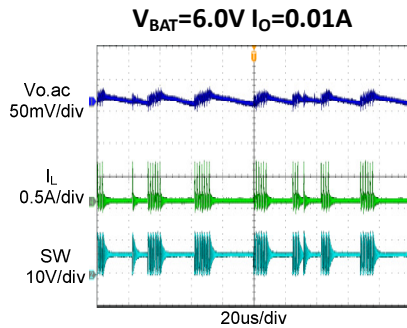
Highlighted pins are high current pins.

BLOCK DIAGRAM

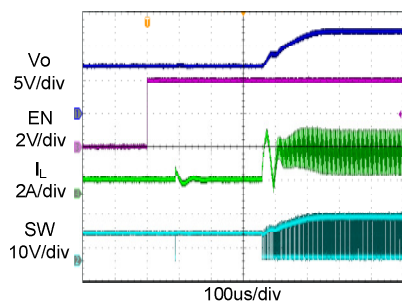


TYPICAL PERFORMANCE CHARACTERISTICS

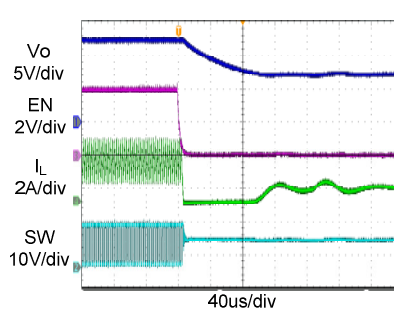
$V_O = 12V$, $L = 2.4\mu H$, $C_{IN} = 22\mu F \times 2$, $C_O = 22\mu F \times 5$, $T_A = +25^\circ C$, unless otherwise noted.



EN ON @ $V_{BAT}=7.4V$ $I_O=1.5A$

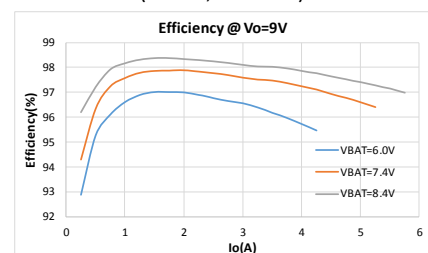


EN OFF @ $V_{BAT}=7.4V$ $I_O=1.5A$

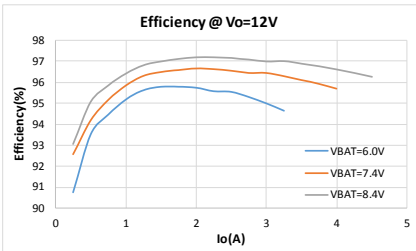


Efficiency vs. I_O @ $V_O=9V$

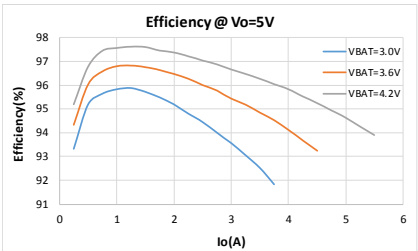
(2 cells, $L=2.4\mu H$)



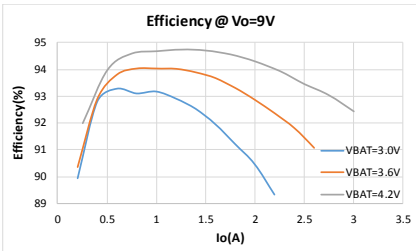
Efficiency vs. I_o @ $V_o=12V$
(2 cells, $L=2.4\mu H$)



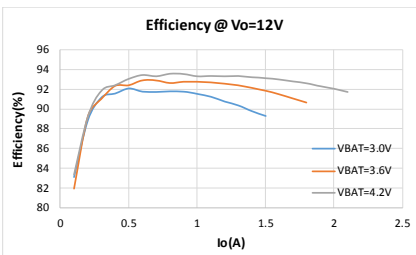
Efficiency vs. I_o @ $V_o=5V$
(single cell, $L=1.2\mu H$)



Efficiency vs. I_o @ $V_o=9V$
(single cell, $L=1.2\mu H$)



Efficiency vs. I_o @ $V_o=12V$
(single cell, $L=1.2\mu H$)



FUNCTIONAL DESCRIPTION

JW5510 is a monolithic DC to DC converter that can operate over a wide input voltage range of 3.0V to 20V. The output voltage can be programmed up to 20V. Internal low $R_{DS(on)}$ N-channel power switches reduce the solution complexity and increase the efficiency.

Flexible Boost Converter

The JW5510 is a flexible boost DC-DC converter which utilizes proprietary single inductor current-mode control with better dynamic response.

Compensation is done internally on the chip. The JW5510 operates in PFM mode at light load. In PFM mode, switching frequency is continuously controlled in proportion to the load current, i.e. switch frequency is decreased when load current drops to increase power efficiency at light load by reducing switching-loss, minimizing the circuit.

Output voltage

The output voltage is set by an external feedback resistive divider. The feedback signal is compared with internal precision 0.9V voltage reference by an error amplifier. The output voltage is given by the equation:

$$V_o(V) = \frac{0.9(V) \times (R_4 + R_5)}{R_5}$$

Where R_4 and R_5 are defined in typical application figure.

Programmable Input / Output Current Limit

As shown in figure1, the current sense resistor R_{CS} should be placed at input terminal or output terminal and closed to the R_{ISET} . The input/output current limit is set by R_{ISET} and R_{CS} ,

which is optional. If the input/output current limit is not desired, the CSN pin should be shorted to VCC, and the CSP pin shorted to GND.

$$I_{LIMIT}(A) = \frac{R_{ISET}(k\Omega)}{R_{CS}(m\Omega)} \times 10(\mu A/A)$$

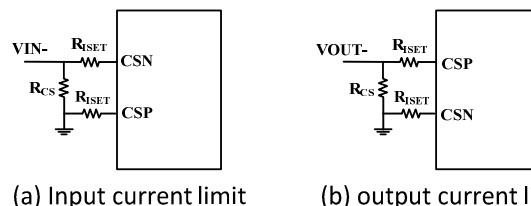


Figure1. Programmable current limit

When the input/output current equals to the current limit threshold, the current limit loop begins to work, it turns down output voltage to limit the output power.

VIN UVLO

When V_{IN} decreases to V_{IN_UVLO} , the converter is terminated. When the V_{IN} recovers and is larger than V_{IN_UVLO} , the JW5510 can re-work if the V_{EN} is still high.

Thermal Control

When the junction temperature of the JW5510 rises above 135°C, it begins to reduce the output power to prevent the temperature from rising further. If the junction temperature of JW5510 rises above 150°C, the chip stops.

Shut-down Mode

The JW5510 shuts down when voltage at EN pin is below 0.8V. The entire regulator is off.

Output Over-Voltage Protection

If the output voltage is larger than V_{O_OVP} rising threshold, the device stops switching. Until the output voltage is less than V_{O_OVP} falling threshold, the device re-starts switching again.

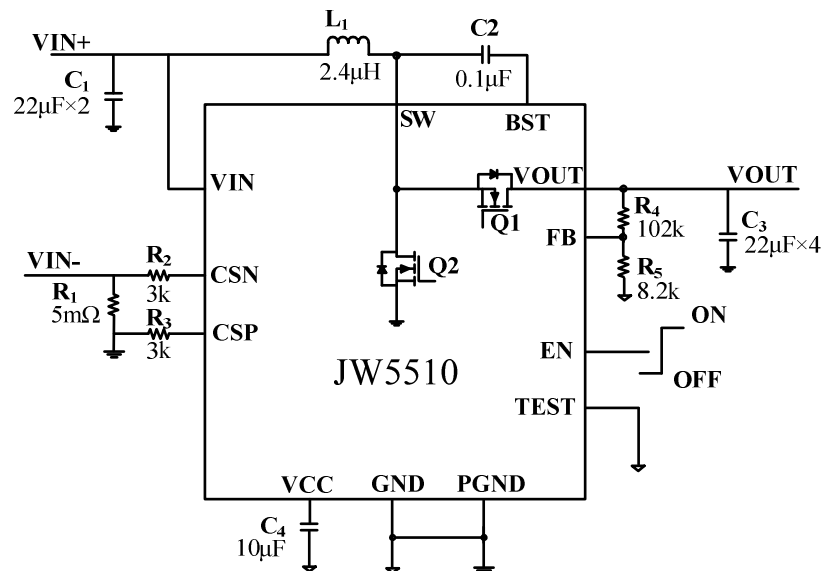
Reference Design

Reference 1: Input current limit set

V_{IN} : 6.0V ~ 8.4V

V_{OUT} : 12V

I_{IN_LIM} : 6A



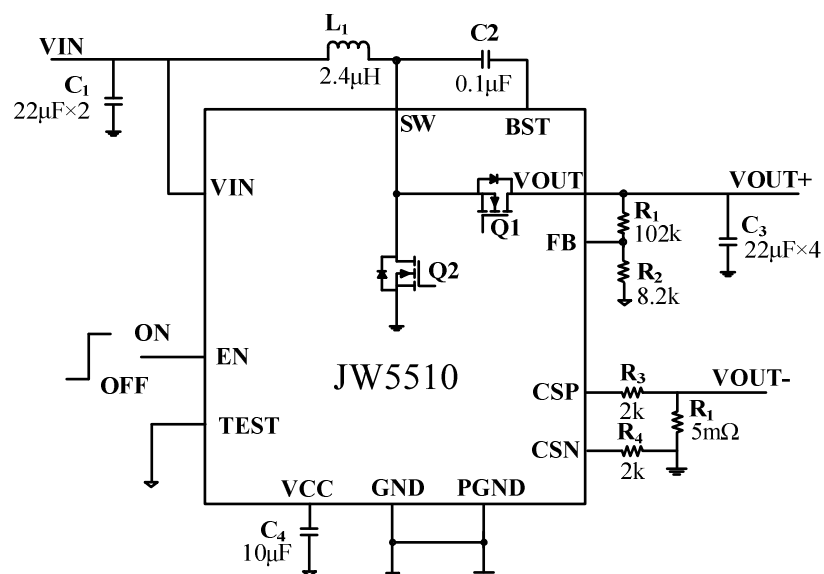
for single cell application we recommend $L1=1.2\mu H$.

Reference 2: output current limit set

V_{IN} : 6.0V ~ 8.4 V

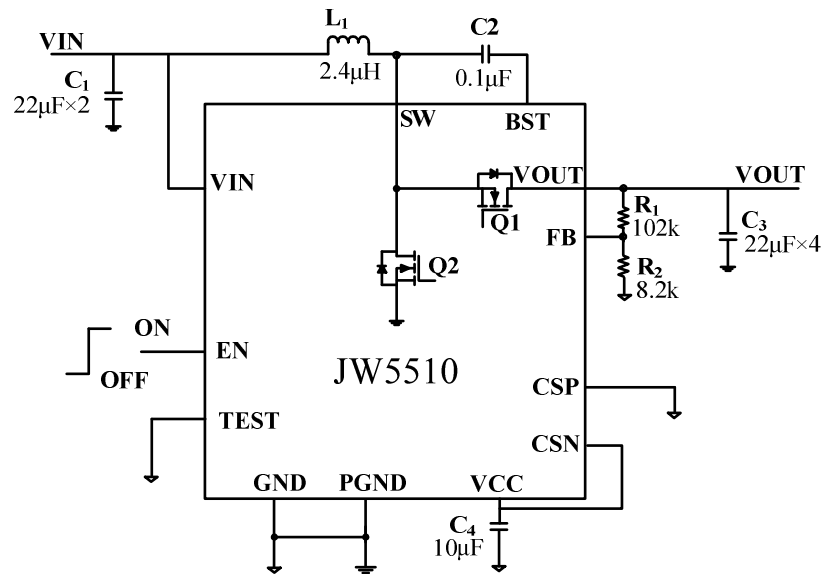
V_{OUT} : 12V

I_{O_LIM} : 4A



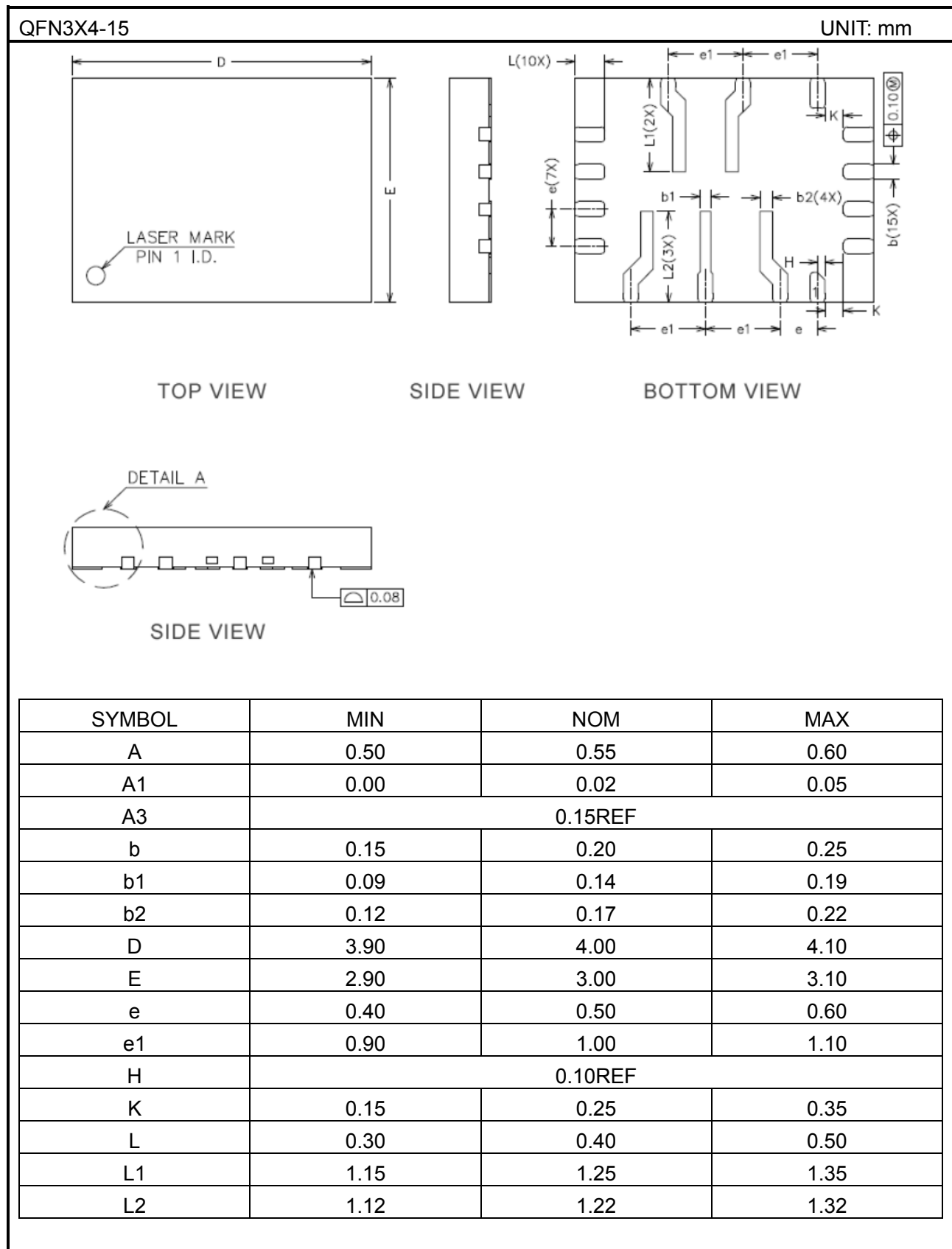
for single cell application we recommend $L1=1.2\mu H$.

Reference 3: no current limit set
 V_{IN} : 6.0V ~ 8.4 V

 V_{OUT} : 12V


for single cell application we recommend $L1=1.2\mu H$.

PACKAGE OUTLINE



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