TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (DTMOS II)

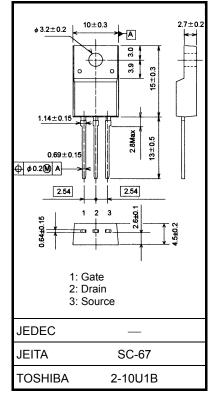
TK15A60U

Switching Regulator Applications

- Low drain-source ON-resistance: R_{DS} (ON) = 0.24 Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 8.5 \text{ S}$ (typ.)
- Low leakage current: I_{DSS} = 100 μ A (max) (V_{DS} = 600 V)
- Enhancement mode: V_{th} = 3.0 to 5.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	600	V
Gate-source voltage		V _{GSS}	±30	V
Drain current	DC (Note 1)	I _D	15	А
	Pulse (Note 1)	I _{DP}	30	A
Drain power dissipati	on (Tc = 25°C)	PD	40	W
Single pulse avalanche energy (Note 2)		E _{AS}	81	mJ
Avalanche current		I _{AR}	15	А
Repetitive avalanche energy (Note 3)		E _{AR}	4	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature range		T _{stg}	-55 to 150	°C



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in

temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

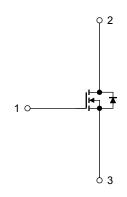
Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to case	R _{th (ch-c)}	3.125	°C/W	
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 0.63 mH, R_G = 25 Ω , I_{AR} = 15 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



Start of commercial production 2008-02

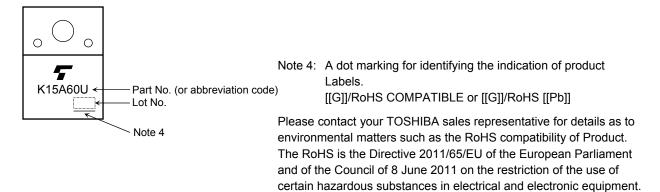
Electrical Characteristics (Ta = 25°C)

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage curre	ent	I _{GSS}	$V_{GS}=\pm 30~V,~V_{DS}=0~V$			±1	μA
Drain cut-off currer	nt	I _{DSS}	$V_{DS} = 600 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	_		100	μA
Drain-source breal	kdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	600			V
Gate threshold vol	tage	V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	3.0		5.0	V
Drain-source ON-r	esistance	R _{DS (ON)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 7.5 \text{ A}$	_	0.24	0.3	Ω
Forward transfer a	dmittance	Y _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 7.5 \text{ A}$	3.0	8.5		S
Input capacitance		C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	950		pF
Reverse transfer capacitance		C _{rss}		_	47		
Output capacitance		C _{oss}		_	2300		
Switching time	Rise time	tr	V_{GS} $0 V$ V_{GS} $0 V$ C T		37		
	Turn-ON time	t _{on}			80		ns
	Fall time	t _f			8		
	Turn-OFF time	t _{off}	$V_{DD} \approx 300 \text{ V}$ Duty \leq 1%, $t_W =$ 10 μs	_	105	_	
Total gate charge		Qg			17		
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 15 \text{ A}$		10		nC
Gate-drain charge		Q _{gd}			7		

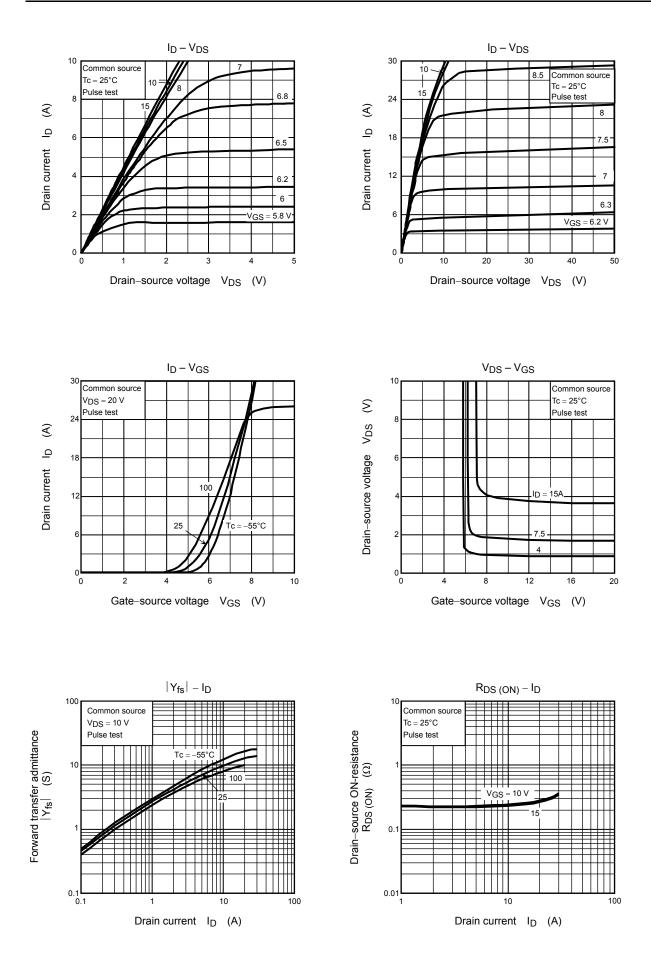
Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	15	А
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	30	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 15 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	$I_{DR} = 15 \text{ A}, V_{GS} = 0 \text{ V},$	_	530	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 100 A/μs	_	9.0	_	μC

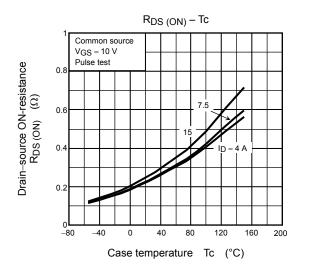
Marking

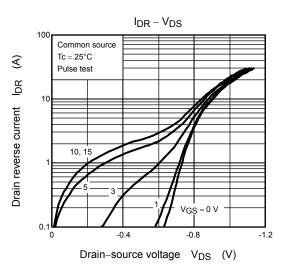


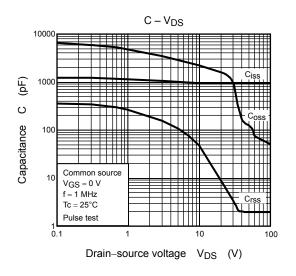
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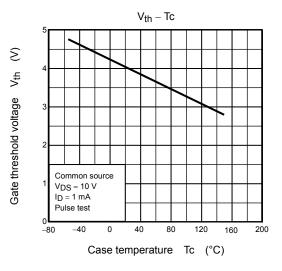


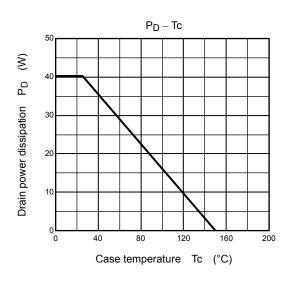
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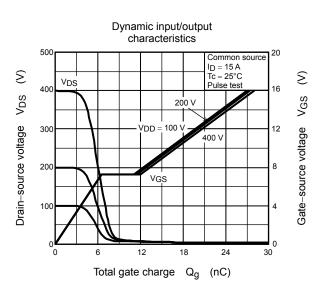


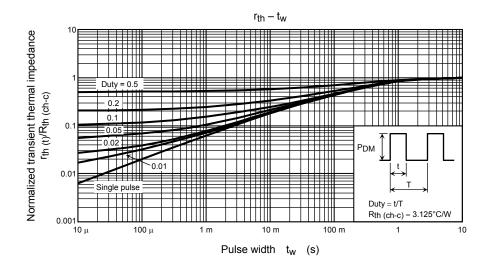


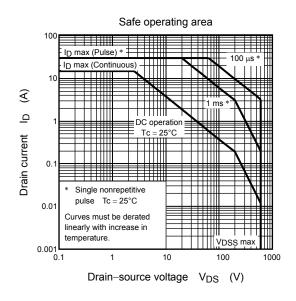


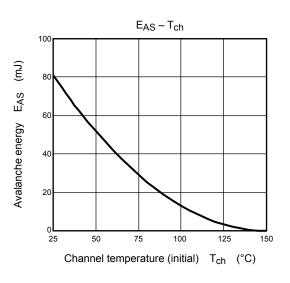


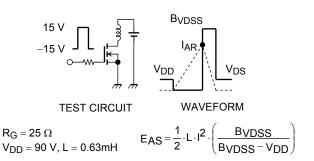












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