

UTC UNISONIC TECHNOLOGIES CO., LTD

K1875 **Preliminary JFET**

FIELD EFFECT TRANSISTOR SILICON N CHANNEL JUNCTION TYPE

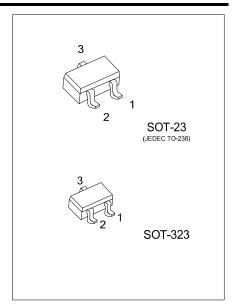
DESCRIPTION

The UTC K1875 is an N-channel JFET, it uses UTC's advanced technology to provide customers low input capacitance and high forward transfer admittance.

The UTC K1875 is suitable for high frequency amplifier and audio frequency amplifier applications, etc.

FEATURES

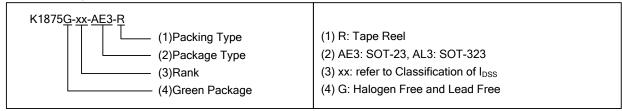
- * High forward transfer admittance
- * Low input capacitance



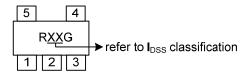
ORDERING INFORMATION

Oudovina Numbau	Dealtons	Pin Assignment			Dooking	
Ordering Number	Package	1	2	3	Packing	
K1875G-xx-AE3-R	SOT-23	D	S	G	Tape Reel	
K1875G-xx-AQ3-R	SOT-723	D	S	G	Tape Reel	

Note: Pin Assignment: D: Drain S: Source G: Gate



MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Gate-Drain Voltage	V_{GDS}	-20	V
Gate-Current	I_{G}	10	mA
Drain Power Dissipation	P _D	100	mW
Junction Temperature	TJ	125	°C
Storage Temperature Range	T _{STG}	-55~125	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Gate Leakage Current	I_{GSS}	V _{GS} =-15V, V _{DS} =0V			-1.0	nA
Gate-Drain Breakdown Voltage	$V_{(BR)GDS}$	V_{DS} =0 V , I_{G} =-100 μ A	-20			V
Drain Current	I _{DSS}	V _{DS} =5V, V _{GS} =0V	6		32	mA
Gate-Source Cut-Off Voltage	V _{GS} (OFF)	$V_{DS}=5V$, $I_{D}=1\mu A$			-2.5	V
Forward Transfer Admittance	$ Y_{fs} $	V _{DS} =5V, V _{GS} =0V, f=1kHz	15	25		mS
Input Capacitance	C _{iss}	V _{DS} =5V, V _{GS} =0V, f=1MHz		7.5	10	pF
Reverse Transfer Capacitance	C_{rss}	V _{DG} =5V, I _D =0V, f=1MHz		2	3	pF

■ CLASSIFICATION OF I_{DSS}

RANK	GR	BL	V
RANGE	6~12	10~20	16~32

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