

### Field Effect Transistor

### Silicon N Channel MOS Type (n-MOS II.5)

### High Speed, High Current Switching Applications

#### Features

- Low Drain-Source ON Resistance
  - $R_{DS(ON)} = 1.8\Omega$  (Typ.)
- High Forward Transfer Admittance
  - $|Y_{fs}| = 3.0S$  (Typ.)
- Low Leakage Current
  - $I_{DSS} = -300\mu A$  (Max.) @  $V_{DS} = 640V$
- Enhancement-Mode
  - $V_{th} = 1.5 \sim 3.5V$  @  $V_{DS} = -10V, I_D = 1mA$

#### Absolute Maximum Ratings (Ta = 25°C)

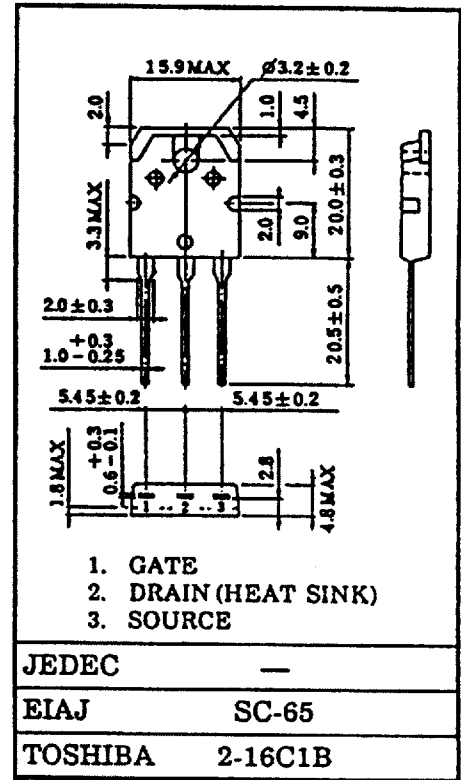
CHARACTERISTIC	SYMBOL	RATING	UNIT
Drain-Source Voltage	$V_{DSS}$	800	V
Drain-Gate Voltage ( $R_{DS} = 20k\Omega$ )	$V_{DGR}$	800	V
Gate-Source Voltage	$V_{GSS}$	$\pm 30$	V
Drain Current	DC	$I_D$	5 A
	Pulse	$I_{DP}$	15
Drain Power Dissipation (Tc = 25°C)	$P_D$	125	W
Channel Temperature	$T_{ch}$	150	°C
Storage Temperature Range	$T_{stg}$	-55 ~ 150	°C

#### Thermal Characteristics

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Case	$R_{\theta(ch-c)}$	1.0	°C/W
Thermal Resistance, Channel to Ambient	$R_{\theta(ch-a)}$	50	°C/W

This transistor is an electrostatic sensitive device. Please handle with caution.

Unit in mm



Weight : 4.6g

## Electrical Characteristics (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Gate Leakage Current	$I_{GSS}$	$V_{GS} = \pm 30V, V_{DS} = 0V$	-	-	$\pm 100$	nA	
Drain Cut-off Current	$I_{DSS}$	$V_{DS} = 640V, V_{GS} = 0V$	-	-	300	$\mu A$	
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = 10mA, V_{GS} = 0V$	800	-	-	V	
Gate Threshold Voltage	$V_{th}$	$V_{DS} = 10V, I_D = -1mA$	1.5	-	3.5	V	
Drain-Source ON Resistance	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 3A$	-	1.8	2.2	$\Omega$	
Forward Transfer Admittance	$ Y_{fs} $	$V_{DS} = 20V, I_D = 3A$	1.0	3.0	-	S	
Input Capacitance	$C_{iss}$	$V_{DS} = 25V, V_{GS} = 0V,$ $f = 1MHz$	-	610	870	pF	
Reverse Transfer Capacitance	$C_{rss}$		-	60	100		
Output Capacitance	$C_{oss}$		-	110	165		
Switching Time	Rise Time	$t_r$		-	30	60	ns
	Turn-on Time	$t_{on}$		-	70	140	
	Fall Time	$t_f$		-	35	70	
	Turn-off Time	$t_{off}$		-	165	330	
Total Gate Charge (Gate-Source Plus Gate-Drain)	$Q_g$	$V_{DD} = 400V, V_{GS} = 10V,$ $I_D = -5A$	-	47	94	nC	
Gate-Source Charge	$Q_{gs}$		-	19	-		
Gate-Drain ("Miller") Charge	$Q_d$		-	28	-		

## Source-Drain Diode Ratings and Characteristics (Ta = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	$I_{DR}$	-	-	-	5	A
Pulse Drain Reverse Current	$I_{DRP}$	-	-	-	15	A
Diode Forward Voltage	$V_{DSF}$	$I_{DR} = 5A, V_{GS} = 0V$	-	-	-1.9	V
Reverse Recovery Time	$t_r$	$I_{DR} = 5A, V_{GS} = 0V$	-	1450	-	ns
Reverse Recovered Charge	$Q_r$	$dI_{DR}/dt = 100A/\mu s$	-	20	-	$\mu C$

