

To all our customers

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Renesas Technology Corp.
Customer Support Dept.
April 1, 2003

Cautions

Keep safety first in your circuit designs!

1. Renesas Technology Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.

Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

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2SK213, 2SK214, 2SK215, 2SK216

Silicon N-Channel MOS FET

www.DataSheet4U.com

RENESAS

ADE-208-1241 (Z)
1st. Edition
Mar. 2001

Application

High frequency and low frequency power amplifier, high speed switching.

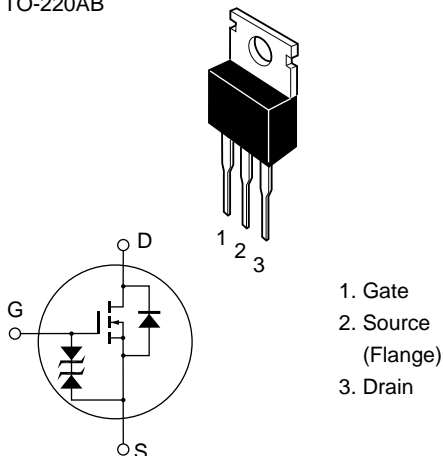
Complementary pair with 2SJ76, J77, J78, J79

Features

- Suitable for direct mounting
- High forward transfer admittance
- Excellent frequency response
- Enhancement-mode

Outline

TO-220AB



2SK213, 2SK214, 2SK215, 2SK216

Absolute Maximum Ratings (Ta = 25°C)

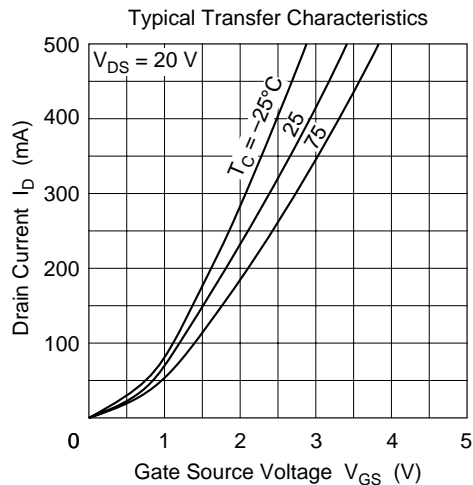
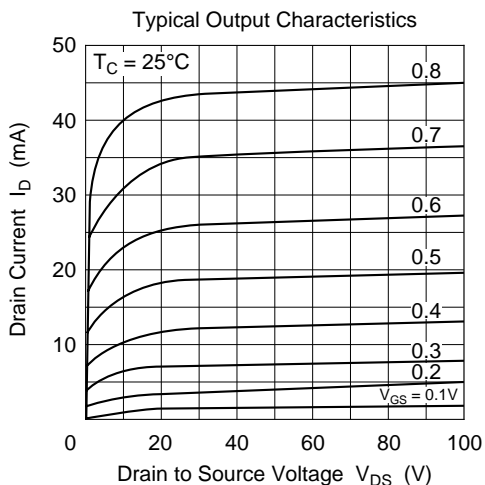
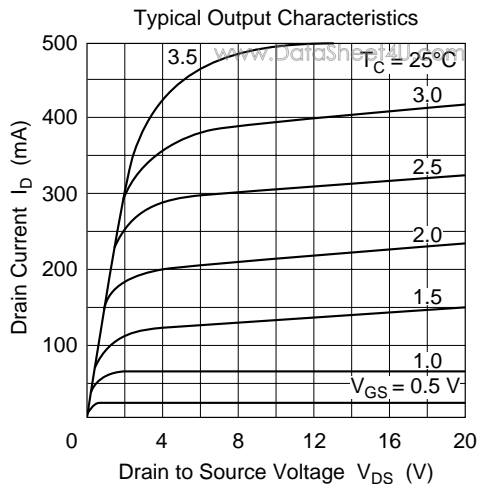
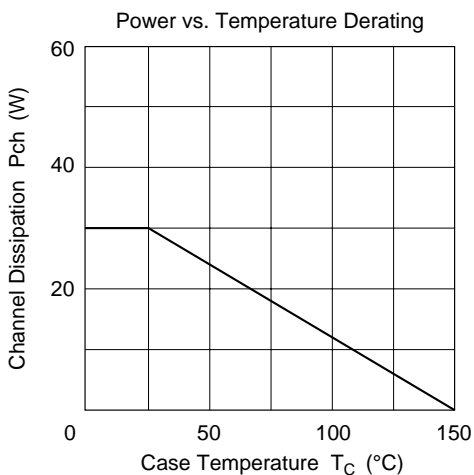
Item		Symbol	Ratings	Unit
Drain to source voltage	2SK213	V_{DSX}	140	V
	2SK214		160	
	2SK215		180	
	2SK216		200	
Gate to source voltage		V_{GSS}	±15	V
Drain current		I_D	500	mA
Body to drain diode reverse drain current		I_{DR}	500	mA
Channel dissipation		Pch	1.75	W
		Pch*1	30	W
Channel temperature		Tch	150	°C
Storage temperature		Tstg	-45 to +150	°C

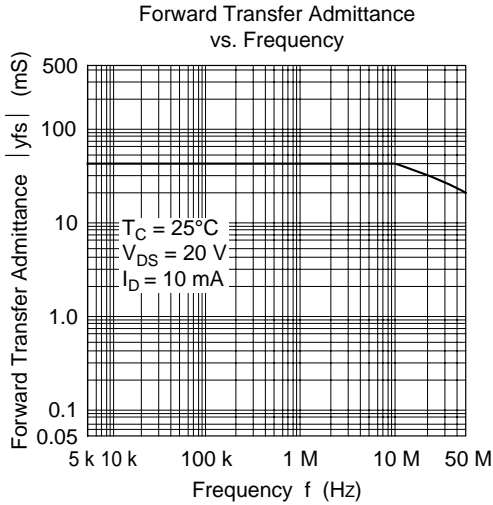
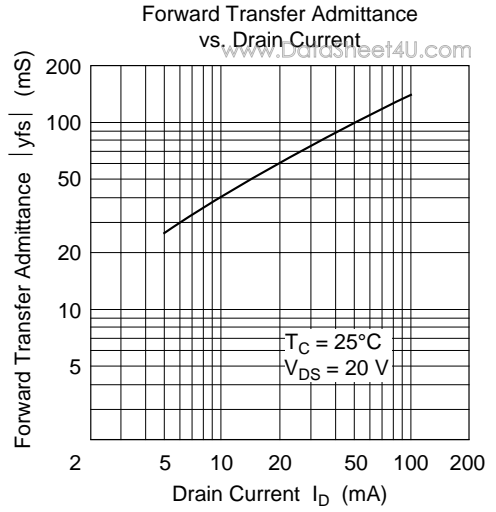
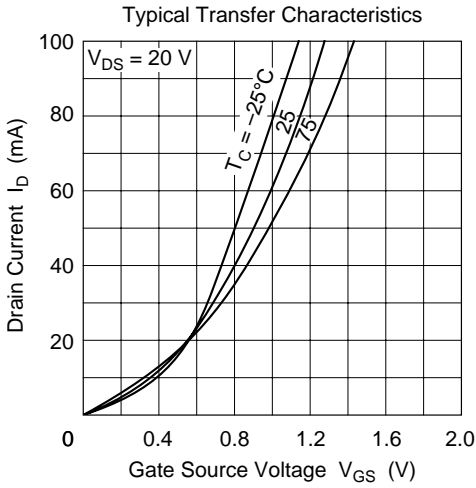
Note: 1. Value at $T_C = 25^\circ\text{C}$

Electrical Characteristics (Ta = 25°C)

Item		Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	2SK213	$V_{(BR)DSX}$	140	—	—	V	$I_D = 1 \text{ mA}, V_{GS} = -2 \text{ V}$
	2SK214		160	—	—	V	
	2SK215		180	—	—	V	
	2SK216		200	—	—	V	
Gate to source breakdown voltage		$V_{(BR)GSS}$	±15	—	—	V	$I_G = \pm 10 \mu\text{A}, V_{DS} = 0$
Gate to source voltage		$V_{GS(on)}$	0.2	—	1.5	V	$I_D = 10 \text{ mA}, V_{DS} = 10 \text{ V}^{*1}$
Drain to source saturation voltage		$V_{DS(sat)}$	—	—	2.0	V	$I_D = 10 \text{ mA}, V_{GD} = 0^{*1}$
Forward transfer admittance		$ y_{fs} $	20	40	—	mS	$I_D = 10 \text{ mA}, V_{DS} = 20 \text{ V}^{*1}$
Input capacitance		Ciss	—	90	—	pF	$I_D = 10 \text{ mA}, V_{DS} = 10 \text{ V}$,
Reverse transfer capacitance		Crss	—	2.2	—	pF	$f = 1 \text{ MHz}$

Note: 1. Pulse test



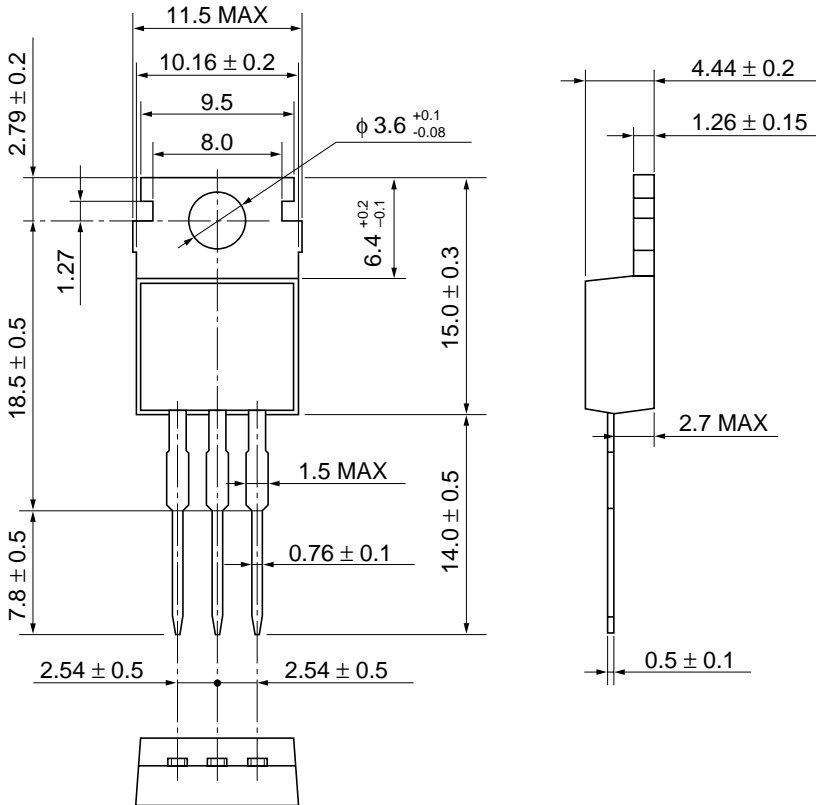


Package Dimensions

www.DataSheet4U.com

As of January, 2001

Unit: mm



Hitachi Code	TO-220AB
JEDEC	Conforms
EIAJ	Conforms
Mass (reference value)	1.8 g

Cautions

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HITACHI

Hitachi, Ltd.

Semiconductor & Integrated Circuits.

Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

URL NorthAmerica : <http://semiconductor.hitachi.com/>
 Europe : <http://www.hitachi-eu.com/hel/ecg>
 Asia : <http://sicapac.hitachi-asia.com>
 Japan : <http://www.hitachi.co.jp/Sicd/indx.htm>

For further information write to:

Hitachi Semiconductor
(America) Inc.
179 East Tasman Drive,
San Jose, CA 95134
Tel: <1> (408) 433-1990
Fax: <1> (408) 433-0223

Hitachi Europe GmbH
Electronic Components Group
Dornacher Straße 3
D-85622 Feldkirchen, Munich
Germany
Tel: <49> (89) 9 9180-0
Fax: <49> (89) 9 29 30 00

Hitachi Europe Ltd.
Electronic Components Group.
Whitebrook Park
Lower Cookham Road
Maidenhead
Berkshire SL6 8YA, United Kingdom
Tel: <44> (1628) 585000
Fax: <44> (1628) 585160

Hitachi Asia Ltd.
Hitachi Tower
16 Collyer Quay #20-00,
Singapore 049318
Tel : <65>-538-6533/538-8577
Fax : <65>-538-6933/538-3877
URL : <http://www.hitachi.com.sg>

Hitachi Asia Ltd.
(Taipei Branch Office)
4/F, No. 167, Tun Hwa North Road,
Hung-Kuo Building,
Taipei (105), Taiwan
Tel : <886>-(2)-2718-3666
Fax : <886>-(2)-2718-8180
Telex : 23222 HAS-TP
URL : <http://www.hitachi.com.tw>

Hitachi Asia (Hong Kong) Ltd.
Group III (Electronic Components)
7/F., North Tower,
World Finance Centre,
Harbour City, Canton Road
Tsim Sha Tsui, Kowloon,
Hong Kong
Tel : <852>-(2)-735-9218
Fax : <852>-(2)-730-0281
URL : <http://www.hitachi.com.hk>

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