TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSIII)

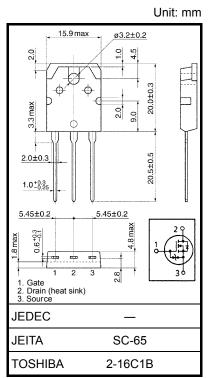
# 2SK2719

# Chopper Regulator, DC-DC Converter and Motor Drive Applications

- Low drain-source ON resistance:  $R_{DS}(ON) = 3.7 \Omega$  (typ.)
- High forward transfer admittance: |Y<sub>fs</sub>| = 2.6 S (typ.)
- Low leakage current:  $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 720 \text{ V)}$
- Enhancement mode:  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics S		ymbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	900	V	
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ ) V		DGR	900	V	
Gate-source voltage		$V_{GSS}$	±30	V	
Drain current	DC (Note 1)	I <sub>D</sub>	3	А	
	Pulse (Note 1)	I <sub>DP</sub>	9		
Drain power dissipation (Tc = 25°C)		PD	125	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	295	mJ	
Avalanche current		I <sub>AR</sub>	3	Α	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	12.5	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics S	ymbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	1.0	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub> 50	.0	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 25$  V,  $T_{ch} = 25^{\circ}C$  (initial), L = 58  $\mu H$ ,  $R_G = 25$   $\Omega$ ,  $I_{AR} = 45$  A

Note 3: Repetitive rating: pulse width limited by maximum junction temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.



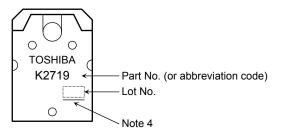
## **Electrical Characteristics (Ta = 25°C)**

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage curr	ent	I <sub>GSS</sub>	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Gate-source brea	kdown voltage	V (BR) GSS	$I_G=\pm 10~\mu A,~V_{DS}=0~V$	±30	_	_	V
Drain cut-off curre	ent	I <sub>DSS</sub>	V <sub>DS</sub> = 720 V, V <sub>GS</sub> = 0 V	_	_	100	μΑ
Drain-source brea	kdown voltage	V (BR) DSS	$I_D = 10$ mA, $V_{GS} = 0$ V	900	_	_	V
Gate threshold voltage		$V_{th}$	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source ON	resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 1.5 A	_	3.7	4.3	Ω
Forward transfer a	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 1.5 A	0.65	2.6	_	S
Input capacitance	capacitance C <sub>iss</sub>		_	750	_	pF	
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	10	_	pF
Output capacitance		Coss		<b>— 70</b>		— pF	
Switching time	Rise time	t <sub>r</sub>	10 V		15		ns
	Turn-on time	t <sub>on</sub>		_	55	_	
	Fall time	t <sub>f</sub>		_	30	_	
	Turn-off time	t <sub>off</sub>	Duty ≤ 1%, t <sub>W</sub> = 10 μs	— 11	0		
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \simeq 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3 \text{ A}$	_	25	_	nC
Gate-source charge		Q <sub>gs</sub>		_	13	_	nC
Gate-drain ("miller") charge		Q <sub>gd</sub>		_	12	_	nC

## Source-Drain Diode Ratings and Characteristics (Ta = 25°C)

Characteristics S	ymbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	3	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	—9		А
Diode forward voltage	$V_{DSF}$	$I_{DR} = 3 A$ , $V_{GS} = 0 V$	_	_	-1.9	٧
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 3 A$ , $V_{GS} = 0 V$	_	1100	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dI <sub>DR</sub> /dt = 100 A/μs	— 7.5	•	_	μС

### Marking

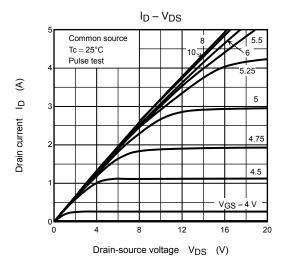


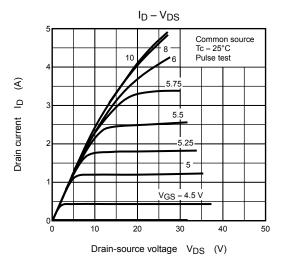
Note 4: A line under a Lot No. identifies the indication of product Labels.

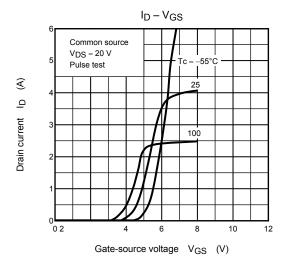
Not underlined: [[Pb]]/INCLUDES > MCV

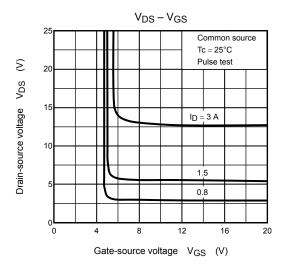
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

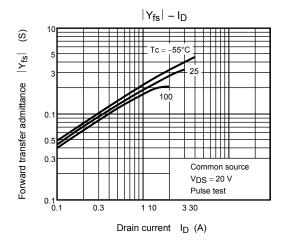
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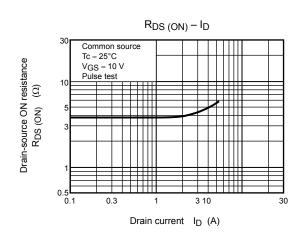


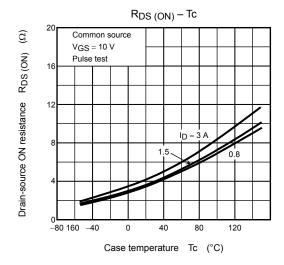


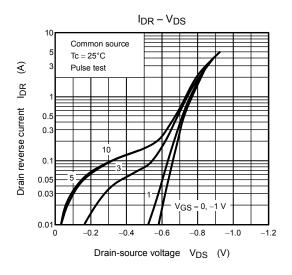


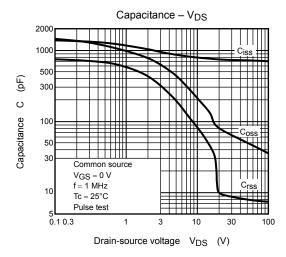


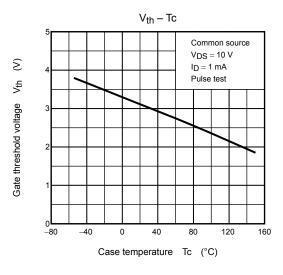


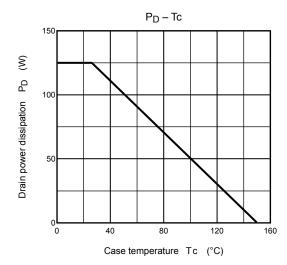


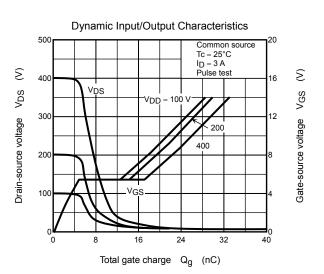


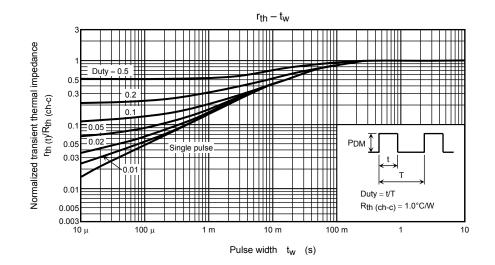


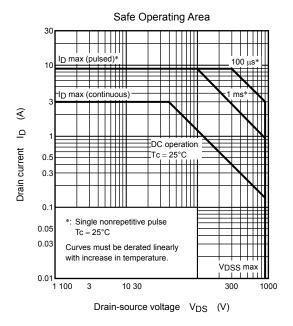


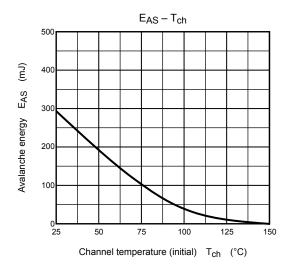


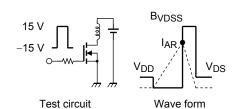












$$R_G = 25~\Omega$$
 
$$V_{DD} = 90~V,~L = 60~mH$$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$

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