Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSVI)

2SK3128

Chopper Regulator, DC-DC Converter and Motor Drive Applications

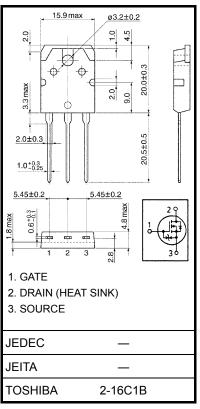
• Low drain-source ON resistance : $R_{DS(ON)} = 9.5 \text{ m}\Omega \text{ (typ.)}$

High forward transfer admittance : |Yfs| = 40 S (typ.)
 Low leakage current : IDSS = 100 µA (max) (VDS = 30 V)

• Enhancement mode : $V_{th} = 1.5 \text{ to } 3.0 \text{ V } (V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	30	V	
Drain-gate voltage (Ro	_{SS} = 20 kΩ)	V_{DGR}	30	V	
Gate-source voltage		V_{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	60	Α	
	Pulse (Note 1)	I _{DP}	180	Α	
Drain power dissipation	n (Tc = 25°C)	P_{D}	150	W	
Single pulse avalanche	e energy (Note 2)	E _{AS}	411	mJ	
Avalanche current		I _{AR}	60	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	1.5	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature ra	ange	T _{stg}	-55 to 150	°C	



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal reverse, channel to case	R _{th (ch-c)}	1.0	°C/W
Thermal reverse, channel to ambient	R _{th (ch-a)}	50	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 25 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 82 μH , $R_{G} = 25 \Omega$, $I_{AR} = 60 \text{ A}$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

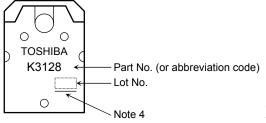
Electrical Characteristics (Ta = 25°C)

Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μA
Drain cut-off cui	rrent	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	30	_		V
Gate threshold v	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.5	_	3.0	V
Drain-source Ol	N resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 30 A	_	9.5	12	mΩ
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 30 A	20	40		S
Input capacitano	е	C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		2300		pF
Reverse transfer	r capacitance	C _{rss}			380	_	
Output capacitar	utput capacitance C _{oss}		_	1100	-		
Switching time	Rise time	t _r	$V_{GS} = 0$ $V_{GS} = 0$ $V_{OUT} = 0$ $V_{OUT} = 0$ $V_{OUT} = 0$ $V_{OUT} = 0$	_	12	_	- ns
	Turn-on time	t _{on}		_	25	_	
	Fall time	t _f		_	75		
	Turn-off time	t _{off}	$V_{DD} \stackrel{.}{=} 30V$ Duty $\leq 1\%$, $t_w = 10 \mu s$	_	200	_	
Total gate charge (Gate-source plus gate-drain)		Qg	V _{DD} ≈ 24 V, V _{GS} = 10 V, I _D = 60 A		66	_	
Gate-source charge		Q _{gs}			45	_	nC
Gate-drain ("miller") charge		Q _{gd}			21	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	60	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	180	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 60 A, V _{GS} = 0 V	_	_	-1.5	V
Reverse recovery time	t _{rr}	I _{DR} = 60 A, V _{GS} = 0 V, dI _{DR} / dt = 50 A / μs	ı	150	1	ns
Reverse recovery charge	Qrr	1DR = 60 A, VGS = 6 V, αIDR / αt = 50 A / μs		0.26	_	μC

Marking



Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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